Characteristics EMIF02-SPK02F2

1 Characteristics

Table 1. Absolute maximum ratings ($T_{amb} = 25$ °C)

Symbol	Parameter	Value	Unit
	ESD discharge IEC 61000-4-2		
V_{PP}	Air discharge	30	kV
	Contact discharge	30	
I _{SPK}	Maximum rms current per channel	350	mA
T _j	Junction temperature range	-30 to 125	°C
T _{stg}	Storage temperature range	-55 to + 150	°C

Figure 3. Electrical characteristics - definitions

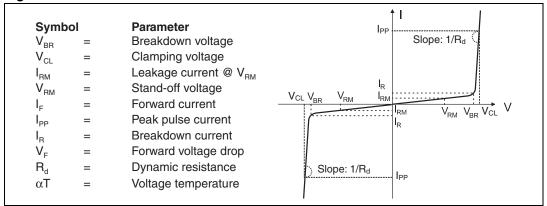


Table 2. Electrical characteristics - values ($T_{amb} = 25 \, ^{\circ}C$)

Symbol	Test conditions	Min	Тур	Max	Unit
V_{BR}	I _R = 1 mA	6			V
I _{RM}	V _{RM} = 3 V			400	nA
R _{I/O}			0.35	0.8	Ω
C _{LINE}	V _R = 0 V DC, 1 MHz	185	250	315	pF
F _c	Cut-off frequency: $Z_{SOURCE} = Z_{LOAD} = 50 \Omega$		20		MHz

EMIF02-SPK02F2 Characteristics

Figure 4. Attenuation measurements versus frequency

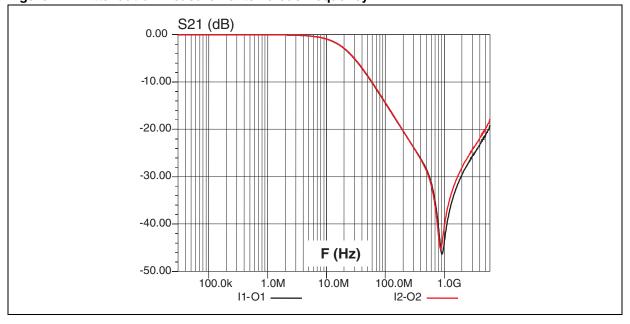
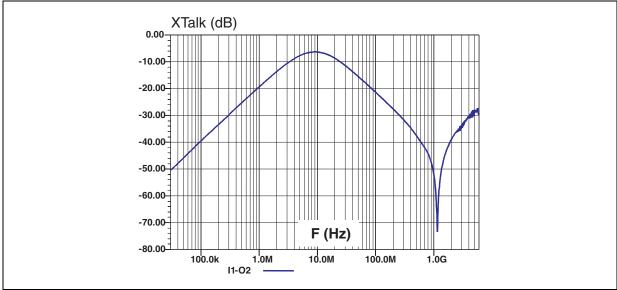


Figure 5. Crosstalk measurements versus frequency



Characteristics EMIF02-SPK02F2

Figure 6. ESD test conditions

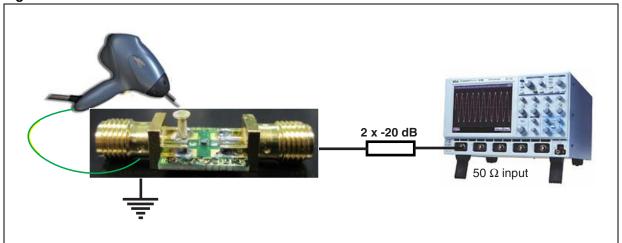
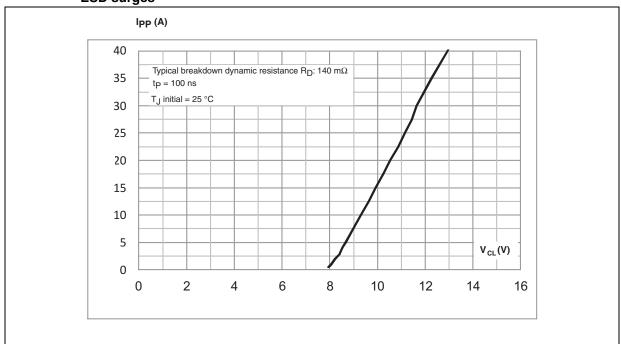


Figure 7. Clamping voltage V_{CL} versus peak pulse current I_{PP} for short pulse duration such as ESD surges



Note: For further information on the dynamic characteristic see the STMicroelectronics' application note AN4022, "TVS short pulse R_d measurement and correlation with TVS clamping voltage during ESD".

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EMIF02-SPK02F2 Characteristics



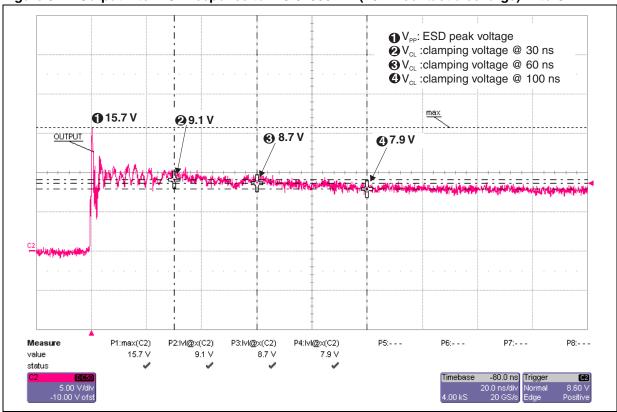
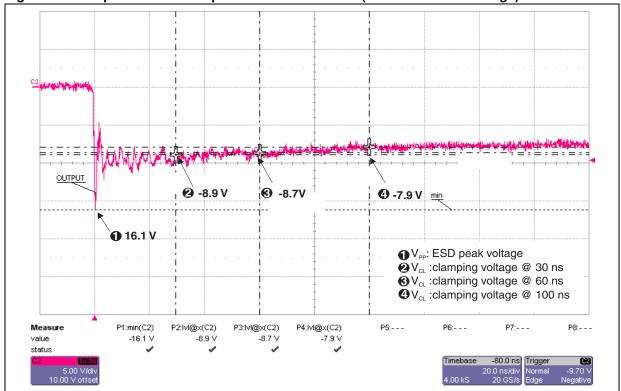


Figure 9. Output filter ESD response to IEC 61000-4-2 (-8 kV contact discharge) I1 to O1



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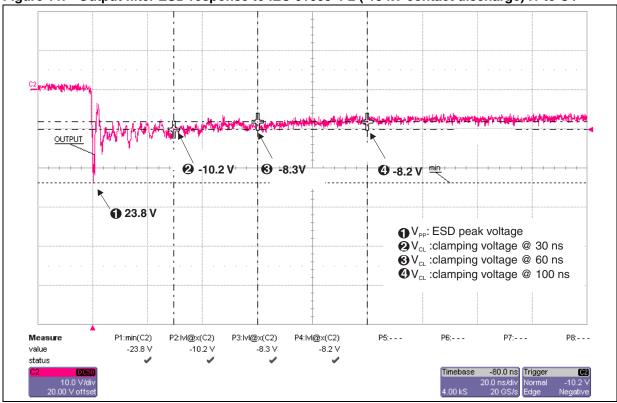
Characteristics EMIF02-SPK02F2

● V_{PP}: ESD peak voltage **②** V_{CL} :clamping voltage @ 30 ns ${f O}$ $V_{\scriptscriptstyle CL}$:clamping voltage @ 60 ns **4** V_{CL} :clamping voltage @ 100 ns 125.5 V 29.7 V € 10 V **⊘**8.3 V P2:IVI@x(C2) P3:IVI@x(C2) P4:IvI@x(C2) P5:---P7:---Measure P1:max(C2) P6:---P8:- - value 25.5 V 9.7 V 10.0 V 8.3 V status

Figure 10. Output filter ESD response to IEC 61000-4-2 (+15 kV contact discharge) I1 to O1



-80.0 ns Trigger



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EMIF02-SPK02F2 Characteristics



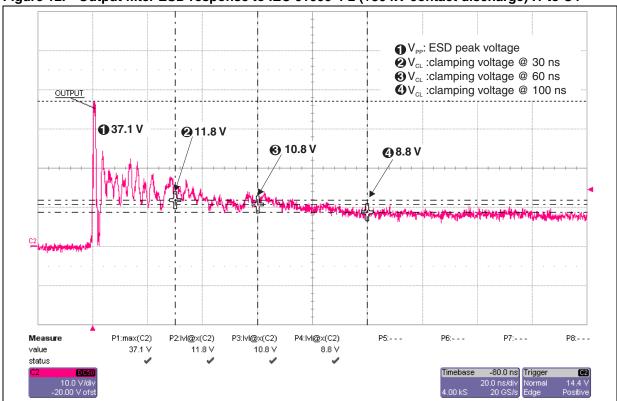
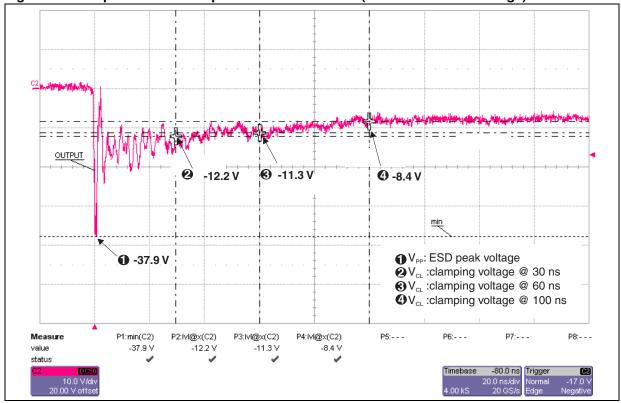


Figure 13. Output filter ESD response to IEC 61000-4-2 (-30 kV contact discharge) I1 to O1



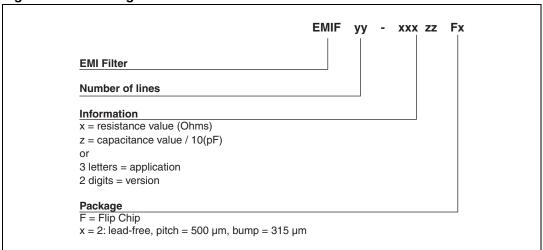
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2 Ordering information scheme

Figure 14. Ordering information scheme



Package information 3

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Figure 15. Package dimensions

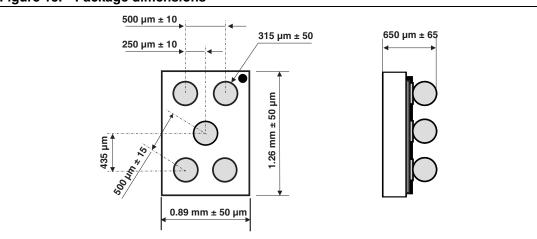


Figure 16. Footprint

Figure 17. Marking Dot, ST logo xx = marking z = manufacturing location yww = datecode (y = year ww = week) **777** 🗉 $\begin{array}{c} \text{Copper pad \ Diameter:} \\ \text{250 } \mu\text{m recommended, 300 } \mu\text{m max} \end{array}$ Solder stencil opening: 330 µm recommended XXZ y w w Solder mask opening recommendation: 340 μm min. for 315 μm copper pad diamete

Package information EMIF02-SPK02F2

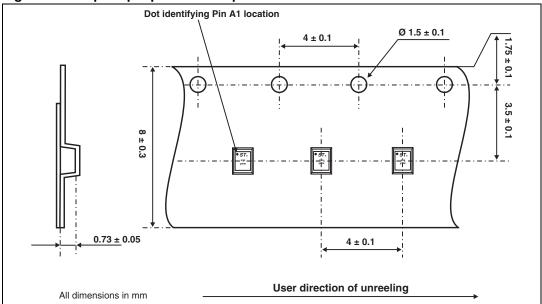


Figure 18. Flip Chip tape and reel specification

Note:

More information is available in the application notes:

AN1235: "Flip Chip: Package description and recommendations for use"

AN1751: "EMI filters: Recommendations and measurements"

4 Ordering information

Table 3. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
EMIF02-SPK02F2	JD	Flip Chip	1.8 mg	5000	Tape and reel 7"

5 Revision history

Table 4. Document revision history

Date	Revision	Changes
17-Sep-2008	1	Initial release.
12-Sep-2011	2	Updated Figure 15 and Figure 16.
3-Apr-2012	3	Updated cover page features and description.Inserted <i>Figure 6</i> to <i>Figure 13</i> .

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