Notes:



Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P_PP	50	W	8/20µs (See Figure 1)
Peak Pulse Current	I _{PP}	6.5	А	8/20µs (See Figure 1)
ESD Protection – Air Discharge	V _{ESD_AIR}	20	kV	IEC 61000-4-2 Standard
ESD Protection – Contact Discharge	V _{ESD_CONTACT}	20	kV	IEC 61000-4-2 Standard

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 5)	P_{D}	250	mW
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ heta JA}$	500	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Standoff Voltage	V_{RWM}		_	5	V	_
Channel Leakage Current (Note 6)	I _{RM}	-	_	0.5	μA	V _{RWM} = 5V
Breakdown Voltage	V_{BR}	6	_	10	V	I _R = 10mA
Clamping Voltage, Positive Transients	V	-	7.0	7	V	$I_{PP} = 1A, t_P = 8/20 \mu s$
	V _C L	1	7.4	_	V	$I_{PP} = 2A, t_P = 8/20 \mu s$
Differential Resistance	R _{DYN}	_	0.2	_	Ω	ITLP = 1A to 10A, t _P = 100ns, I/O to GND
Channel Input Capacitance	CIN	_	0.8	_	pF	$V_R = 0V$, $f = 1MHz$

5. Device mounted on FR-4 PCB pad Jayout (2oz copper) per Diodes Incorporated's recommended pad Jayout, refer to http://www.diodes.com/package-outlines.html.

6. Short duration pulse test used to minimize self-heating effect.





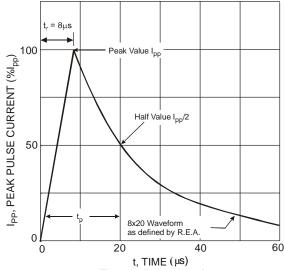
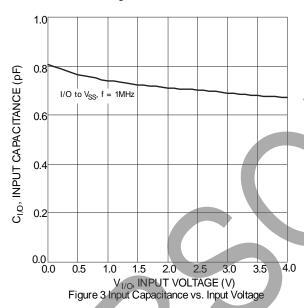
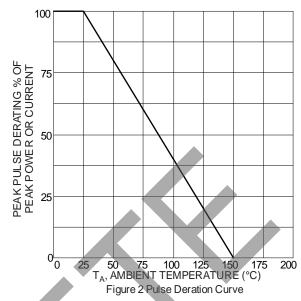
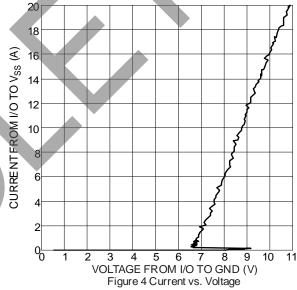


Figure 1 Pulse Waveform





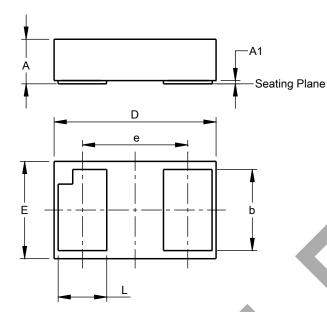




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-DSN1006-2 (Type B)

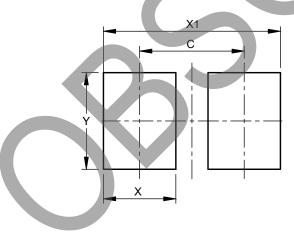


X3-DSN1006-2 (Type B)					
Dim	Min	Max	Тур		
A	0.250	0.300	0.275		
A1	0.00	0.02	0.01		
b	0.490	0.510	0.500		
D	0.975	1.025	1.00		
Е	0.575	0.625	0.600		
е			0.650		
L	0.290	0.310	0.300		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-DSN1006-2 (Type B)



Dimensions	Value (in mm)
C	0.65
Х	0.45
X1	1.10
Υ	0.60

Note 7: Device side walls are electrically active bare silicon. Avoid contact of solder or flux on the side walls during the PCB assembly process.



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 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
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