

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current, V _{GS} = 10V (Note 7)	Steady State	T _C = +25°C T _C = +70°C	I _D	60 45	A
Maximum Body Diode Forward Current (Note 7)			I _S	2	A
Pulsed Drain Current (380μs Pulse, Duty Cycle = 1%)			I _{DM}	90	A
Pulsed Drain Body Diode Forward Current (380μs Pulse, Duty Cycle = 1%)			I _{SM}	90	A
Avalanche Current (L = 0.1mH) (Note 8)			I _{AS}	24	A
Avalanche Energy (L = 0.1mH) (Note 8)			E _{AS}	29	mJ

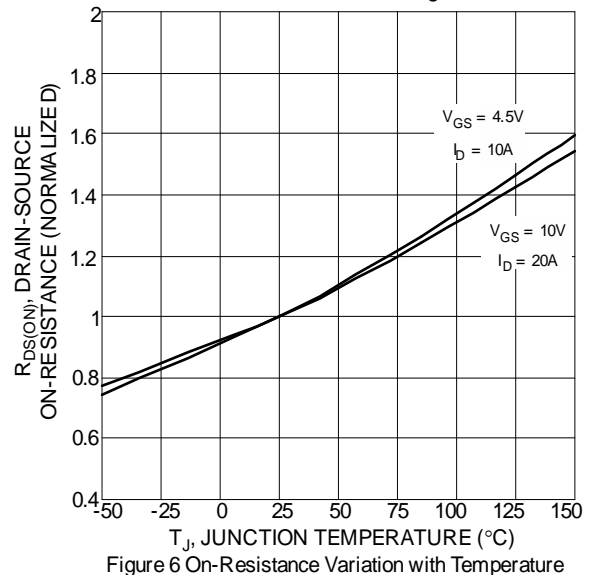
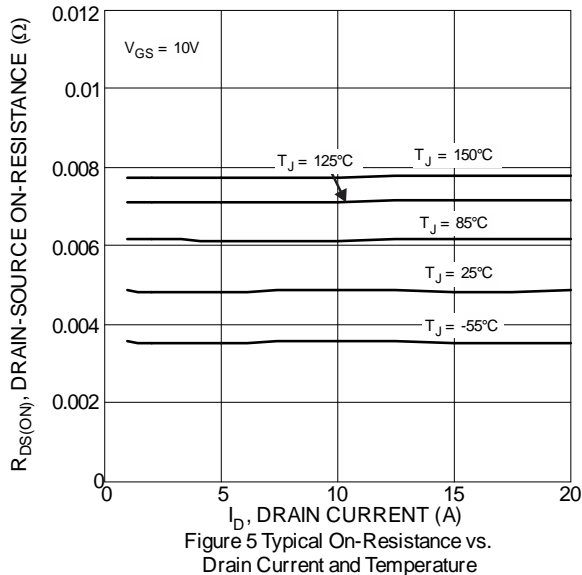
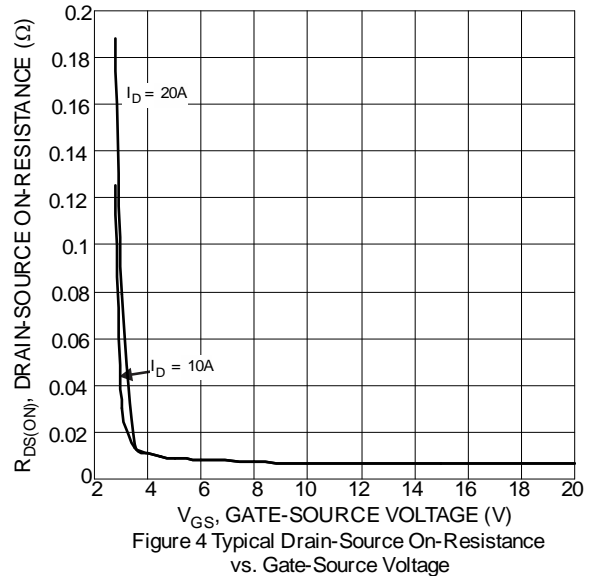
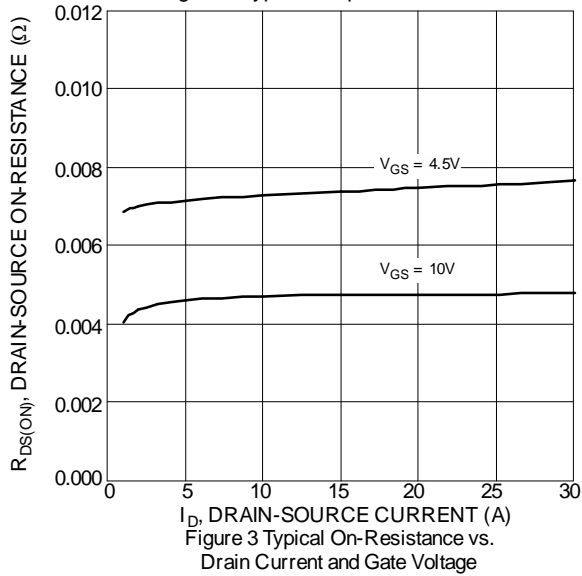
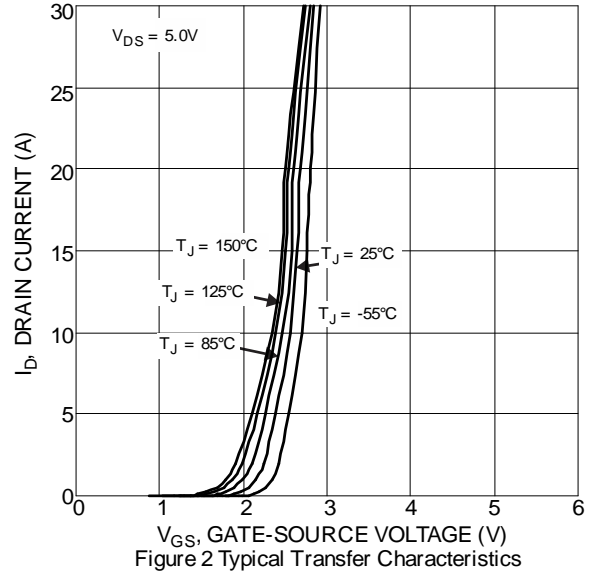
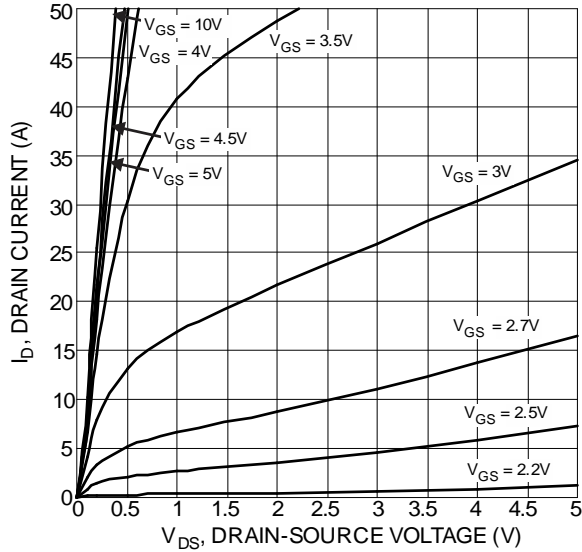
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

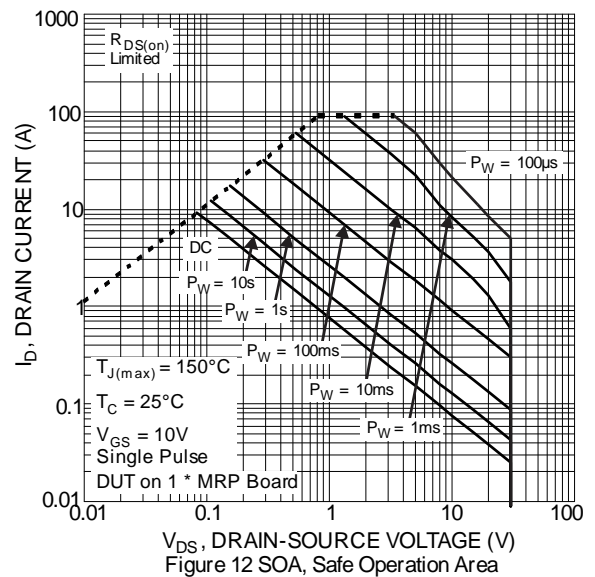
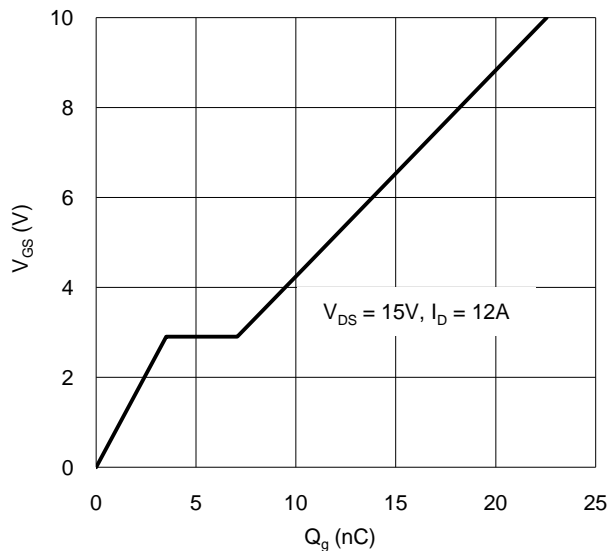
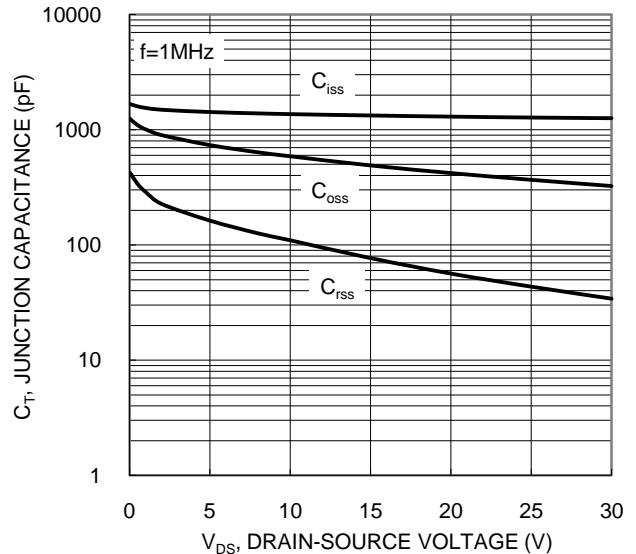
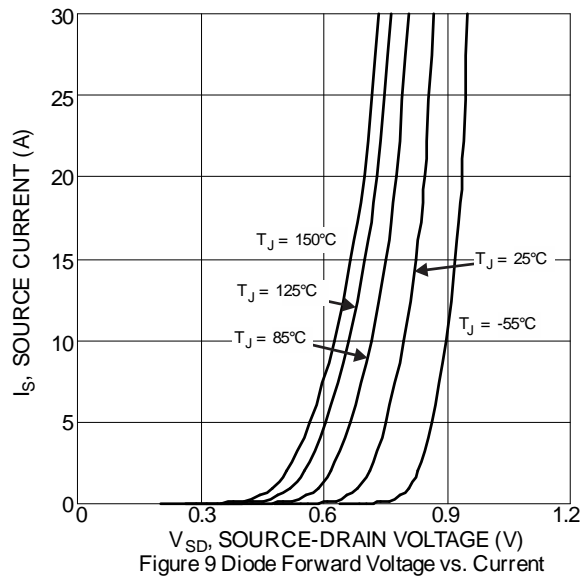
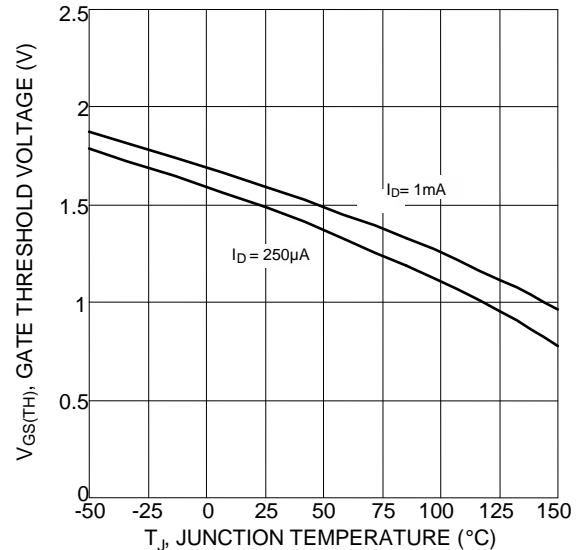
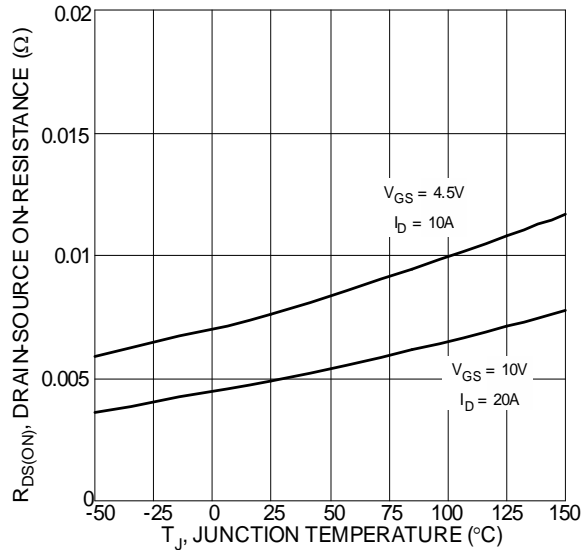
Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P _D	1.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	130	°C/W
Total Power Dissipation (Note 6)		P _D	2.0	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	63	°C/W
Thermal Resistance, Junction to Case (Note 7)		R _{θJC}	2.9	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

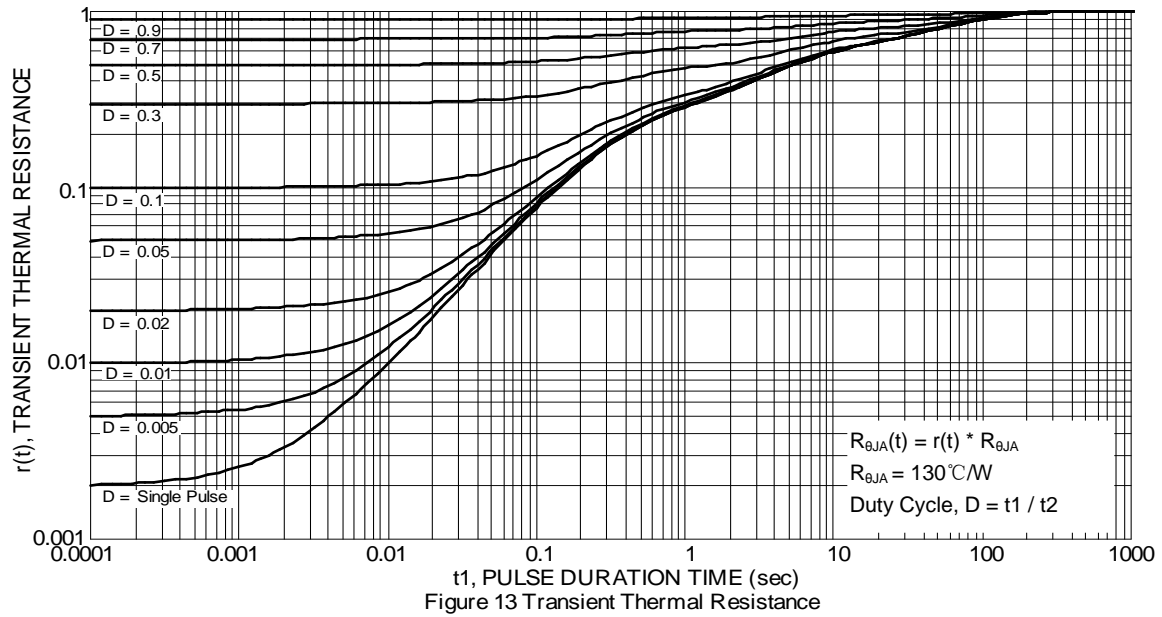
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	V _{GS} = 0V, I _D = 250µA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	—	—	1	µA	V _{DS} = 24V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = +20V, V _{DS} = 0V V _{GS} = -16V, V _{DS} = 0V
ON CHARACTERISTICS (Note 9)						
Gate Threshold Voltage	V _{GS(TH)}	1.0	—	3.0	V	V _{DS} = V _{GS} , I _D = 250µA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	5.6	7	mΩ	V _{GS} = 10V, I _D = 9.0A
			8.0	11		V _{GS} = 4.5V, I _D = 8.5A
Diode Forward Voltage	V _{SD}	—	0.70	1.2	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	C _{iss}	—	1,155	—	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	456	—		
Reverse Transfer Capacitance	C _{rss}	—	72	—		
Gate Resistance	R _g	—	1.6	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	8.4	—	nC	V _{DD} = 15V, I _D = 9A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	16.7	—		
Gate-Source Charge	Q _{gs}	—	2.2	—		
Gate-Drain Charge	Q _{gd}	—	3.5	—		
Turn-On Delay Time	t _{D(ON)}	—	3.5	—	ns	V _{DD} = 15V, V _{GS} = 10V, R _g = 3Ω, I _D = 9A
Turn-On Rise Time	t _r	—	5.5	—		
Turn-Off Delay Time	t _{D(OFF)}	—	13.5	—		
Turn-Off Fall Time	t _f	—	4.6	—		
Reverse Recovery Time	t _{RR}	—	19.3	—	ns	I _F = 1.5A, di/dt = 100A/µs
Reverse Recovery Charge	Q _{RR}	—	8.6	—	nC	

- Notes:
- Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.



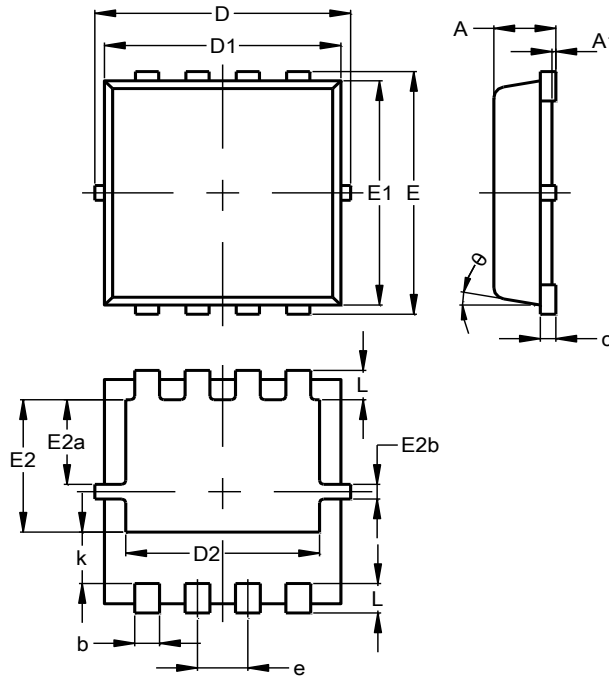




Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (Type UX)

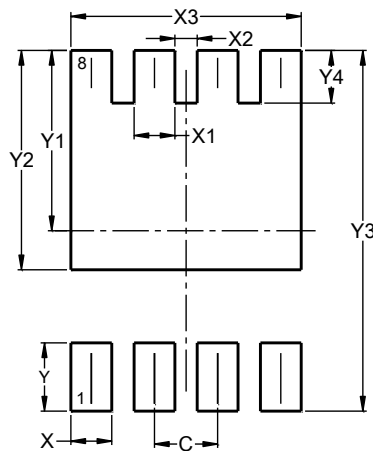


PowerDI3333-8 (Type UX)			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	--
b	0.25	0.40	0.32
c	0.10	0.25	0.15
D	3.20	3.40	3.30
D1	2.95	3.15	3.05
D2	2.30	2.70	2.50
E	3.20	3.40	3.30
E1	2.95	3.15	3.05
E2	1.60	2.00	1.80
E2a	0.95	1.35	1.15
E2b	0.10	0.30	0.20
e	0.65 BSC		
k	0.50	0.90	0.70
L	0.30	0.50	0.40
θ	0°	12°	10°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8 (Type UX)



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	0.420
X2	0.230
X3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540

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