

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage			V _{DSS}	-100	V
Gate-Source Voltage	V _{GSS}	±20	V		
Continuous Drain Current (Note 5) V _{GS} = -10V	Steady	$T_C = +25^{\circ}C$	- I _D -	-9	A
Continuous Drain Current (Note 5) VGS = -10V	State	$T_{C} = +100^{\circ}C$	טי	-5.5	
Maximum Body Diode Forward Current (Note 5)	Is	-4	А		
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	-15	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units	
Total Power Dissipation (Note 5)	T _C = +25°C	D	42	W
Total Power Dissipation (Note 5)	$T_{C} = +100^{\circ}C$	PD	17	
Thermal Resistance, Junction to Ambient (Note 5)	R _{0JA}	44	°C/W	
Thermal Resistance, Junction to Case (Note 5)		R _{0JC}		
Operating and Storage Temperature Range		T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

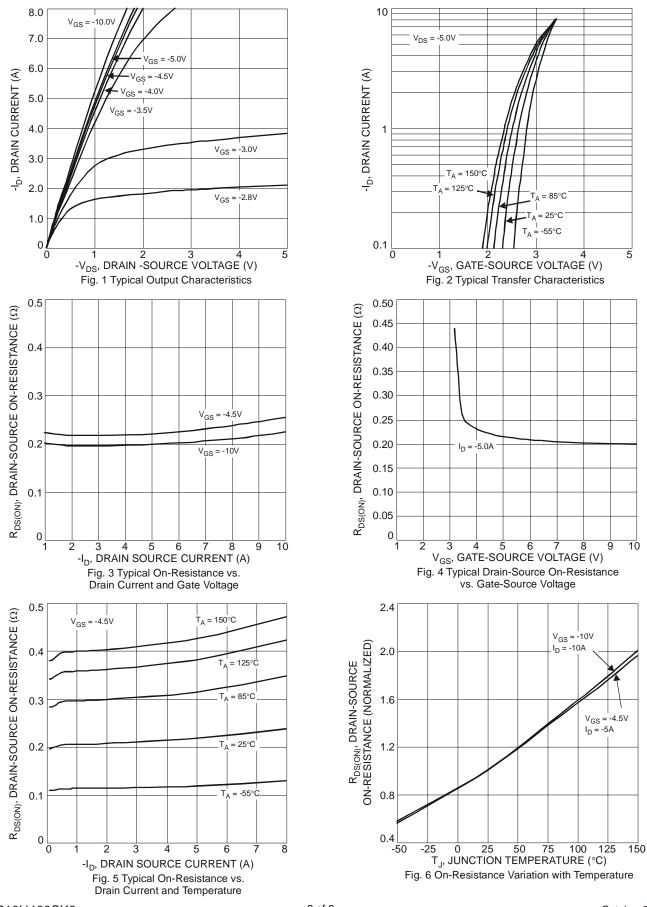
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	-100	_	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	IDSS	_	_	-1	μA	$V_{DS} = -80V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(th)}	-1	_	-3	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
Static Drain-Source On-Resistance	D		190	240	mΩ	$V_{GS} = -10V, I_D = -5A$	
Static Drain-Source On-Resistance	R _{DS (ON)}		210	300	11122	V _{GS} = -4.5V, I _D =-5A	
Diode Forward Voltage	V _{SD}	_	-0.7	-1.2	V	$V_{GS} = 0V, I_{S} = -5A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	Ciss		1239	—		V_{DS} = -25V, V_{GS} = 0V, f = 1MHz	
Output Capacitance	C _{oss}		42		pF		
Reverse Transfer Capacitance	Crss		28	_			
Gate Resistance	R _G		13	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg		8.4	_			
Total Gate Charge (V _{GS} = -10V)	Qg	_	17.5	_	nC	V _{DS} = -60V, I _D = -5A	
Gate-Source Charge	Q _{gs}	_	2.8	_	nc		
Gate-Drain Charge	Q _{gd}	_	3.2	_			
Turn-On Delay Time	t _{D(on)}	_	9.1	_			
Turn-On Rise Time	tr	_	14.9	_		V_{DD} = -50V, R_G = 9.1 Ω , I_D = -5A	
Turn-Off Delay Time	t _{D(off)}		57.4	_	ns		
Turn-Off Fall Time	t _f		34.4		1		
Body Diode Reverse Recovery Time	t _{rr}		25.2	_	ns	V _{GS} = 0V, I _S = -5A, dl/dt = 100A/µs	
Body Diode Reverse Recovery Charge	Qrr		24.5	_	nC	$V_{GS} = 0V$, $I_{S} = -5A$, $dI/dt = 100A/\mu s$	

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.

6. Short duration pulse test used to minimize self-heating effect.

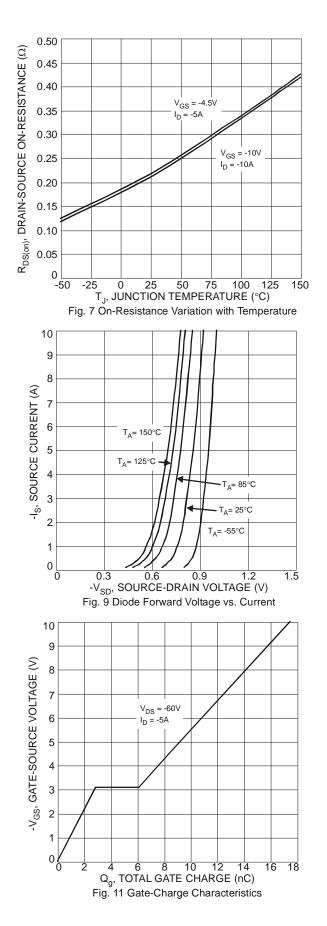
7. Guaranteed by design; not subject to production testing.

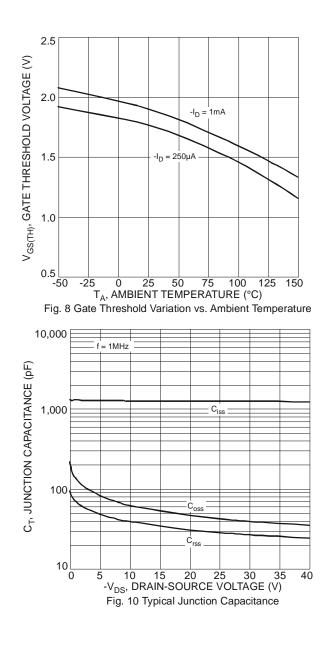




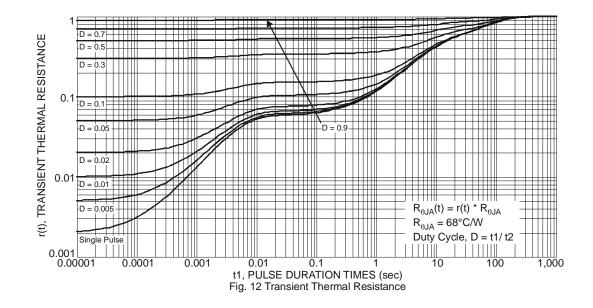
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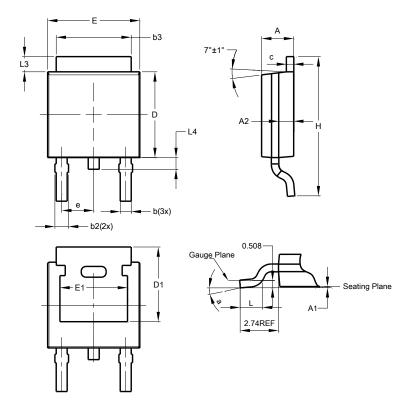






Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

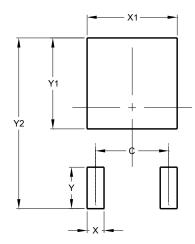


TO252 (DPAK)					
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	4.572
Х	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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