

Maximum Ratings (@ $T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	-12	V
Gate-Source Voltage			V_{GSS}	-6	V
Continuous Drain Current (Note 5) V _{GS} = -4.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-3.3 -2.7	А
Continuous Drain Current (Note 5) V _{GS} = -2.5V	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I _D	-3.0 -2.4	А
Pulsed Drain Current (Note 6)			I _{DM}	20	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 7)	P _D	0.82	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 7)	$R_{\theta JA}$	150	°C/W
Thermal Resistance, Junction to Case @T _C = +25°C (Note 7)	R ₀ JC	42.66	°C/W
Power Dissipation (Note 5)	P _D	1.59	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	$R_{\theta JA}$	80.29	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

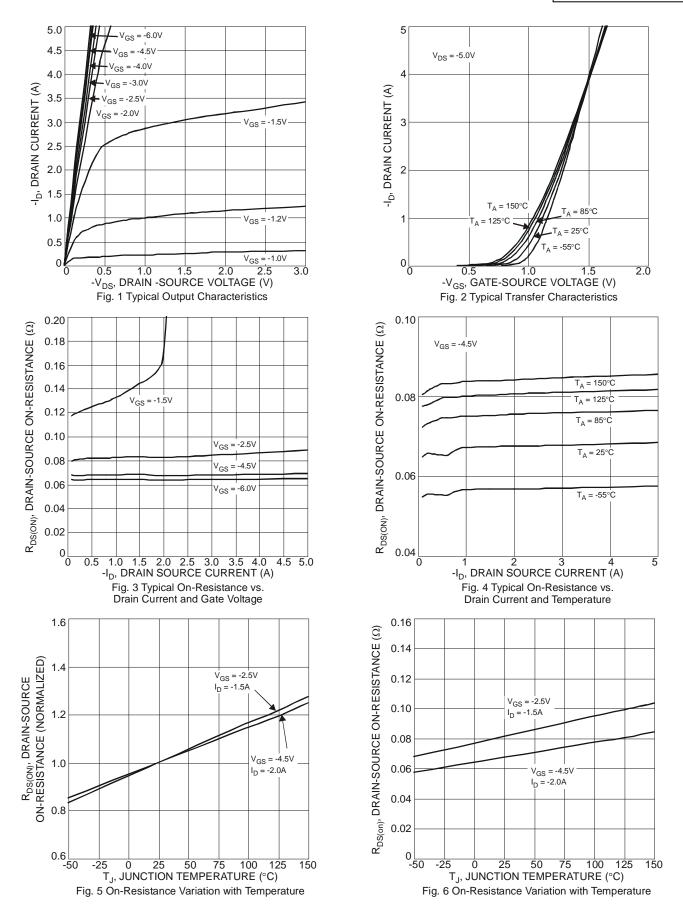
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	-12	-	-	V	$V_{GS} = 0V, I_{D} = -250\mu A$	
Gate-Source Breakdown Voltage	BV _{GSS}	-6.0	-	-	V	$V_{DS} = 0V, I_{G} = -250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	-	-1	μΑ	$V_{DS} = -9.6V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	-100	nA	$V_{GS} = -6V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)						_	
Gate Threshold Voltage	V _{GS(TH)}	-0.35	-0.5	-0.65	V	$V_{DS} = V_{GS}$, $I_D = -250\mu A$	
		-	0.065	0.08		$V_{GS} = -4.5V$, $I_D = -500$ mA	
Static Drain-Source On-Resistance	D	-	0.077	0.1	Ω	$V_{GS} = -2.5V$, $I_D = -500$ mA	
Static Dialit-Source Off-Nesistance	R _{DS(ON)}	-	0.108	0.13	Ω	$V_{GS} = -1.5V, I_D = -500mA$	
		-	0.4	10		$V_{GS} = -0.9V, I_{D} = -100mA$	
Forward Transfer Admittance	Y _{fs}	-	4	-	S	$V_{DS} = -6V, I_{D} = -500mA$	
Diode Forward Voltage	V _{SD}	-	-0.6	-1.0	V	$V_{GS} = 0V, I_{S} = -500mA$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	-	213	350	., ., ., ., .,		
Output Capacitance	Coss	-	119	250	pF	$V_{DS} = -6V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	Crss	-	54.4	90			
Total Gate Charge	Q_g	-	2.5	5			
Gate-Source Charge	Qgs	-	0.3	1	nC	$V_{GS} = -4.5V, V_{DS} = -6V,$	
Gate-Drain Charge	Q_{gd}	-	0.6	-	110	I _D = -500mA	
Gate Charge at V _{TH}	$Q_{g(TH)}$	-	0.15	-			
Turn-On Delay Time	t _{D(ON)}	-	16.7	-		$V_{DS} = -6V, V_{GS} = -2.5V,$ $R_G = 20\Omega, I_D = -500mA$	
Turn-On Rise Time	t _R	-	20.6	-	ns		
Turn-Off Delay Time	t _{D(OFF)}	-	38.4	-	ns		
Turn-Off Fall Time	t _F	-	28.4	-			
Reverse Recovery Charge	Q_{RR}	-	2.0	-	nC	$V_{DD} = -4.0V$, $I_F = -0.5A$,	
Reverse Recovery Time	t _{RR}	-	9.5	-	ns	di/dt =100A/μs	

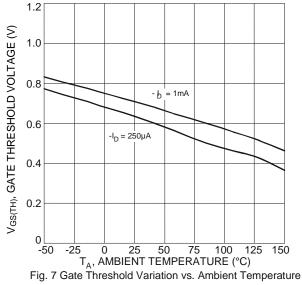
Notes:

- Device mounted on FR-4 material with 1inch² (6.45cm²), 2oz. (0.071mm thick) Cu.
 Repetitive rating, pulse width limited by junction temperature.
 Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.









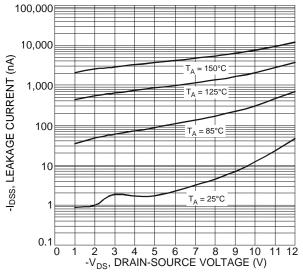
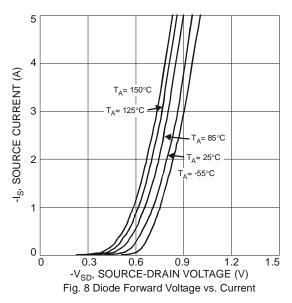
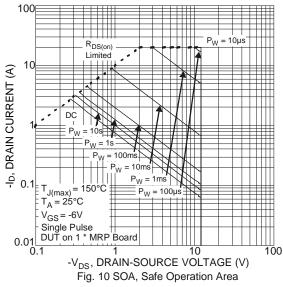
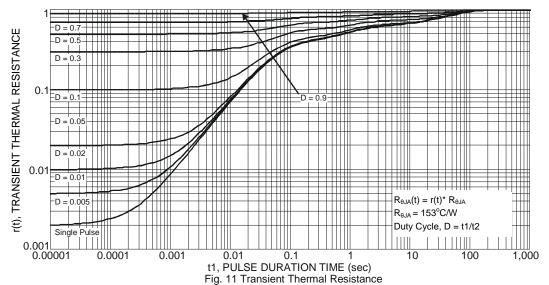


Fig. 9 Typical Drain-Source Leakage Current vs. Voltage





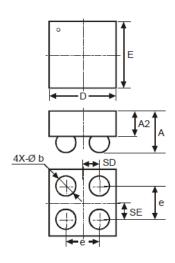




Package Outline Dimension

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-WLB1010-4

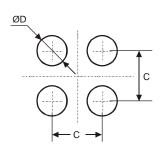


U-WLB1010-4					
Dim	Min	Max	Тур		
D	0.95	1.05	1.00		
Е	0.95	1.05	1.00		
Α	-	0.62	-		
A2	_	_	0.38		
b	0.25	0.35	0.30		
е	-	-	0.50		
SD	_	_	0.25		
SE	_	_	0.25		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-WLB1010-4



Dimensions	Value (in mm)
С	0.50
D	0.25



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