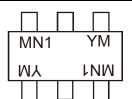


Marking Information



MN1= Product Type Marking Code YM = Date Code Marking Y = Date Code Marking Y or \overline{Y} = Year (ex: I = 2021) M or \overline{M} = Month (ex: 9 = September)

Date	Code Key
	V

Date Code Key												
Year	2007		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	U			J	K	L	М	Ν	0	Р	R	S
Month	lan	Feb	Mar	Apr	May	lun	lul	Αυα	Sen	Oct	Nov	Dec
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteri	stic	Symbol	Value	Units
Drain-Source Voltage		V _{DSS}	60	V
Gate-Source Voltage (Note 5)	Continuous	V _{GSS}	±20	V
Drain Current (Note 5)	Continuous Continuous @ +100°C Pulsed	I _D	115 73 800	mA

Thermal Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation	Po	250	mW
Derating above T _A = +25°C (Note 5)	PD	1.6	mW/°C
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	500	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	О°

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

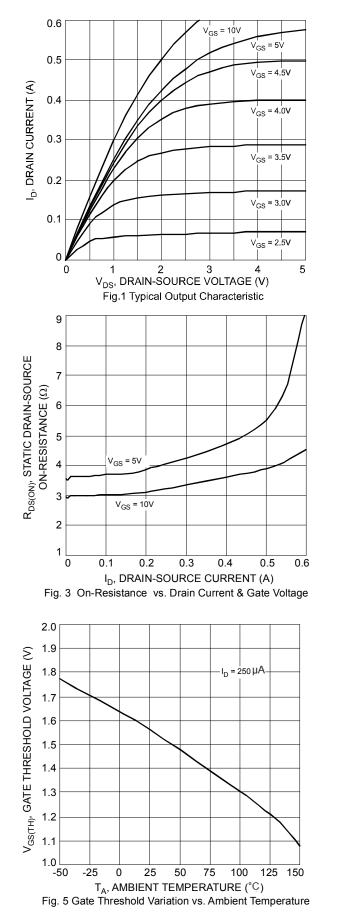
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							•	
Drain-Source Breakdown Voltage		BV _{DSS}	60	70		V	$V_{GS} = 0V, I_D = 10\mu A$	
Zero Gate Voltage Drain Current	@ T _C = +25°C @ T _C = +125°C	I _{DSS}	_	_	1.0 500	μA	V _{DS} = 60V, V _{GS} = 0V	
Gate-Body Leakage				_	±5	μA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)								
Gate Threshold Voltage		V _{GS(th)}	1.2	—	2.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
	@ T _J = +25°C	R _{DS(on)}	_	3.5	6	Ω	V _{GS} = 5V, I _D = 0.115A	
Static Drain-Source On-Resistance	@ T _J = +125°C			3.0	5		V _{GS} = 10V, I _D = 0.115A	
Forward Transconductance		g fs	80	_		mS	V _{DS} = 10V, I _D = 0.115A	
Diode Forward Voltage		V _{SD}	_	0.8	1.2	V	V _{GS} = 0V, I _S = 115mA	
DYNAMIC CHARACTERISTICS (Note	e 7)						•	
Input Capacitance		Ciss	_	23	_	pF		
Output Capacitance Reverse Transfer Capacitance		Coss	_	3.4	_	pF	V _{DS} = 25V, V _{GS} = 0V, f = 1.0MHz	
		Crss	_	1.4	_	pF		
Turn-On Delay Time		t _{D(on)}		10		ns	$V_{DD} = 30V, I_D = 0.115A, R_L = 150\Omega,$	
urn-Off Delay Time		t _{D(off)}		33		ns	$V_{\text{GEN}} = 10V, R_{\text{GEN}} = 25\Omega$	

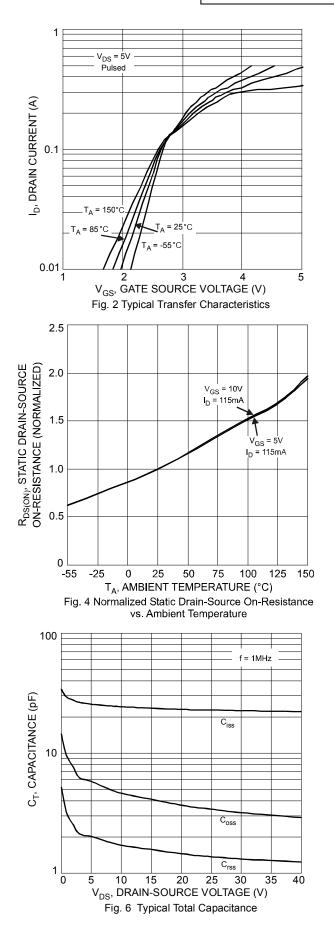
5. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on www.diodes.com/package-outlines.html Notes: 6. Short duration pulse test used to minimize self-heating effect.

7. Guaranteed by design. Not subject to product testing.



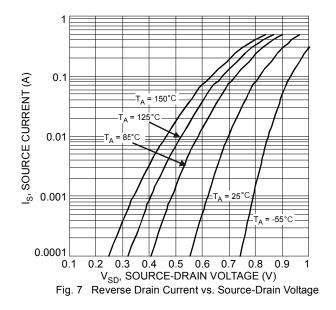
DMN66D0LDW





DMN66D0LDW Document number: DS31232 Rev. 7 - 2 Downloaded from Arrow.com.

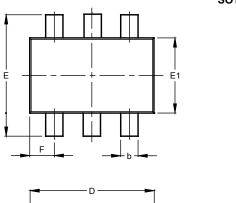


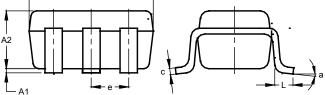




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

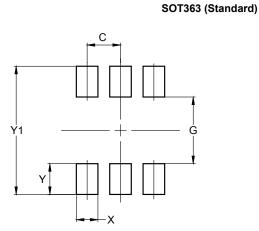




SOT363 (Standard)							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.80	1.00	0.90				
b	0.10	0.35	0.225				
с	0.08	0.22	0.15				
D	1.80	2.20	2.00				
ш	2.00	2.45	2.225				
E1	1.15	1.35	1.25				
e	1		0.65				
F	0.25	0.45	0.35				
L	0.25	0.46	0.355				
a	0°	8°					
All I	All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.650
G	1.300
Х	0.420
Y	0.600
Y1	2.500

SOT363 (Standard)



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