Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 6) V_{GS} = 10V	Steady State	T_A = 25°C T_A = 70°C	I_D	0.65 0.50	A
Maximum Continuous Body Diode Forward Current (Note 6)			I_S	0.4	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I_{DM}	4	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		P_D	0.29	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	420	°C/W
Total Power Dissipation (Note 6)		P_D	0.35	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	360	°C/W
Thermal Resistance, Junction to Case		$R_{\theta JC}$	128	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to 150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	30	-	-	V	V_{GS} = 0V, I_D = 250µA
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	µA	V_{DS} = 30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	-	-	±10	µA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	0.8	-	1.6	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS\ (ON)}$	-	0.2	0.4	Ω	V_{GS} = 10V, I_D = 0.25A
		-	0.3	0.7		V_{GS} = 4.5V, I_D = 0.25A
		-	0.4	1.0		V_{GS} = 4.0V, I_D = 0.25A
		-	0.9	-		V_{GS} = 2.5V, I_D = 0.01A
Diode Forward Voltage	V_{SD}	-	0.8	1.2	V	V_{GS} = 0V, I_S = 0.23A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	-	50	-	pF	V_{DS} = 15V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	-	10	-	pF	
Reverse Transfer Capacitance	C_{rss}	-	6.8	-	pF	
Gate Resistance	R_g	-	114	-	Ω	V_{DS} = V_{GS} = 0V,f = 1.0MHz
Total Gate Charge (V_{GS} = 4.5V)	Q_g	-	0.6	-	nC	V_{DS} = 10V, I_D = 250mA
Total Gate Charge (V_{GS} = 10V)	Q_g	-	1.3	-	nC	
Gate-Source Charge	Q_{gs}	-	0.2	-	nC	
Gate-Drain Charge	Q_{gd}	-	0.1	-	nC	
Turn-On Delay Time	$t_{D(on)}$	-	2.8	-	ns	V_{GS} = 10V, V_{DS} = 30V, I_D = 100mA, R_G = 10Ω
Turn-On Rise Time	t_r	-	3.2	-	ns	
Turn-Off Delay Time	$t_{D(off)}$	-	26.3	-	ns	
Turn-Off Fall Time	t_f	-	22.8	-	ns	

Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

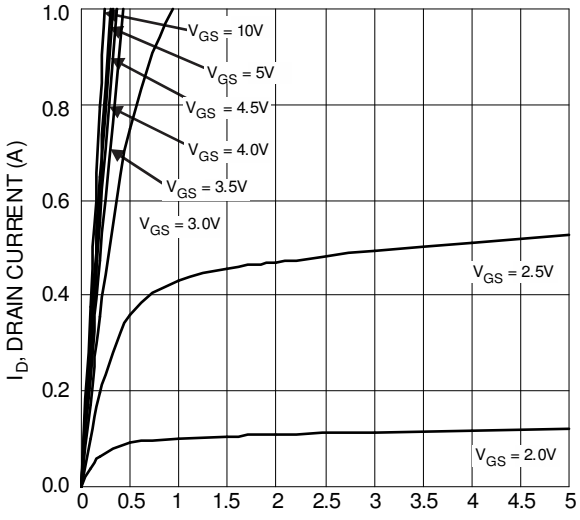


Figure 1 Typical Output Characteristic

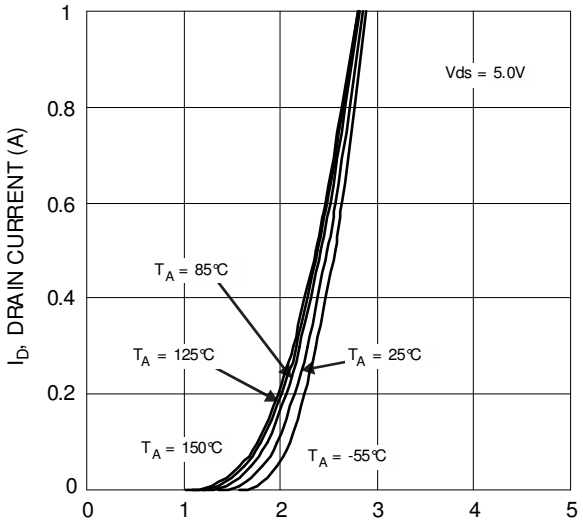


Figure 2 Typical Transfer Characteristic

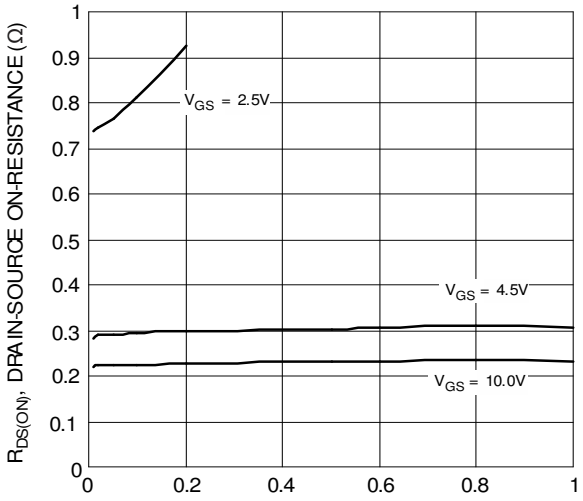


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

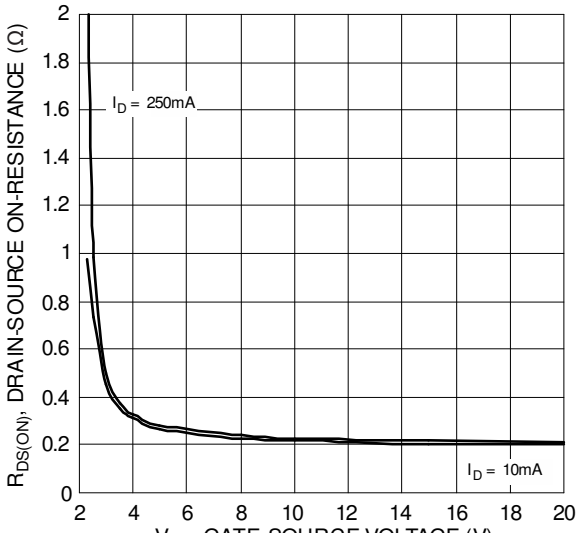


Figure 4 TypicalTransfer Characteristic

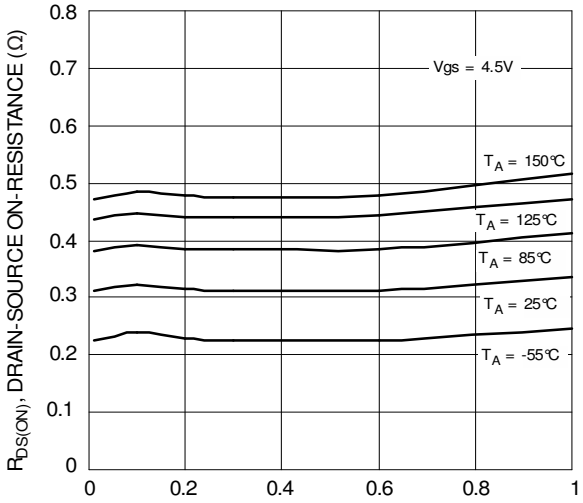


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

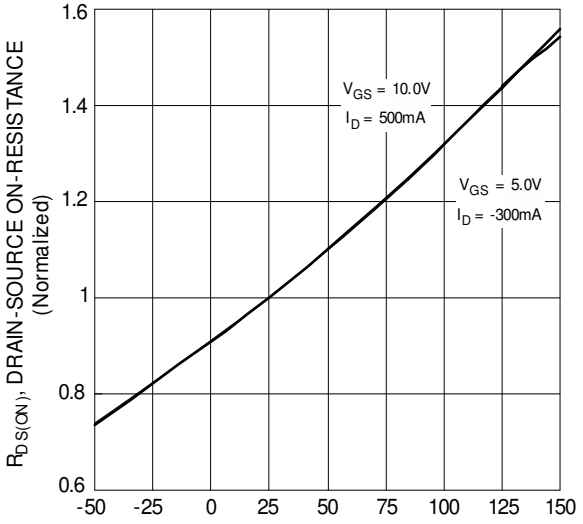


Figure 6 On-Resistance Variation with Temperature

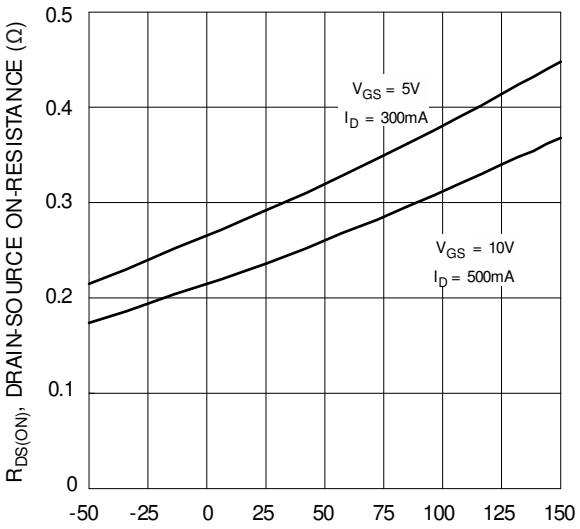


Figure 7 On-Resistance Variation with Temperature

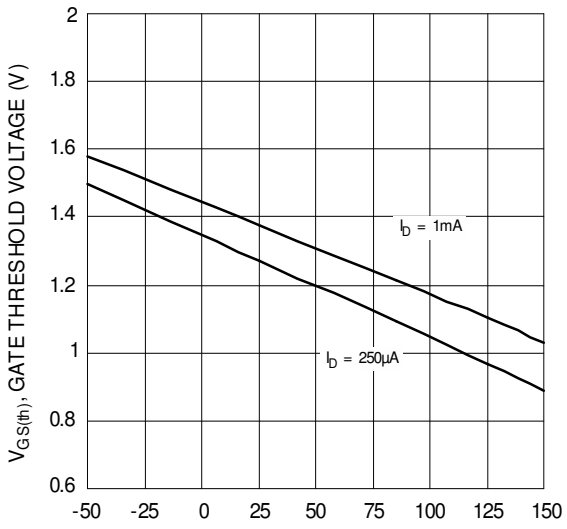


Figure 8 Gate Threshold Variation vs. Ambient Temperature

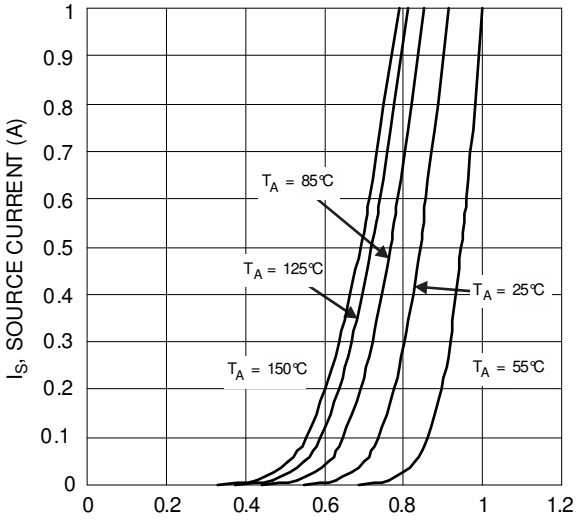


Figure 9 Diode Forward Voltage vs. Current

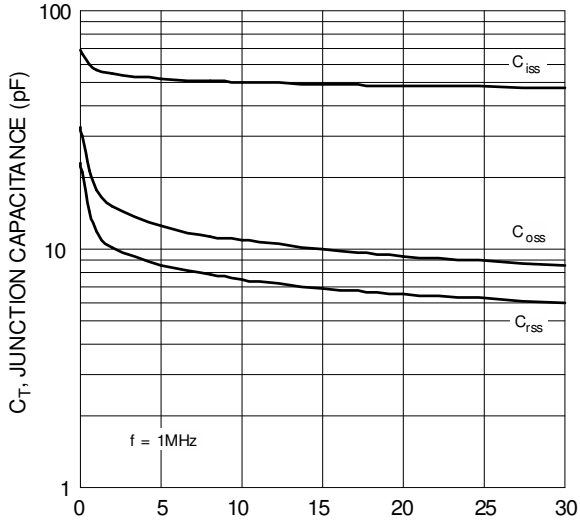


Figure 10 Typical Junction Capacitance

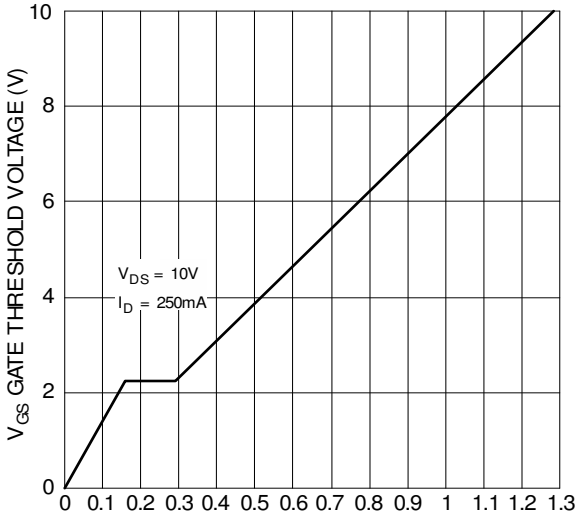


Figure 11 Gate Charge

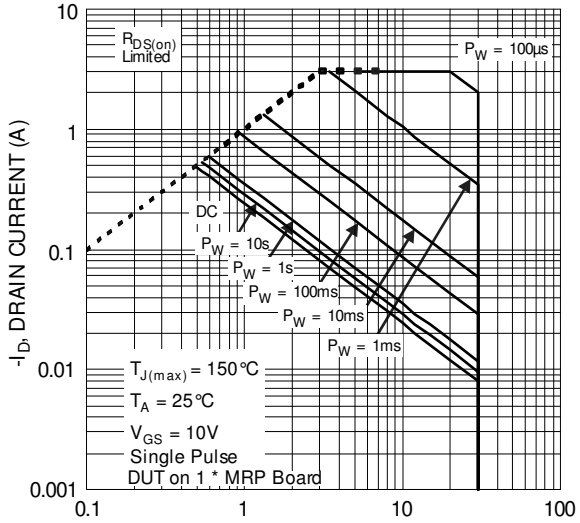


Figure 12 SOA, Safe Operation Area

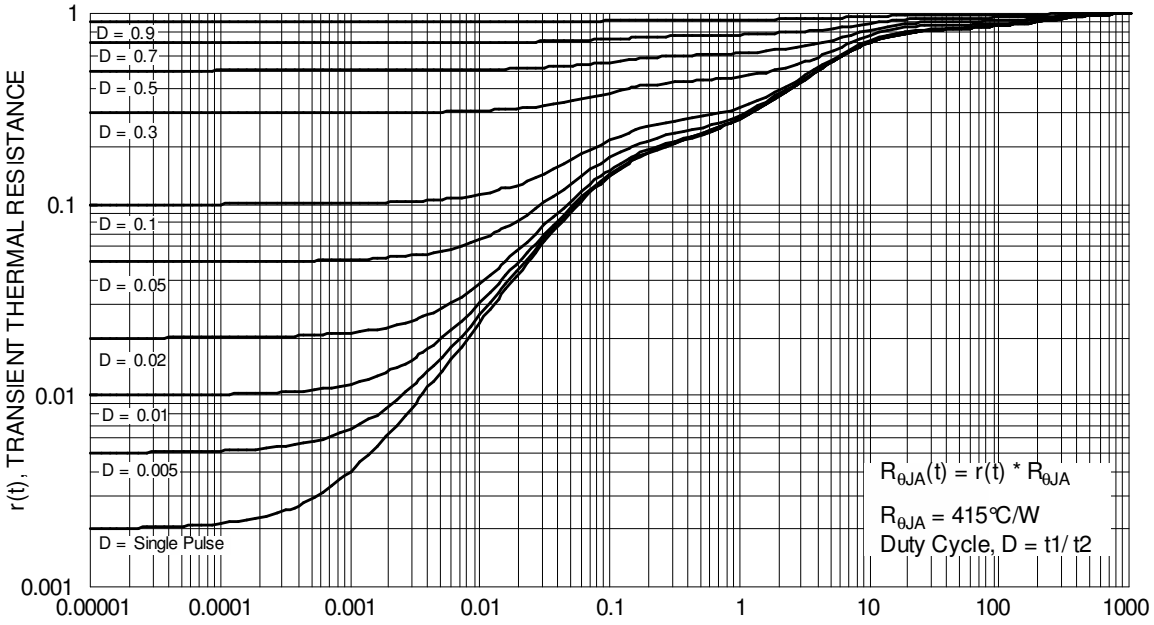
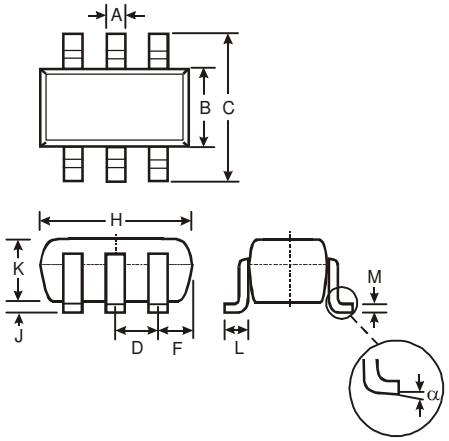


Figure 13 Transient Thermal Resistance

Package Outline Dimensions

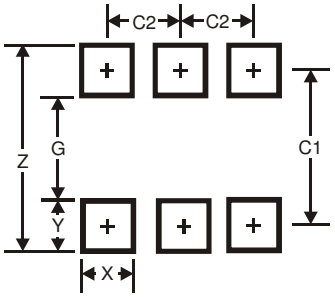
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



SOT363			
Dim	**Min**	**Max**	**Typ**
A	0.10	0.30	0.25
B	1.15	1.35	1.30
C	2.00	2.20	2.10
D	0.65 Typ		
F	0.40	0.45	0.425
H	1.80	2.20	2.15
J	0	0.10	0.05
K	0.90	1.00	1.00
L	0.25	0.40	0.30
M	0.10	0.22	0.11
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	2.5
G	1.3
X	0.42
Y	0.6
C1	1.9
C2	0.65

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