

NOT RECOMMENDED FOR NEW DESIGN USE DMN2058U

DMG3420U

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V _{DSS}	20	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 5)	Steady State	$T_A = +25^{\circ}C$ $T_A = +85^{\circ}C$	I _D	5.47 3.43	А
Pulsed Drain Current (Note 6)			I _{DM}	20	А

Thermal Characteristics

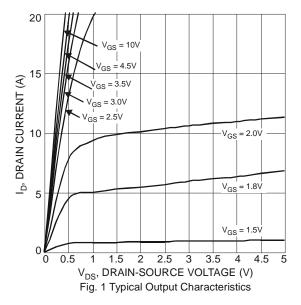
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	0.74	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	$R_{\theta JA}$	167	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

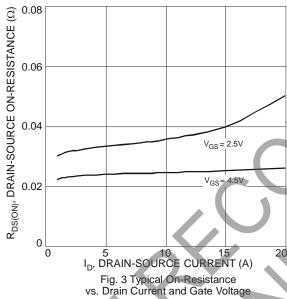
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	20			V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	47	_	1.0	μA	$V_{DS} = 20V$, $V_{GS} = 0V$	
Gate-Source Leakage	Igss	13	_	±100	nΑ	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)	ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	0.95	1.2	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
			21	29	mΩ	$V_{GS} = 10V, I_D = 6A$	
Static Dunin Source On Bosistanes			25	35		$V_{GS} = 4.5V, I_D = 5A$	
Static Drain-Source On-Resistance	R _{DS(ON)}		34	48		V _{GS} = 2.5V, I _D = 4A	
			65	91		V _{GS} = 1.8V, I _D = 2A	
Forward Transfer Admittance	Y _{fs}	/- ,	9	_	s	$V_{DS} = 5V, I_D = 3.8A$	
Diode Forward Voltage	V _{SD}		0.75	1.0	V	V _{GS} = 0V, I _S = 1A	
DYNAMIC CHARACTERISTICS (Note 8)					•		
Input Capacitance	C _{iss}		434.7	1	pF		
Output Capacitance	Coss	_	69.1	-	pF	$V_{DS} = 10V, V_{GS} = 0V,$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	61.2	_	pF	1 = 1.UIVIMZ	
Gate Resistance	Rg	_	1.53	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_g	_	5.4	_	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 6A	
Gate-Source Charge	Q _{gs}	_	0.9	_	nC		
Gate-Drain Charge	Q_{gd}	_	1.5	_	nC		
Turn-On Delay Time	t _{D(ON)}	_	6.5	_	ns		
Turn-On Rise Time	t _R	_	8.3	_	ns	$V_{DD} = 10V, V_{GS} = 5V,$	
Turn-Off Delay Time	t _{D(OFF)}	_	21.6	_	ns	$R_L = 1.7\Omega$, $R_g = 6\Omega$	
Turn-Off Fall Time	t _F	_	5.3	_	ns		

Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Repetitive rating, pulse width limited by junction temperature.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.





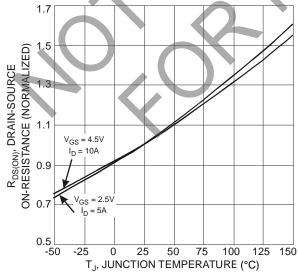
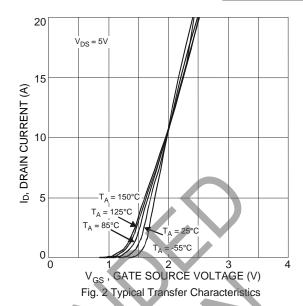
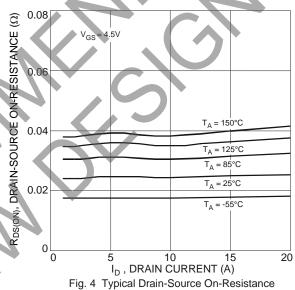
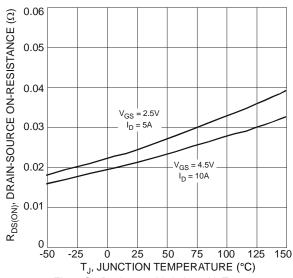


Fig. 5 On-Resistance Variation with Temperature





vs. Drain Current and Temperature







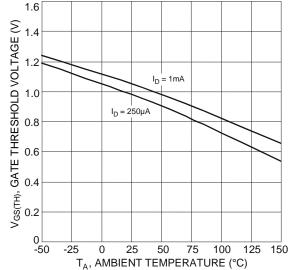
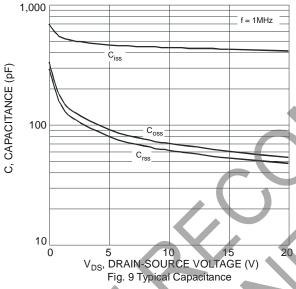
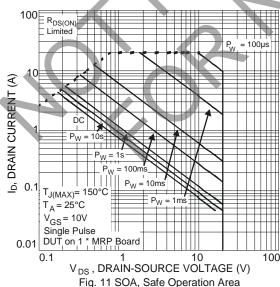
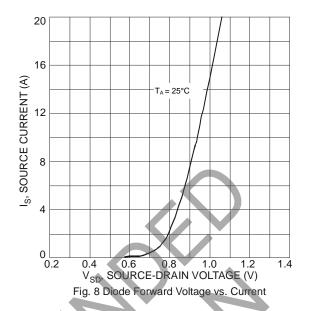
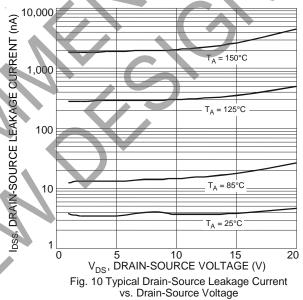


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

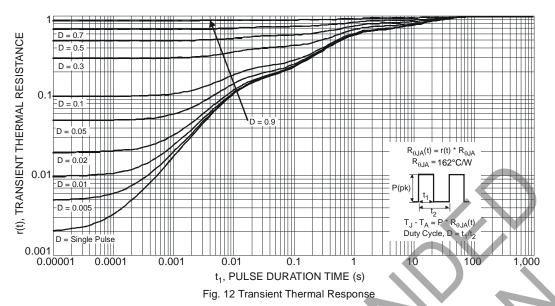






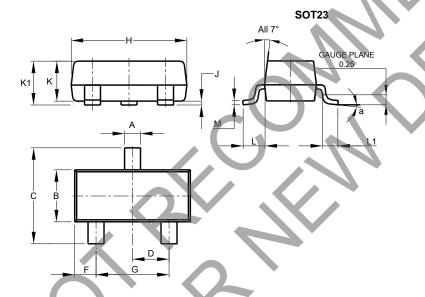


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Package Outline Dimensions

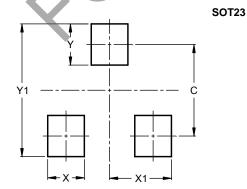
Please see http://www.diodes.com/package-outlines.html for the latest version.



SOT23					
Dim	Min	Max	Тур		
Α	0.37	0.51	0.40		
В	1.20	1.40	1.30		
С	2.30	2.50	2.40		
D	0.89	1.03	0.915		
F	0.45	0.60	0.535		
G	1.78	2.05	1.83		
Н	2.80	3.00	2.90		
7	0.013	0.10	0.05		
K	0.890	1.00	0.975		
K1	0.903	1.10	1.025		
L	0.45	0.61	0.55		
L1	0.25	0.55	0.40		
М	0.085	0.150	0.110		
а	0°	8°			
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Y	0.9
Y1	2.9



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