

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Power Dissipation	P _{PP}	84	W	8/20μs, Per Fig. 1
Peak Pulse Current	IPP	6	А	8/20μs, Per Fig. 1
ESD Protection – Contact Discharge	$V_{ESD_Contact}$	±30	kV	Standard IEC 61000-4-2
ESD Protection – Air Discharge	V_{ESD_Air}	±30	kV	Standard IEC 61000-4-2

Thermal Characteristics

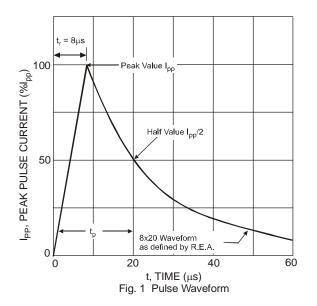
Characteristic	Symbol	Value	Unit
Package Power Dissipation (Note 4)	PD	200	mW
Thermal Resistance, Junction to Ambient (Note 4)	R _{0JA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

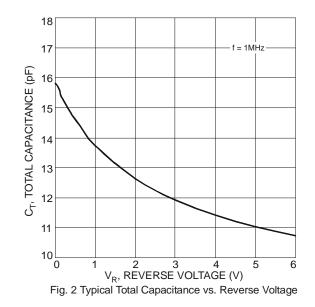
Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Conditions
Reverse Standoff Voltage	VRWM	-	-	5	V	-
Channel Leakage Current (Note 5)	I _{RM}	-	10	100	nA	$V_{RWM} = 5V$
Clamping Voltage, Positive Transients		-	7.0	9.0	v	I _{PP} = 1A, t _p = 8/20µS
	14	-	8.7	10.7		$I_{PP} = 3A, t_p = 8/20\mu S$
	V _{CL}	-	10.5	12.0		$I_{PP} = 5A, t_p = 8/20\mu S$
		-	11.5	14.0		$I_{PP} = 6A, t_p = 8/20\mu S$
Breakdown Voltage	V _{BR}	6	7	8	V	$I_R = 1 m A$
Differential Resistance	R _{DIF}	-	0.2	-	Ω	I _R = 1A, tp = 8/20µS
Channel Input Capacitance	CT	-	15	20	pF	$V_R = 0V, f = 1MHz$

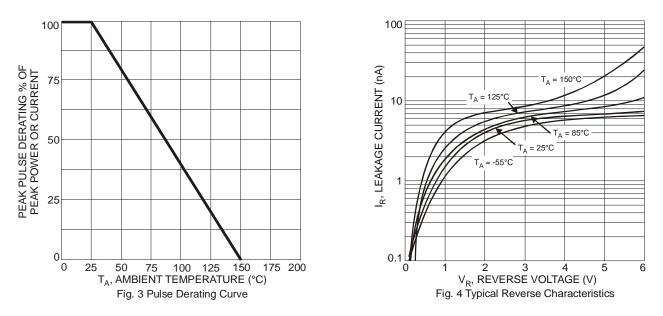
Notes: 4. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. suggested pad layout AP02001, which can be found on our website at http://www.diodes.com.

5. Short duration pulse test used to minimize self-heating effect.

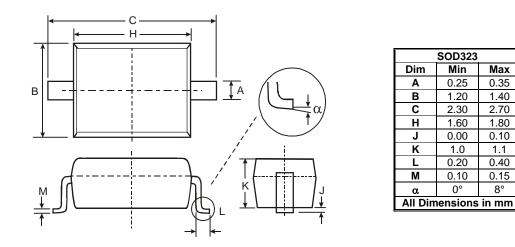




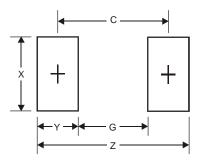




Package Outline Dimensions



Suggested Pad Layout



Dimensions	Value (in mm)
Z	3.75
G	1.05
Х	0.65
Y	1.35
С	2.40

Max

0.35

1.40

2.70

1.80

0.10

1.1

0.40

0.15

8°



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