

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

| Characteristic                                        | Symbol                   | Value       | Unit | Conditions                      |
|-------------------------------------------------------|--------------------------|-------------|------|---------------------------------|
| Peak Pulse Current, per IEC 61000-4-5                 | I <sub>PP</sub>          | 20          | A    | I/O to V <sub>SS</sub> , 8/20μs |
| Peak Pulse Power, per IEC 61000-4-5                   | P <sub>PP</sub>          | 180         | W    | I/O to V <sub>SS</sub> , 8/20μs |
| ESD Protection – Contact Discharge, per IEC 61000-4-2 | V <sub>ESD_CONTACT</sub> | 30          | kV   | I/O to V <sub>SS</sub>          |
| ESD Protection – Air Discharge, per IEC 61000-4-2     | V <sub>ESD_AIR</sub>     | 30          | kV   | I/O to V <sub>SS</sub>          |
| Operating Temperature                                 | T <sub>OP</sub>          | -55 to +85  | °C   | —                               |
| Storage Temperature                                   | T <sub>STG</sub>         | -55 to +150 | °C   | —                               |

**Thermal Characteristics**

| Characteristic                                           | Symbol           | Value | Unit |
|----------------------------------------------------------|------------------|-------|------|
| Power Dissipation Typical (Note 5)                       | P <sub>D</sub>   | 300   | mW   |
| Thermal Resistance, Junction to Ambient Typical (Note 5) | R <sub>θJA</sub> | 417   | °C/W |

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

| Characteristic                   | Symbol                                   | Min | Typ  | Max | Unit | Test Conditions                                          |
|----------------------------------|------------------------------------------|-----|------|-----|------|----------------------------------------------------------|
| Reverse Working Voltage          | V <sub>RWM</sub>                         | —   | —    | 3.3 | V    | I <sub>R</sub> =1mA, I/O to V <sub>SS</sub>              |
| Reverse Current (Note 6)         | I <sub>R</sub>                           | —   | —    | 1   | μA   | V <sub>R</sub> = 3.3V, I/O to V <sub>SS</sub>            |
| Reverse Breakdown Voltage        | V <sub>BR</sub>                          | 4.5 | —    | 8.0 | V    | I <sub>R</sub> = 1mA, I/O to V <sub>SS</sub>             |
| Forward Clamping Voltage         | V <sub>F</sub>                           | —   | 0.8  | 1.2 | V    | I <sub>F</sub> = 15mA, V <sub>SS</sub> to I/O            |
| Reverse Clamping Voltage (Note7) | V <sub>C</sub>                           | —   | 6    | —   | V    | I <sub>PP</sub> = 5A, I/O to V <sub>SS</sub> , 8/20μs    |
| ESD Clamping Voltage             | V <sub>ESD</sub>                         | —   | 7.5  | —   | V    | TLP, 12A, t <sub>p</sub> = 100ns, I/O to V <sub>SS</sub> |
| Dynamic Reverse Resistance       | R <sub>DIF-R</sub>                       | —   | 0.15 | —   | Ω    | TLP, 12A, t <sub>p</sub> = 100ns, I/O to V <sub>SS</sub> |
| Channel Input Capacitance        | C <sub>I/O</sub>                         | —   | 2.4  | 3   | pF   | V <sub>I/O</sub> = 1.65V, V <sub>SS</sub> = 0V, f = 1MHz |
| Delta C <sub>I/O</sub>           | C <sub>I/OMAX</sub> –C <sub>I/OMIN</sub> | —   | 0.04 | —   | pF   | C <sub>I/OMAX</sub> –C <sub>I/OMIN</sub>                 |

- Notes:
5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes, Inc. website at <http://www.diodes.com/package-outlines.html>.
  6. Short duration pulse test used to minimize self-heating effect.
  7. Clamping voltage value is based on an 8x20μs peak pulse current (I<sub>PP</sub>) waveform.

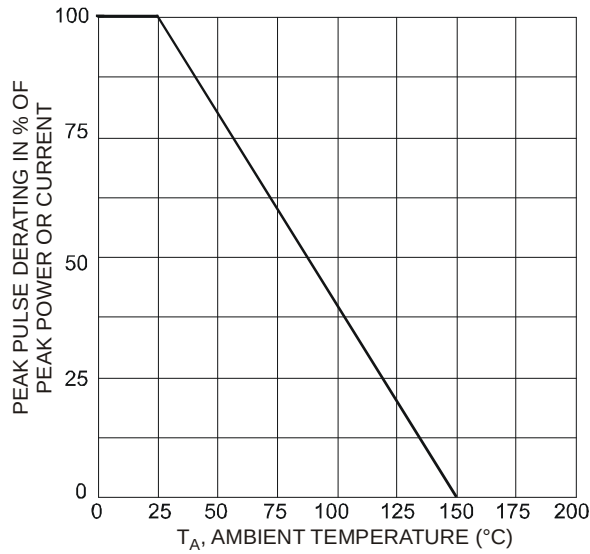


Figure 1 Pulse Derating Curve

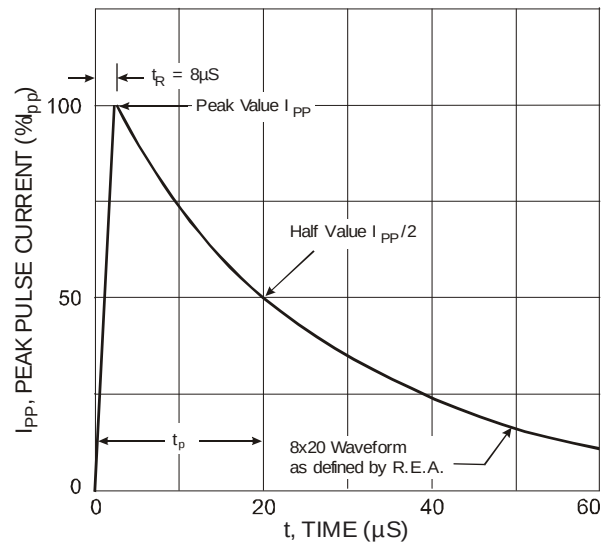


Figure 2 Pulse Waveform

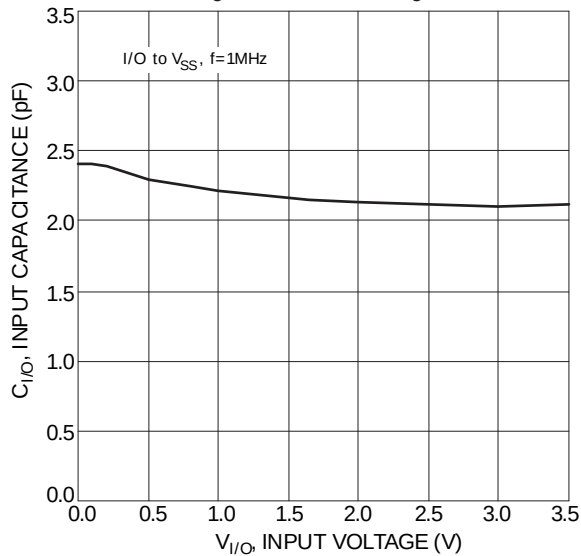


Figure 3 Input Capacitance vs. Input Voltage

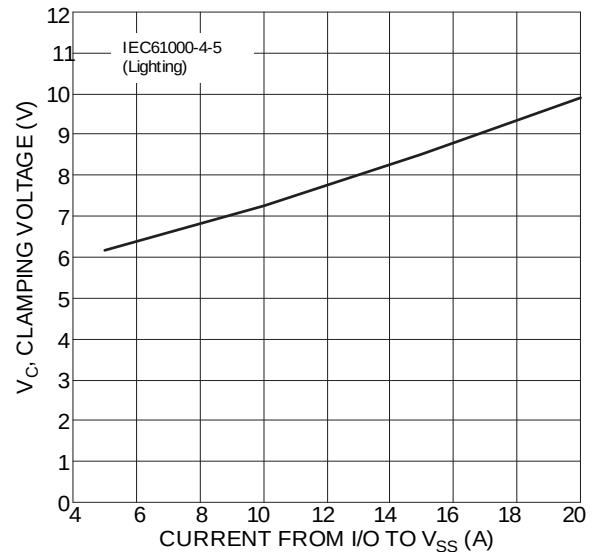


Figure 4 Clamping Voltage Characteristic

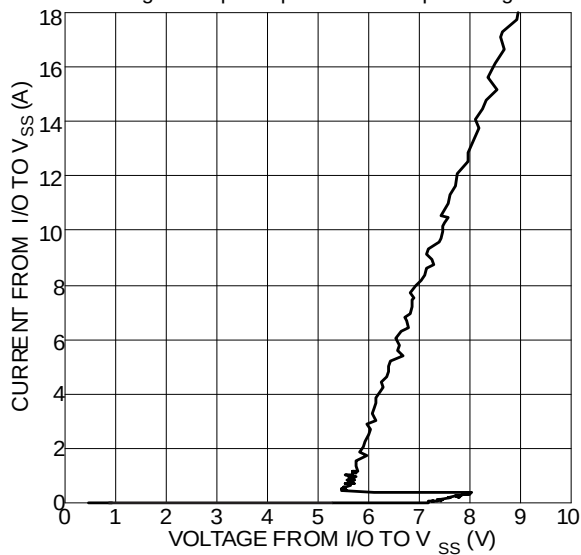
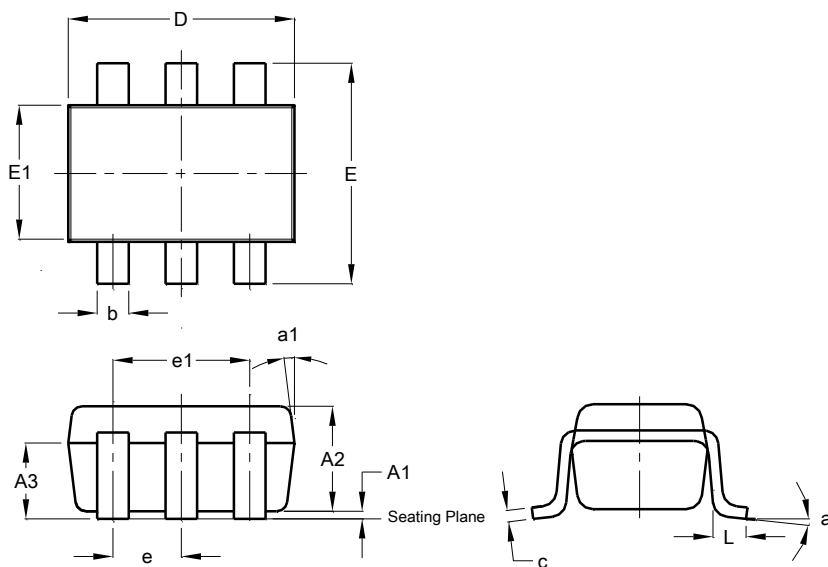


Figure 5 Current vs. Voltage

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT26**

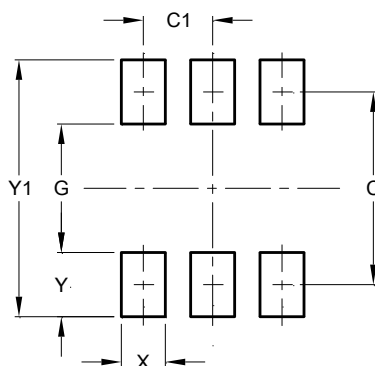


| SOT26                |       |      |      |
|----------------------|-------|------|------|
| Dim                  | Min   | Max  | Typ  |
| A1                   | 0.013 | 0.10 | 0.05 |
| A2                   | 1.00  | 1.30 | 1.10 |
| A3                   | 0.70  | 0.80 | 0.75 |
| b                    | 0.35  | 0.50 | 0.38 |
| c                    | 0.10  | 0.20 | 0.15 |
| D                    | 2.90  | 3.10 | 3.00 |
| e                    | —     | —    | 0.95 |
| e1                   | —     | —    | 1.90 |
| E                    | 2.70  | 3.00 | 2.80 |
| E1                   | 1.50  | 1.70 | 1.60 |
| L                    | 0.35  | 0.55 | 0.40 |
| a                    | —     | —    | 8°   |
| a1                   | —     | —    | 7°   |
| All Dimensions in mm |       |      |      |

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SOT26**



| Dimensions | Value (in mm) |
|------------|---------------|
| C          | 2.40          |
| C1         | 0.95          |
| G          | 1.60          |
| X          | 0.55          |
| Y          | 0.80          |
| Y1         | 3.20          |

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