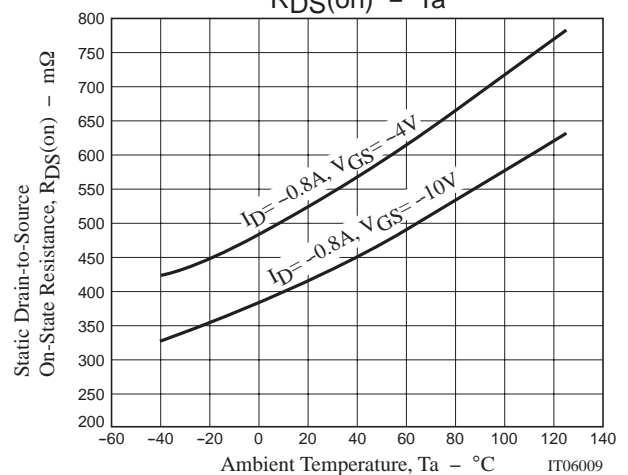
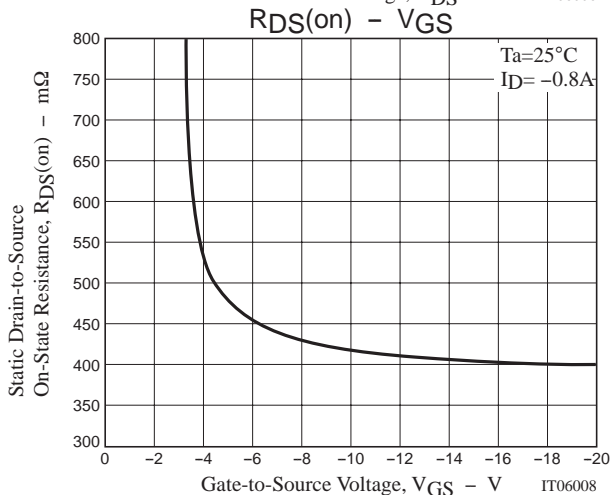
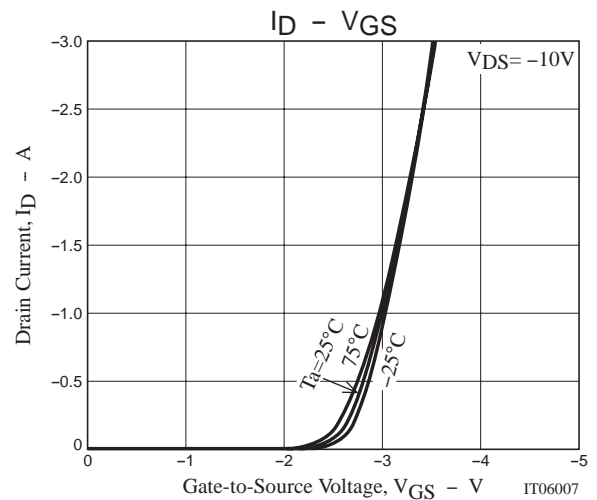
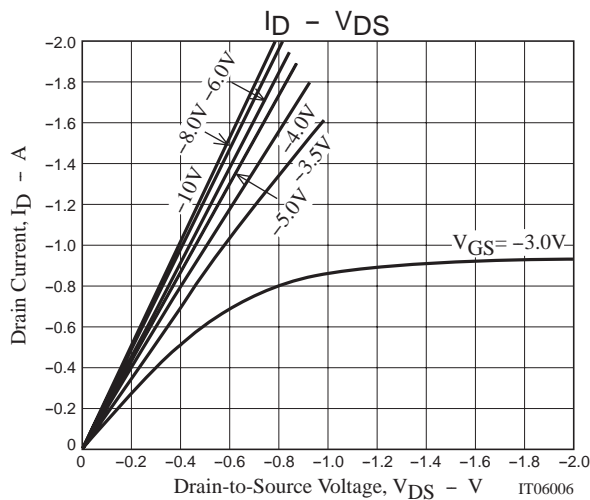
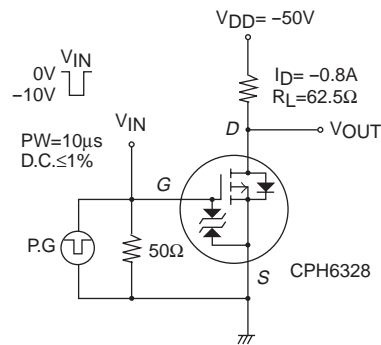
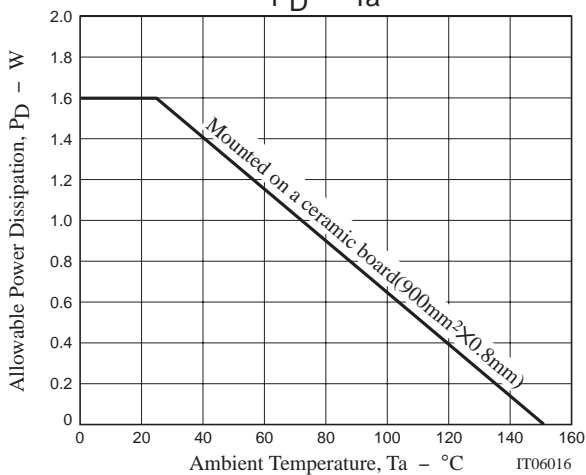
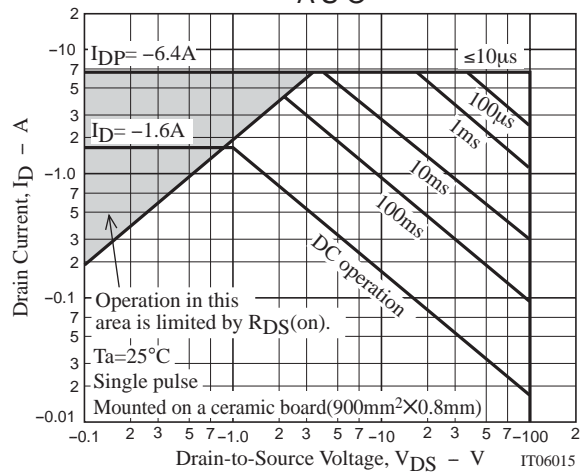
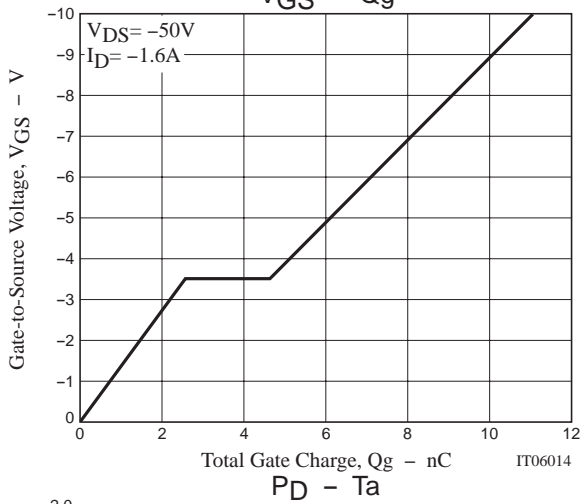
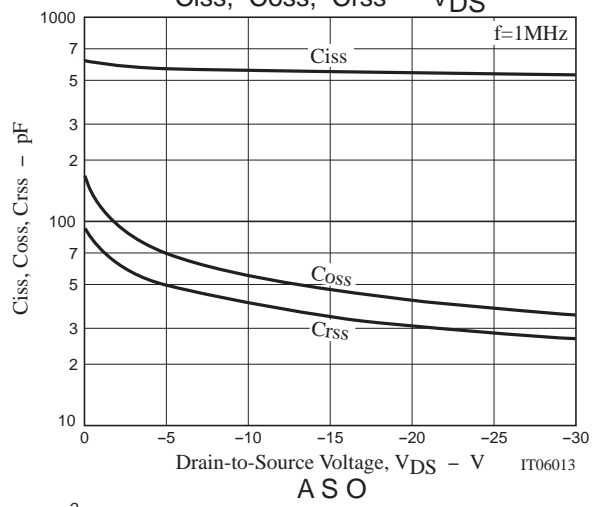
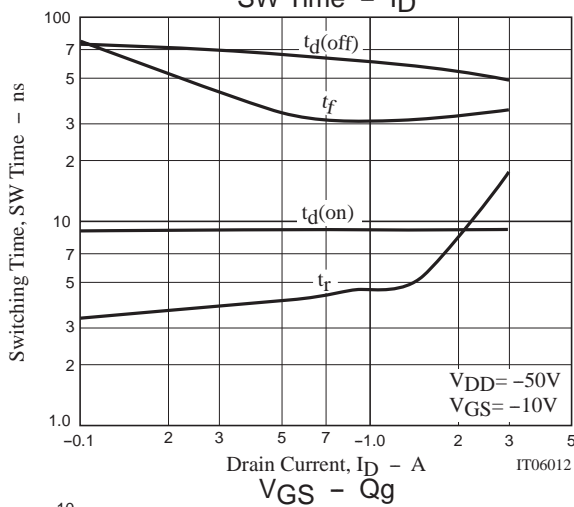
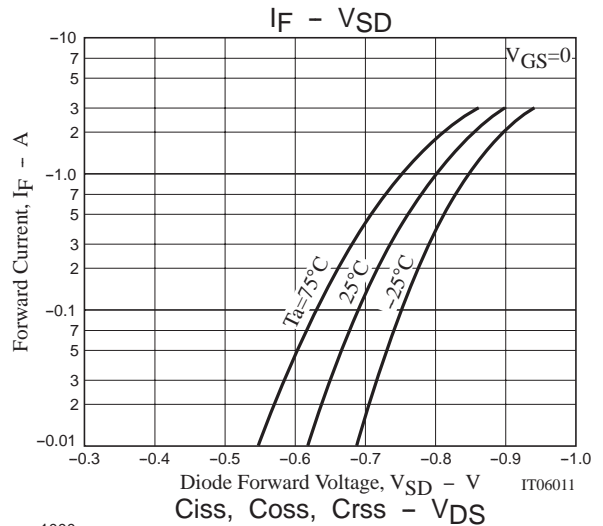
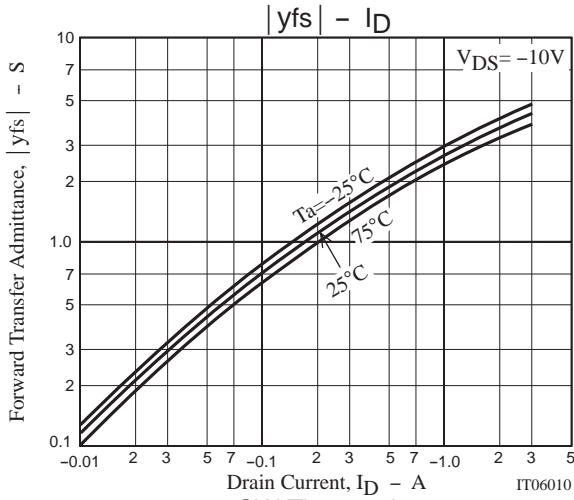


Continued from preceding page.

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	$C_{iss}$	$V_{DS} = -20V, f = 1MHz$		535		pF
Output Capacitance	$C_{oss}$	$V_{DS} = -20V, f = 1MHz$		43		pF
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS} = -20V, f = 1MHz$		31		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit.		9		ns
Rise Time	$t_r$	See specified Test Circuit.		5		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit.		62		ns
Fall Time	$t_f$	See specified Test Circuit.		34		ns
Total Gate Charge	$Q_g$	$V_{DS} = -50V, V_{GS} = -10V, I_D = -1.6A$		11		nC
Gate-to-Source Charge	$Q_{gs}$	$V_{DS} = -50V, V_{GS} = -10V, I_D = -1.6A$		2.6		nC
Gate-to-Drain "Miller" Charge	$Q_{gd}$	$V_{DS} = -50V, V_{GS} = -10V, I_D = -1.6A$		2		nC
Diode Forward Voltage	$V_{SD}$	$I_S = -1.6A, V_{GS} = 0$		-0.83	-1.2	V

## Switching Time Test Circuit





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