

# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V <sub>DSS</sub>	-50	V
Drain-Gate Voltage (Note 5)		$V_{DGR}$	-50	V
Gate-Source Voltage	Continuous	V <sub>GSS</sub>	±20	V
Drain Current (Note 6)	Continuous	I <sub>D</sub>	-130	mA

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Units
Total Power Dissipation	$P_{D}$	150	mW
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	833	°C/W
Operating and Storage Temperature Range	T <sub>j</sub> , T <sub>STG</sub>	-55 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	-						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-50	-75	_	V	$V_{GS} = 0V, I_D = -250\mu A$	
		_	_	-1	μA	$V_{DS} = -50V$ , $V_{GS} = 0V$ , $T_{J} = +25$ °C	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		-2	μΑ	$V_{DS} = -50V$ , $V_{GS} = 0V$ , $T_{J} = +125$ °C	
		_		-100	nA	$V_{DS} = -25V$ , $V_{GS} = 0V$ , $T_{J} = +25$ °C	
Gate-Body Leakage	I <sub>GSS</sub>	_	_	±50	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-0.8	-1.6	-2.0	V	$V_{DS} = V_{GS}$ , $I_D = -1mA$	
Static Drain-Source On-Resistance	R <sub>DS (ON)</sub>		2	10	Ω	$V_{GS} = -5V, I_D = -0.100A$	
Forward Transconductance	g <sub>FS</sub>	0.05	_	_	S	$V_{DS} = -25V, I_D = -0.1A$	
DYNAMIC CHARACTERISTICS							
Input Capacitance	C <sub>iss</sub>	_	_	45	pF		
Output Capacitance	Coss	_	_	25	pF	$V_{DS} = -25V, V_{GS} = 0V, f = 1.0MHz$	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	_	12	pF		
SWITCHING CHARACTERISTICS							
Turn-On Delay Time	t <sub>D(ON)</sub>	_	10	_	ns	$V_{DD} = -30V$ , $I_{D} = -0.27A$ ,	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	18	_	ns	$R_{GEN} = 50\Omega$ , $V_{GS} = -10V$	

Notes:

- $5. \quad R_{\text{GS}} \leq 20 \text{K}\Omega.$
- 6. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at http://www.diodes.com/package-outlines.html.
- 7. Short duration pulse test used to minimize self-heating effect.



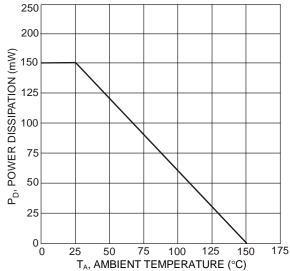
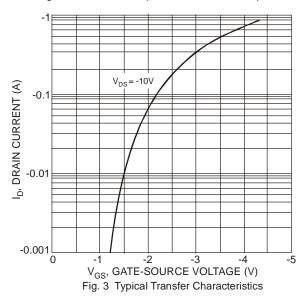
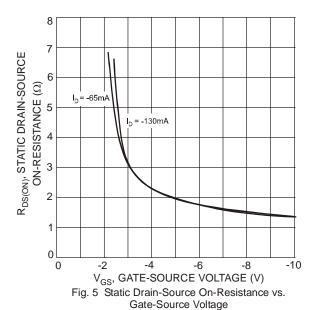
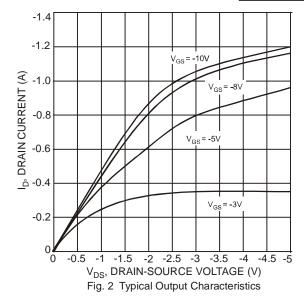


Fig. 1 Max Power Dissipation vs. Ambient Temperature







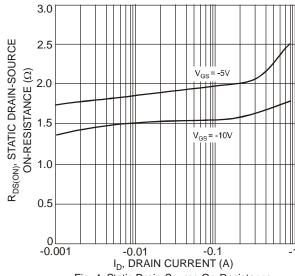


Fig. 4 Static Drain-Source On-Resistance vs. Drain Current

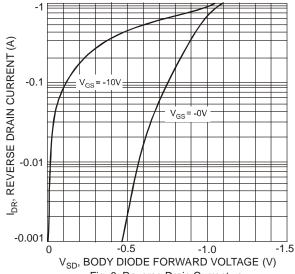


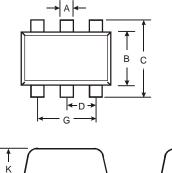
Fig. 6 Reverse Drain Current vs. Body Diode Forward Voltage



## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

### **SOT563**



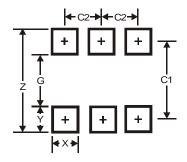
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SOT563				
Dim	Min	Max	Тур	
Α	0.15	0.30	0.20	
В	1.10	1.25	1.20	
С	1.55	1.70	1.60	
D	-	-	0.50	
G	0.90	1.10	1.00	
Н	1.50	1.70	1.60	
K	0.55	0.60	0.60	
L	0.10	0.30	0.20	
М	0.10	0.18	0.11	
All Dimensions in mm				

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

## SOT563



Dimensions	Value (in mm)
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5



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