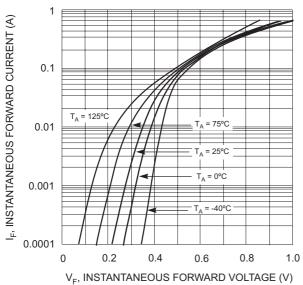
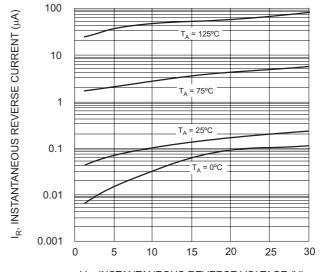
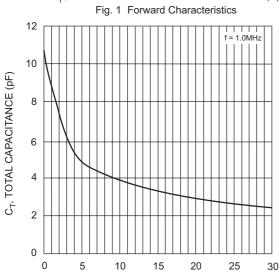
BAT54 thru BAT54S



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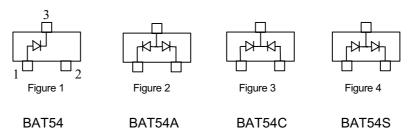




 $\label{eq:VR} {\rm V_R,\,REVERSE\,\,VOLTAGE\,\,(V)}$ Fig. 3 Typical Capacitance vs. Reverse Voltage

T_A, AMBIENT TEMPERATURE (°C) Fig. 4 Power Derating Curve

Pin Configuration - Top View





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