

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	40	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R		
RMS Reverse Voltage	V _{R(RMS)}	28	V
Average Rectified Current	I _O	1.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	5.5	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P _D	500	mW
Typical Thermal Resistance, Junction to Ambient Air (Note 5)	R _{θJA}	200	°C/W
Operating Temperature Range	T _J	-40 to +125	°C
Storage Temperature Range	T _{STG}	-40 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	V _{(BR)R}	40	—	—	V	I _R = 300μA
Forward Voltage	V _F	—	225	270	mV	I _F = 50mA
			235	290		I _F = 100mA
			290	340		I _F = 250mA
			340	400		I _F = 500mA
			390	450		I _F = 750mA
			420	500		I _F = 1000mA
			475	600		I _F = 1500mA
Reverse Current (Note 6)	I _R	—	25	100	μA	V _R = 30V
Total Capacitance	C _T	—	175	—	pF	V _R = 0V, f = 1.0MHz
		—	25	—	pF	V _R = 25V, f = 1.0MHz
Reverse Recovery Time	t _{RR}	—	12	—	ns	I _F = 10mA, I _{RR} = 0.1*I _R

Notes: 5. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
6. Short duration pulse test used to minimize self-heating effect.

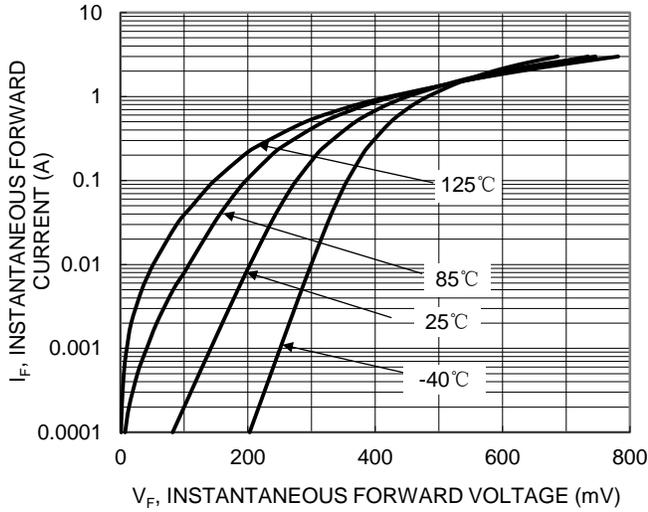


Fig.1 Typical Forward Characteristics

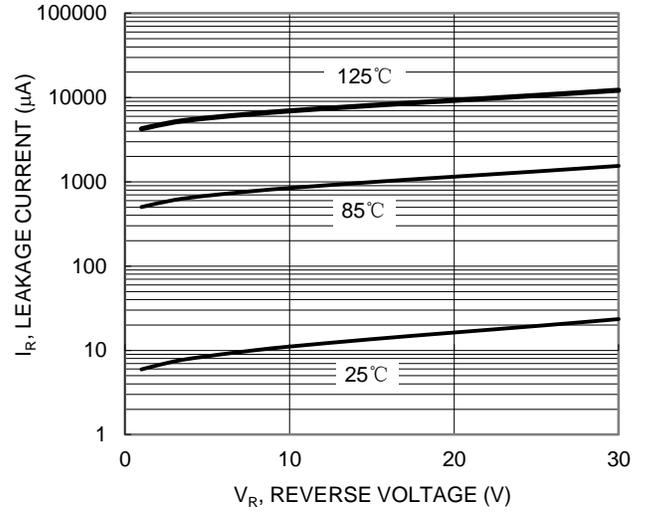


Fig.2 Typical Reverse Characteristics

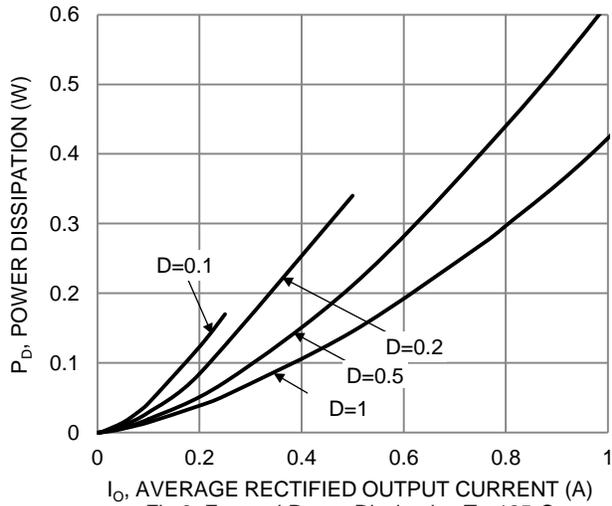


Fig.3. Forward Power Dissipation $T_j=125^\circ\text{C}$

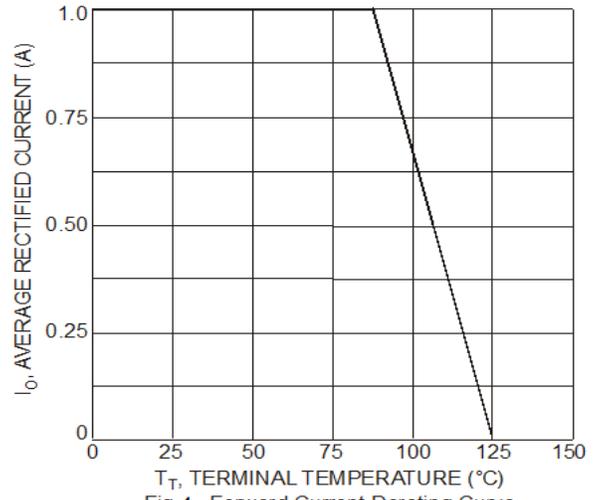


Fig.4 Forward Current Derating Curve

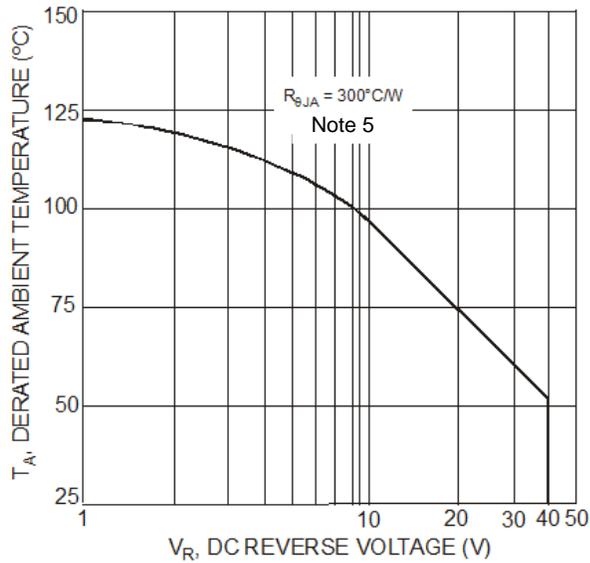


Fig. 5 Operating Temperature Derating

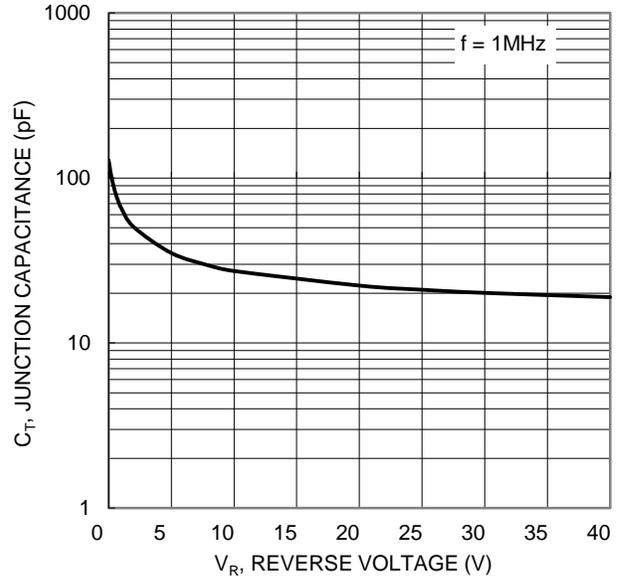


Fig. 6 Typical Junction Capacitance

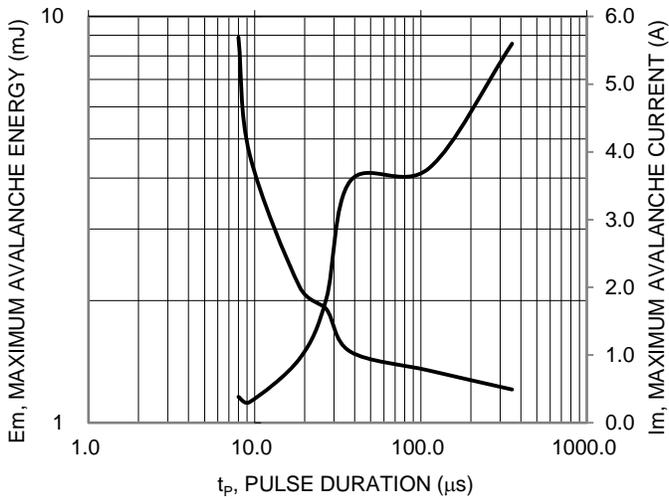


Fig. 7 Single Pulse Maximum Avalanche Energy and Current

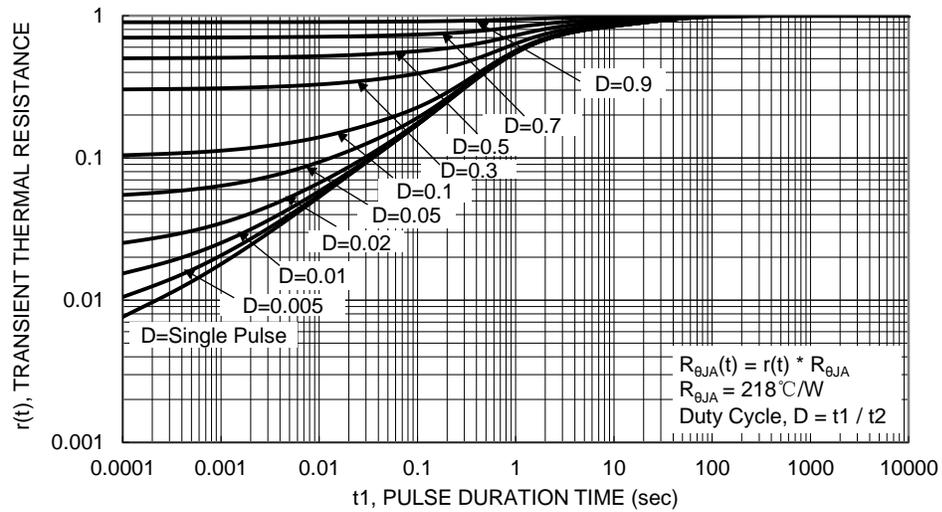
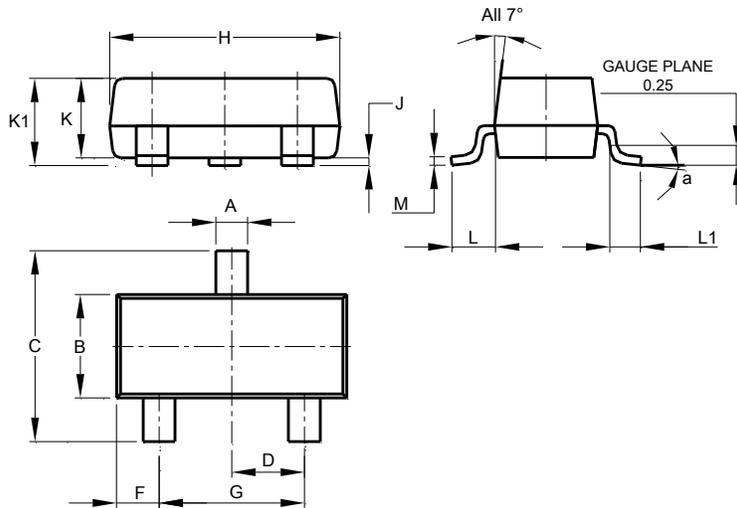


Fig. 8 Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23

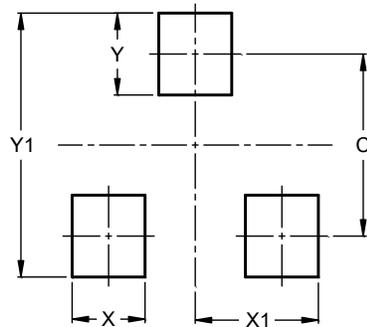


SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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