



Absolute Maximum Ratings

Parameter	Min.	Max.	Units
Storage Temperature	-65	150	°C
Ambient Temperature with Power Applied	-40	85	°C
Supply Voltage to Ground Potential (Inputs & V _{CC} Only)	-0.5	4.6	V
Supply Voltage to Ground Potential (Outputs & D/O Only)		4.6	V
DC Input Voltage	-0.5	5.5	V
DC Output Current	-	120	mA
Power Dissipation	-	0.5	W

Stress beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}$ C to $+85^{\circ}$ C, $V_{CC} = 3.3V \pm 10\%$)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
VIH	Input HIGH Voltage	Guaranteed Logic HIGH Level	2.0			V
VIL	Input LOW Voltage	Guaranteed Logic LOW Level	-0.5		0.8	V
I _{IH}	Input HIGH Current	$V_{CC} = Max., V_{IN} = V_{CC}$			±1	
I _{IL}	Input LOW Current	V _{CC} = Max., V _{IN} = GND			±1	μA
I _{OZH}	High Impedance Output Current	$0 \le A, B \le V_{CC}$			±1	
V _{IK}	Clamp Diode Voltage	$V_{CC} = Min., I_{IN} = -18 \text{ mA}$		-0.73	-1.2	V
_		V _{CC} = Min., V _{IN} = 0.0V, I _{ON} = 48mA		5	7	
R _{ON}	Switch On Resistance ⁽⁴⁾	V _{CC} = Min., V _{IN} = 2.4V, I _{ON} = 15mA		8	15	Ω

Capacitance ($T_A = 25^{\circ}C$, f = 1 MHz)

Parameters ⁽⁵⁾	Description	Test Conditions	Тур.	Units
C _{IN}	Input Capacitance	$V_{\rm IN} = 0V$	3.5	pF
C _{OFF}	A/B Capacitance, Switch Off	$V_{\rm IN} = 0V$	5.0	pF
C _{ON}	A/B Capacitance, Switch On	$V_{IN} = 0V$	10.0	pF

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at V_{CC} = 3.3V, T_A = 25°C ambient and maximum loading.

3. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

4. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.





Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	$V_{CC} = Max.$	V_{IN} = GND or V_{CC}		0.5	1.0	
ΔI_{CC}	Supply Current per Input HIGH	V _{CC} = Max.	$V_{\rm IN} = 3.0 V^{(3)}$			2.5	mA

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.

2. Typical values are at Vcc = 3.3V, +25°C ambient.

3. Per TTL driven input (control input only); A and B pins do not contribute to ΔI cc.

Switching Characteristics over 3.3V Operating Range

			P13C3	2X245	
			Com.		
Parameters	Description	Test Conditions	Min.	Max.	Units
t _{PLH} t _{PHL}	Propagation Delay ^(2,3) Ax to Bx	$C_{L} = 50 \text{ pF}$ $R_{L} = 500\Omega$		0.25	
t _{PZH} t _{PZL}	Bus Enable Time BE to Ax or Bx	$C_{L} = 50 \text{ pF}$ $R_{L} = 500\Omega$ $R = 500\Omega$	1.5	6.5	ns
t _{PHZ} t _{PLZ}	$\frac{Bus}{BE} \text{ to } Ax \text{ or } Bx$		1.5	5.5	

Switching Characteristics over 2.5V Operating Range

			P13C3	P13C32X245	
			Co	om.	_
Parameters	Description	Test Conditions	Min	Max	Units
t _{PLH} t _{PHL}	Propagation Delay ^(2,3) Ax to Bx	$C_L = 50 \text{ pF}$ $R_L = 500\Omega$		0.25	
t _{PZH} t _{PZL}	$\frac{Bus Enable Time}{BE to Ax or Bx}$	$C_{L} = 50 \text{ pF}$ $R_{L} = 500\Omega$ $R = 500\Omega$	1.5	9.8	ns
t _{PHZ} t _{PLZ}	$\frac{Bus}{BE} \text{ to } Ax \text{ or } Bx$		1.5	8.3	

Notes:

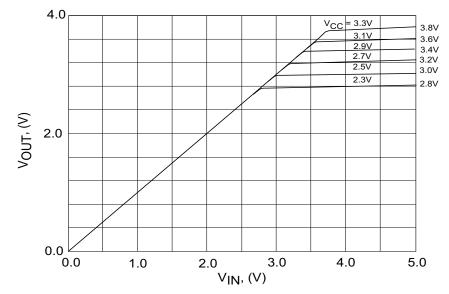
1. See test circuit and waveforms.

2. This parameter is guaranteed but not tested on Propagation Delays.

3. The bus switch contributes no propagational delay other than the RC delay of the On-Resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25 ns for 50 pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.







Output Voltage vs. Input Voltage over Various Supply Voltages

Part Marking

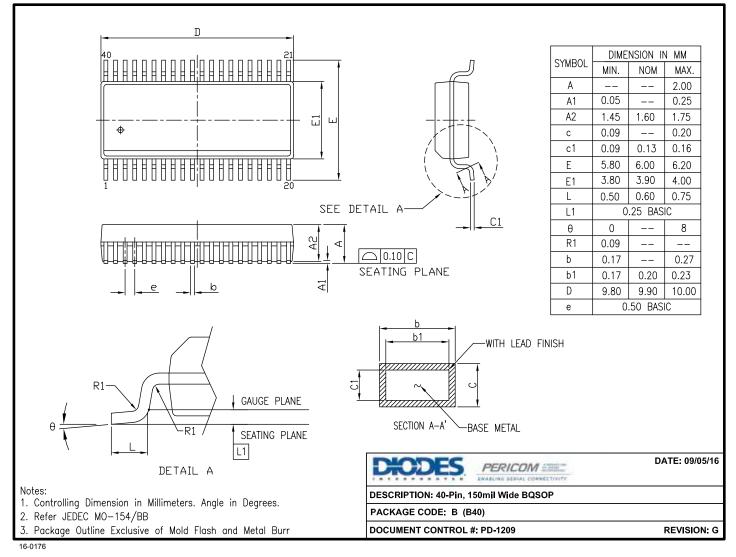


YY: Date Code (Year) WW: Date Code (Workweek) 1st X: Assembly Site Code 2nd X: Fab Site Code The Bar above "I" means Fab3 of MGN





Packaging Mechanical: 40-BQSOP (B)



For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Ordering Code	Package Code	Package Description
PI3C32X245BEX	В	40-pin, 150mil Wide (BQSOP)

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm

antimony compounds.

4. E = Pb-free and Green

5. X suffix = Tape/Reel





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