### UNCLAMPED COLLECTOR-TO-EMITTER AVALANCHE CHARACTERISTICS (-55° ≤ TJ ≤ 175°C)

Characteristic	Symbol	Value	Unit
$ \begin{array}{l} \mbox{Single Pulse Collector-to-Emitter Avalanche Energy} \\ V_{CC} = 50 \mbox{ V, } V_{GE} = 5.0 \mbox{ V, Pk } I_L = 22 \mbox{ A, } R_G = 1000 \ \Omega, \mbox{ L} = 1.8 \mbox{ mH}, \mbox{Starting } T_J = 25^\circ \mbox{C} \\ V_{CC} = 50 \mbox{ V, } V_{GE} = 5.0 \mbox{ V, Pk } I_L = 17 \mbox{ A, } R_G = 1000 \ \Omega, \mbox{ L} = 3.0 \mbox{ mH}, \mbox{Starting } T_J = 25^\circ \mbox{C} \\ V_{CC} = 50 \mbox{ V, } V_{GE} = 5.0 \mbox{ V, Pk } I_L = 19 \mbox{ A, } R_G = 1000 \ \Omega, \mbox{ L} = 1.8 \mbox{ mH}, \mbox{Starting } T_J = 25^\circ \mbox{C} \\ \end{array} $	E <sub>AS</sub>	435 433 325	mJ
Reverse Avalanche Energy V <sub>CC</sub> = 100 V, V <sub>GE</sub> = 20 V, Pk I <sub>L</sub> = 25.8 A, L = 6.0 mH, Starting T <sub>J</sub> = 25°C	E <sub>AS(R)</sub>	2000	mJ

# THERMAL CHARACTERISTICS

Thermal Resistance, Junction to Case			1.3	°C/W
Thermal Resistance, Junction to Ambient	DPAK (Note 1)	$R_{ hetaJA}$	95	°C/W
Maximum Lead Temperature for Soldering Purposes,	TL	275	°C	

1. When surface mounted to an FR4 board using the minimum recommended pad size.

### **ELECTRICAL CHARACTERISTICS**

Characteristic	Symbol	Test Conditions	Temperature	Min	Тур	Max	Unit
OFF CHARACTERISTICS			-	-			-
Collector-Emitter Clamp Voltage	BV <sub>CES</sub>	I <sub>C</sub> = 2.0 mA	$T_J = -40^{\circ}C$ to 150°C	380	395	420	V <sub>DC</sub>
		I <sub>C</sub> = 10 mA	$T_J = -40^{\circ}C$ to 150°C	390	405	430	]
Zero Gate Voltage Collector Current	I <sub>CES</sub>		T <sub>J</sub> = 25°C	-	1.5	5	μA <sub>DC</sub>
		V <sub>CE</sub> = 350 V, V <sub>GE</sub> = 0 V	T <sub>J</sub> = 150°C	-	10	30*	
		VGE - O V	$T_J = -40^{\circ}C$	-	0.5	2.5	
		V <sub>CE</sub> = 15 V, V <sub>GE</sub> = 0 V	T <sub>J</sub> = 25°C	-	_	2.0	]
Reverse Collector-Emitter Leakage Current	I <sub>ECS</sub>		T <sub>J</sub> = 25°C	-	0.7	1.0	mA
		$V_{CE} = -24 V$	T <sub>J</sub> = 150°C	-	12	25*	1
			$T_J = -40^{\circ}C$	-	0.1	1.0	
Reverse Collector-Emitter Clamp Voltage	B <sub>VCES(R)</sub>		$T_J = 25^{\circ}C$	27	33	37	V <sub>DC</sub>
		$I_{\rm C} = -75  {\rm mA}$	T <sub>J</sub> = 150°C	30	36	40	
			$T_J = -40^{\circ}C$	25	32	35	
Gate-Emitter Clamp Voltage	BV <sub>GES</sub>	l <sub>G</sub> = 5.0 mA	$T_J = -40^{\circ}C$ to 150°C	11	13	15	V <sub>DC</sub>
Gate-Emitter Leakage Current	I <sub>GES</sub>	V <sub>GE</sub> = 10 V	$T_J = -40^{\circ}C$ to 150°C	384	640	700	μA <sub>DC</sub>
Gate Emitter Resistor (Note 3)	$R_{GE}$	_	T <sub>J</sub> = -40°C to 150°C	10	16	26	kΩ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. \*Maximum Value of Characteristic across Temperature Range.

## **ELECTRICAL CHARACTERISTICS (continued)**

Characteristic	Symbol	Test Conditions	Temperature	Min	Тур	Мах	Unit
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V <sub>GE(th)</sub>		$T_J = 25^{\circ}C$	1.2	1.5	1.8	$V_{DC}$
		I <sub>C</sub> = 1.0 mA, V <sub>GE</sub> = V <sub>CE</sub>	$T_J = 150^{\circ}C$	0.8	1.0	1.3	
			$T_J = -40^{\circ}C$	1.4	1.7	2.0*	
Threshold Temperature Coefficient (Negative)	-	-	_	-	3.4	-	mV/°C
Collector-to-Emitter On-Voltage	V <sub>CE(on)</sub>		T <sub>J</sub> = 25°C	1.0	1.2	1.5	V <sub>DC</sub>
		I <sub>C</sub> = 6.0 A, V <sub>GE</sub> = 4.0 V	T <sub>J</sub> = 150°C	1.0	1.2	1.5	
		•GE = 1.0 •	$T_J = -40^{\circ}C$	1.0	1.2	1.5*	]
			$T_J = 25^{\circ}C$	1.2	1.4	1.6*	]
		I <sub>C</sub> = 8.0 A, V <sub>GE</sub> = 4.0 V	T <sub>J</sub> = 150°C	1.2	1.4	1.6	]
		I <sub>C</sub> = 10 A, V <sub>GE</sub> = 4.0 V	$T_J = -40^{\circ}C$	1.2	1.4	1.6*	]
			T <sub>J</sub> = 25°C	1.3	1.5	1.8	1
			T <sub>J</sub> = 150°C	1.3	1.5	1.9	
			$T_J = -40^{\circ}C$	1.3	1.6	1.8*	
			$T_J = 25^{\circ}C$	1.7	1.9	2.3	
		I <sub>C</sub> = 15 A, V <sub>GE</sub> = 4.0 V	$T_J = 150^{\circ}C$	1.9	2.2	2.5*	
		I <sub>C</sub> = 10 A, V <sub>GE</sub> = 4.5 V	$T_J = -40^{\circ}C$	1.5	1.9	2.3	
			$T_J = 25^{\circ}C$	1.3	1.5	1.8*	
			$T_J = 150^{\circ}C$	1.3	1.5	1.8*	
		UL -	$T_J = -40^{\circ}C$	1.3	1.5	1.8*	
		I <sub>C</sub> = 6.5 A, V <sub>GE</sub> = 3.7 V	T <sub>J</sub> = 25°C	-	-	1.65	
Forward Transconductance	gfs	$V_{CE} = 5.0 \text{ V}, \text{ I}_{C} = 6.0 \text{ A}$	T <sub>J</sub> = −40°C to 150°C	8.0	14	25	Mhos
YNAMIC CHARACTERISTICS (Note	9 3)		-	-	-	-	-
Input Capacitance	C <sub>ISS</sub>			400	800	1000	pF

Input Capacitance	CISS		T 4000 to	400	800	1000	рг
Output Capacitance	C <sub>OSS</sub>	V <sub>CC</sub> = 25 V, V <sub>GE</sub> = 0 V f = 1.0 MHz	T <sub>J</sub> = -40°C to 150°C	50	75	100	
Transfer Capacitance	C <sub>RSS</sub>			4.0	7.0	10	

## SWITCHING CHARACTERISTICS (Note 3)

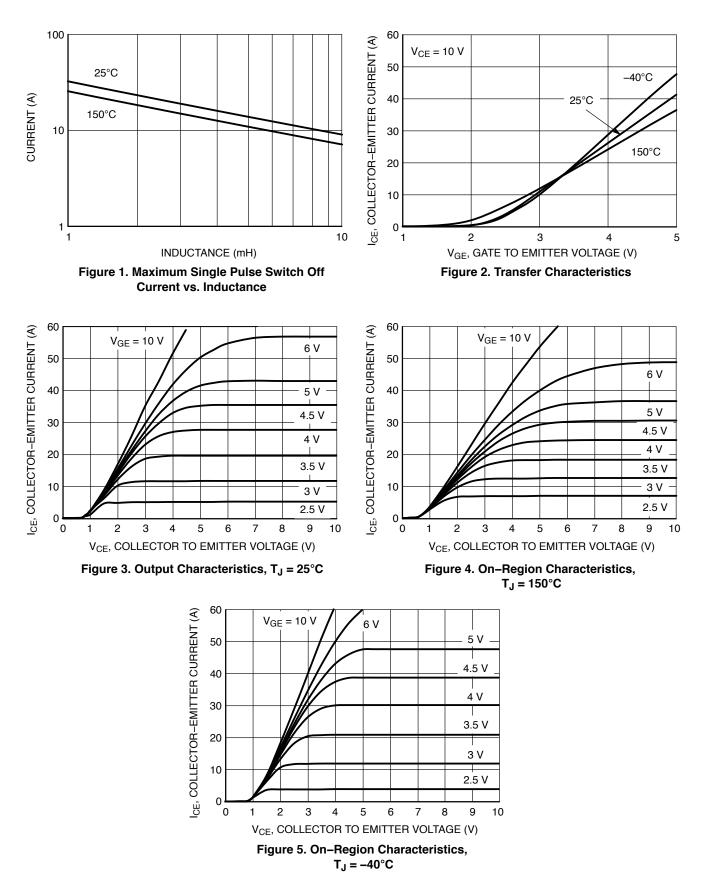
Turn-Off Delay Time (Resistive)	t <sub>d(off)</sub>		T <sub>J</sub> = 25°C	-	4.0	10	μSec
Fall Time (Resistive)	t <sub>f</sub>		T <sub>J</sub> = 25°C	-	9.0	15	
Turn-On Delay Time	t <sub>d(on)</sub>	$\label{eq:V_CC} \begin{array}{l} V_{CC} = 10 \; V, \; I_{C} = 6.5 \; A \\ R_{G} = 1.0 \; k\Omega, \; R_{L} = 1.5 \; \Omega \end{array}$	T <sub>J</sub> = 25°C	-	0.7	4.0	μSec
Rise Time	t <sub>r</sub>	$V_{CC}$ = 10 V, I <sub>C</sub> = 6.5 A R <sub>G</sub> = 1.0 kΩ, R <sub>L</sub> = 1.5 Ω	T <sub>J</sub> = 25°C	-	4.5	7.0	

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2. Pulse Test: Pulse Width  $\leq$  300 µS, Duty Cycle  $\leq$  2%.

3. Not production tested.

## **TYPICAL ELECTRICAL CHARACTERISTICS**



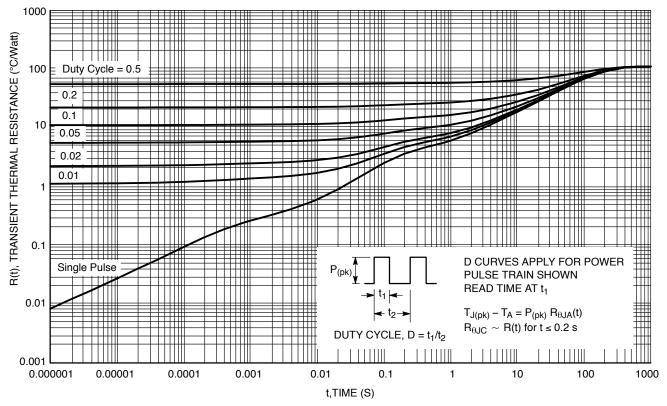
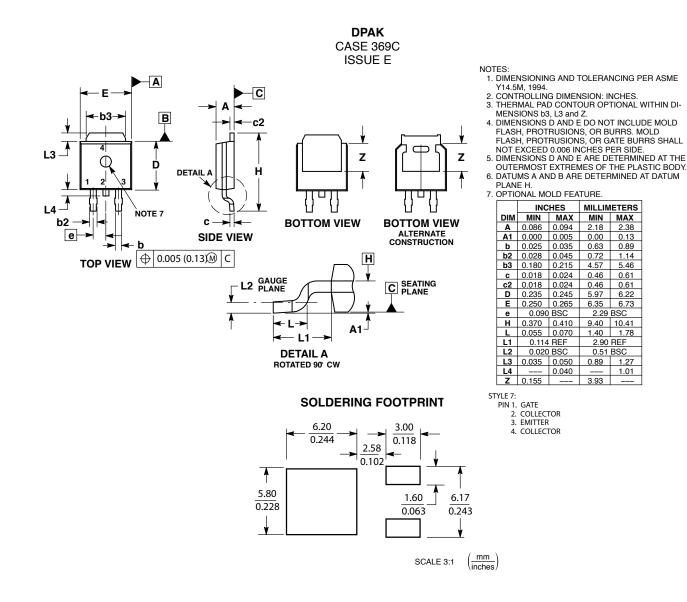


Figure 6. Transient Thermal Resistance (Non-normalized Junction-to-Ambient mounted on minimum pad area)

#### PACKAGE DIMENSIONS



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