

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)CES}$	Collector-to-Emitter Breakdown Voltage	1200	—	—	V	$V_{GE} = 0V, I_C = 250\mu A$
$V_{(BR)ECS}$	Emitter-to-Collector Breakdown Voltage ④	18	—	—	V	$V_{GE} = 0V, I_C = 1.0A$
$\Delta V_{(BR)CES}/\Delta T_J$	Temperature Coeff. of Breakdown Voltage	—	1.13	—	V/ $^\circ\text{C}$	$V_{GE} = 0V, I_C = 2.5mA$
$V_{CE(ON)}$	Collector-to-Emitter Saturation Voltage	—	3.17	4.3	V	$I_C = 5.0A, V_{GE} = 15V$
		—	4.04	—		$I_C = 11A, V_{GE} = 15V$
		—	2.84	—		$I_C = 5.0A, T_J = 150^\circ\text{C}$ See Fig.2, 5
$V_{GE(th)}$	Gate Threshold Voltage	3.5	—	6.5		$V_{CE} = V_{GE}, I_C = 250\mu A$
$\Delta V_{GE(th)}/\Delta T_J$	Temperature Coeff. of Threshold Voltage	—	-10	—	mV/ $^\circ\text{C}$	$V_{CE} = V_{GE}, I_C = 1mA$
g_{fe}	Forward Transconductance ⑤	2.3	3.5	—	S	$V_{CE} = 100V, I_C = 5.0A$
I_{CES}	Zero Gate Voltage Collector Current	—	—	250	μA	$V_{GE} = 0V, V_{CE} = 1200V$
		—	—	2.0		$V_{GE} = 0V, V_{CE} = 10V, T_J = 25^\circ\text{C}$
		—	—	1000		$V_{GE} = 0V, V_{CE} = 1200V, T_J = 150^\circ\text{C}$
I_{GES}	Gate-to-Emitter Leakage Current	—	—	± 100	nA	$V_{GE} = \pm 20V$

Switching Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
Q_g	Total Gate Charge (turn-on)	—	28	43	nC	$I_C = 5.0A$
Q_{ge}	Gate - Emitter Charge (turn-on)	—	4.4	6.6		$V_{CC} = 400V$ See Fig.8
Q_{gc}	Gate - Collector Charge (turn-on)	—	12	18		$V_{GE} = 15V$
$t_{d(on)}$	Turn-On Delay Time	—	23	—	ns	$T_J = 25^\circ\text{C}$ $I_C = 5.0A, V_{CC} = 960V$ $V_{GE} = 15V, R_G = 50\Omega$ Energy losses include "tail" See Fig. 9,10,14
t_r	Rise Time	—	26	—		
$t_{d(off)}$	Turn-Off Delay Time	—	93	140		
t_f	Fall Time	—	270	400		
E_{on}	Turn-On Switching Loss	—	0.45	—	mJ	See Fig. 9,10,14
E_{off}	Turn-Off Switching Loss	—	0.44	—		
E_{ts}	Total Switching Loss	—	0.89	1.2		
t_{sc}	Short Circuit Withstand Time	10	—	—	μs	$V_{CC} = 720V, T_J = 125^\circ\text{C}$ $V_{GE} = 15V, R_G = 50\Omega$
$t_{d(on)}$	Turn-On Delay Time	—	23	—	ns	$T_J = 150^\circ\text{C}$, $I_C = 5.0A, V_{CC} = 960$ $V_{GE} = 15V, R_G = 50\Omega$ Energy losses include "tail" See Fig. 10,11,14
t_r	Rise Time	—	28	—		
$t_{d(off)}$	Turn-Off Delay Time	—	100	—		
t_f	Fall Time	—	620	—		
E_{ts}	Total Switching Loss	—	1.7	—	mJ	
L_E	Internal Emitter Inductance	—	13	—	nH	Measured 5mm from package
C_{ies}	Input Capacitance	—	435	—	pF	$V_{GE} = 0V$ $V_{CC} = 30V$ See Fig. 7 $f = 1.0MHz$
C_{oes}	Output Capacitance	—	44	—		
C_{res}	Reverse Transfer Capacitance	—	8.3	—		

Notes:

- ① Repetitive rating; $V_{GE} = 20V$, pulse width limited by max. junction temperature. (See fig. 13b)
- ② $V_{CC} = 80\%(V_{CES})$, $V_{GE} = 20V$, $L = 10\mu H$, $R_G = 50\Omega$, (See fig. 13a)
- ③ Repetitive rating; pulse width limited by maximum junction temperature.
- ④ Pulse width $\leq 80\mu s$; duty factor $\leq 0.1\%$.
- ⑤ Pulse width $5.0\mu s$, single shot.

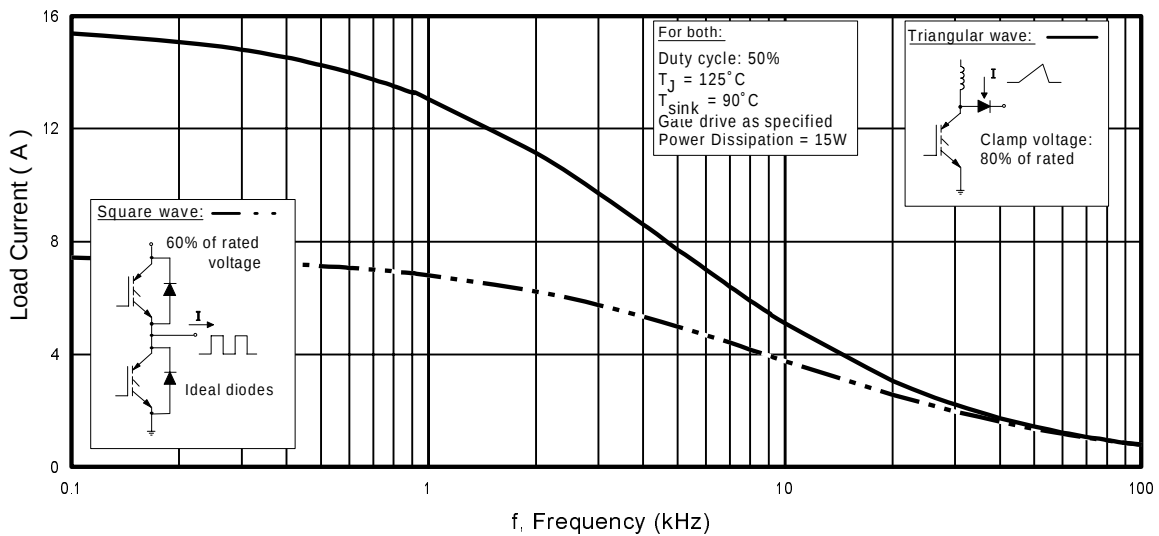


Fig. 1 - Typical Load Current vs. Frequency
(Load Current = I_{RMS} of fundamental)

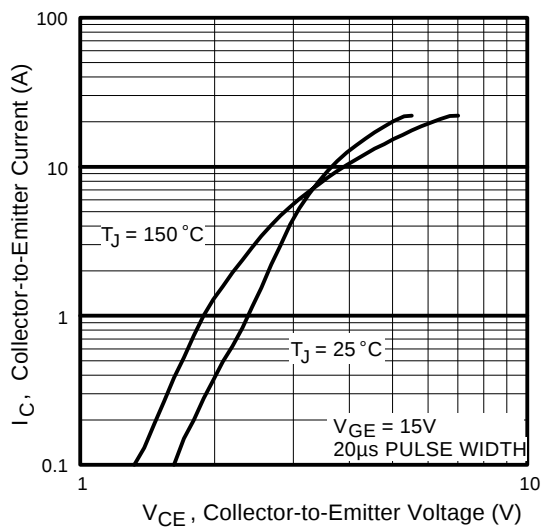


Fig. 2 - Typical Output Characteristics
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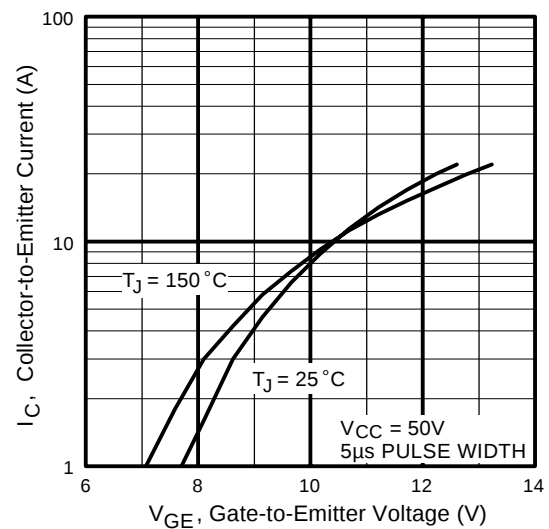


Fig. 3 - Typical Transfer Characteristics

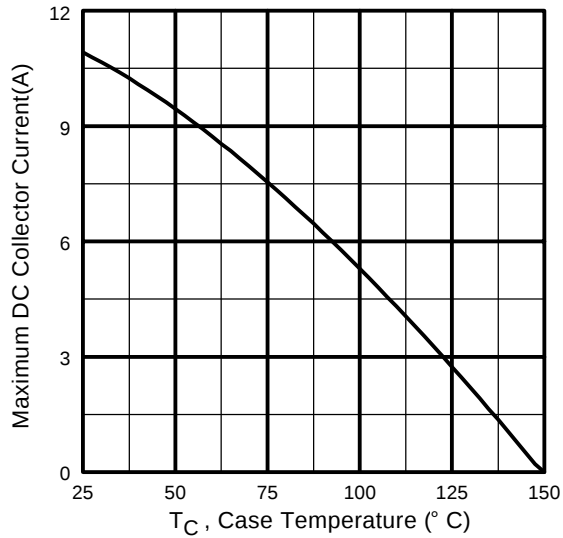


Fig. 4 - Maximum Collector Current vs. Case Temperature

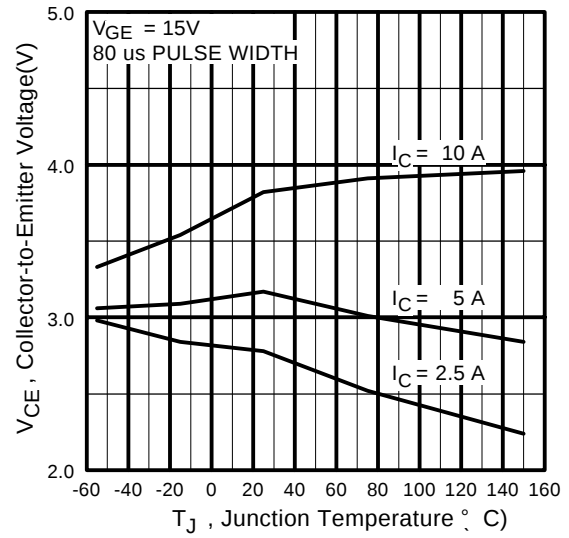


Fig. 5 - Typical Collector-to-Emitter Voltage vs. Junction Temperature

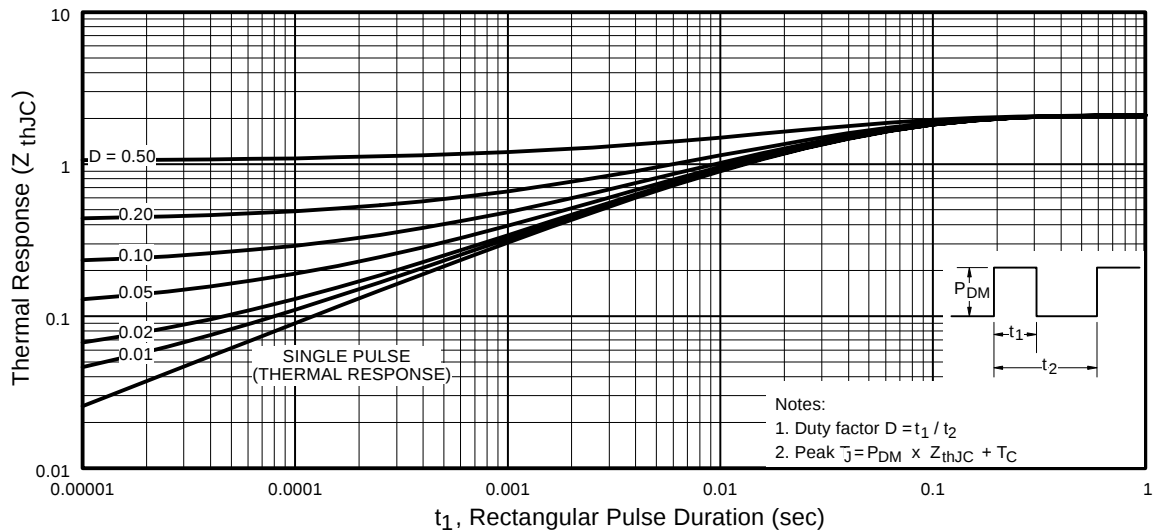


Fig. 6 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

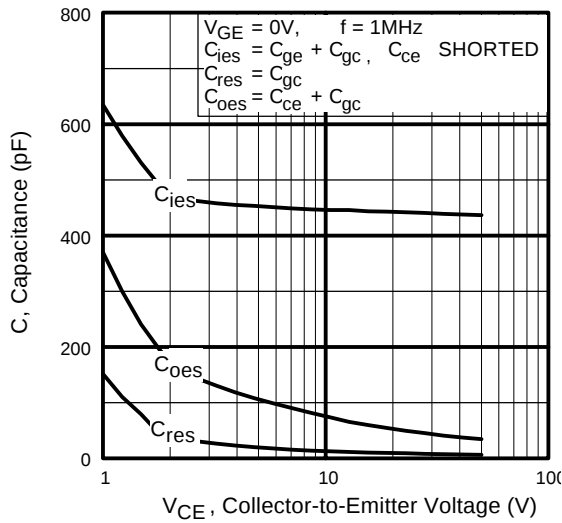


Fig. 7 - Typical Capacitance vs. Collector-to-Emitter Voltage

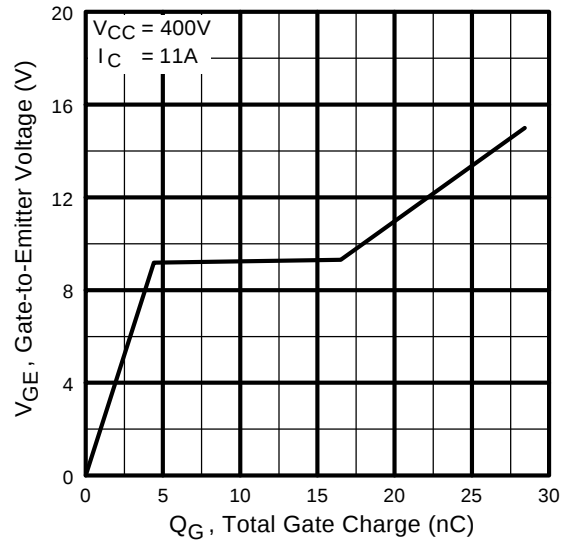


Fig. 8 - Typical Gate Charge vs. Gate-to-Emitter Voltage

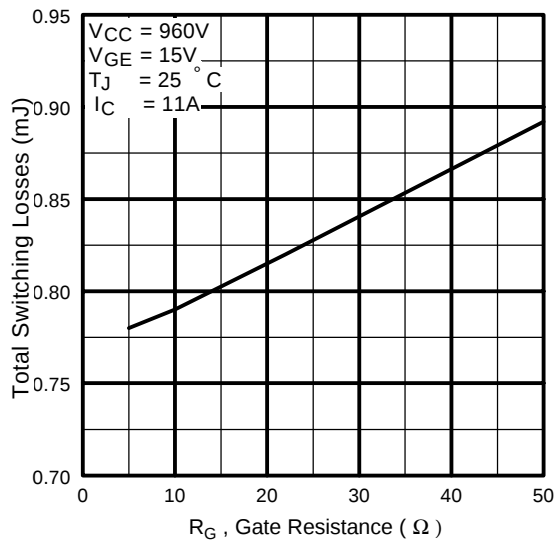


Fig. 9 - Typical Switching Losses vs. Gate Resistance

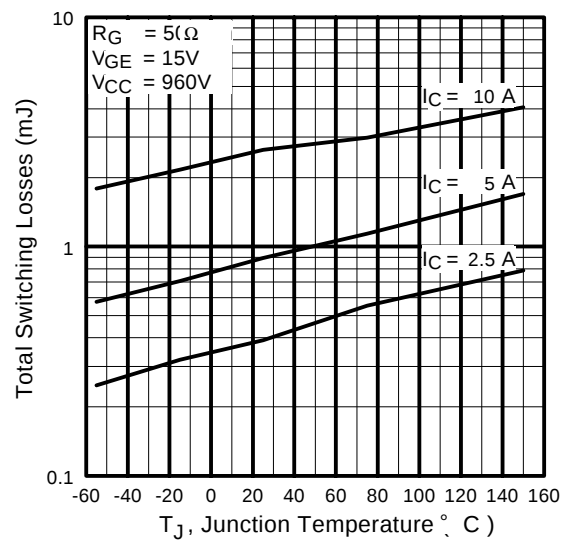


Fig. 10 - Typical Switching Losses vs. Junction Temperature

IRG4PH20K

International
IR Rectifier

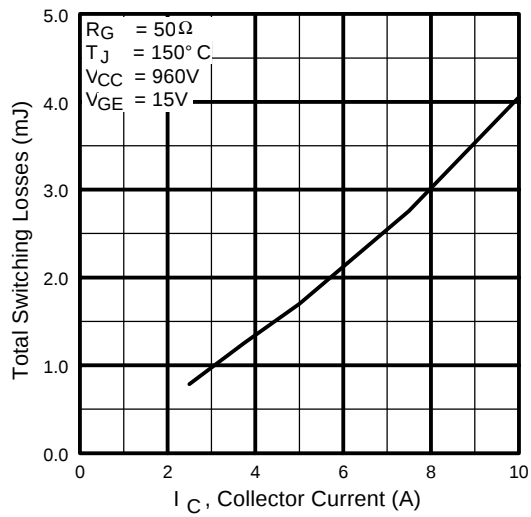


Fig. 11 - Typical Switching Losses vs. Collector-to-Emitter Current

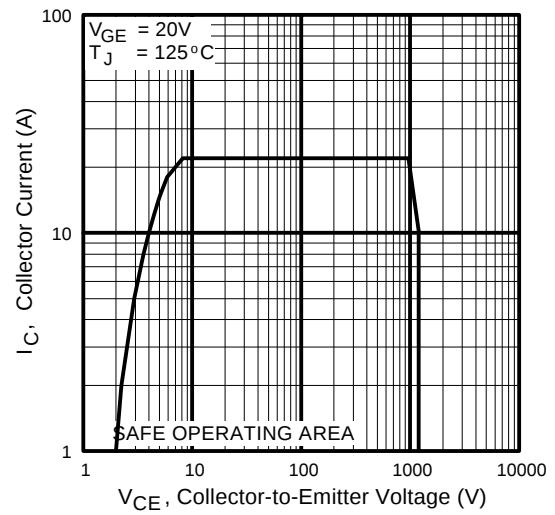


Fig. 12 - Turn-Off SOA

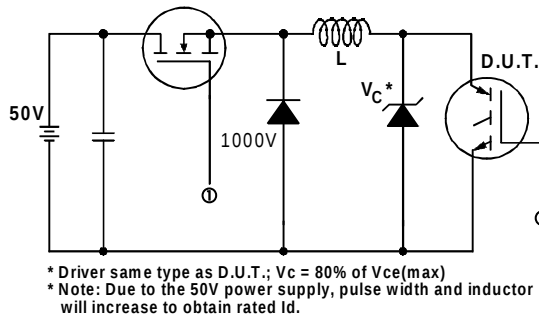


Fig. 13a - Clamped Inductive Load Test Circuit

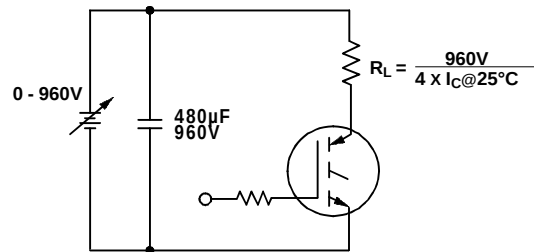


Fig. 13b - Pulsed Collector Current Test Circuit

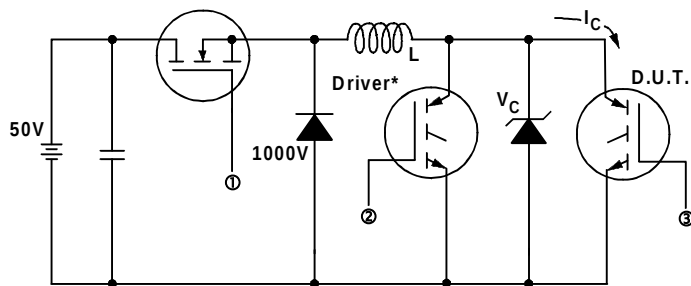


Fig. 14a - Switching Loss Test Circuit

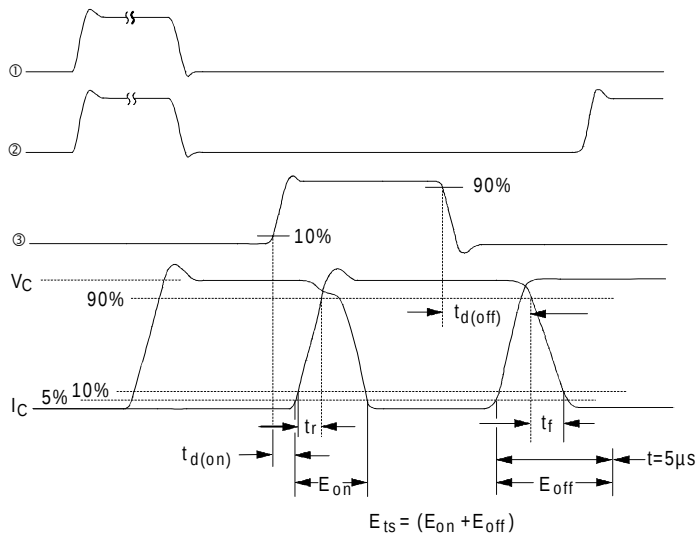
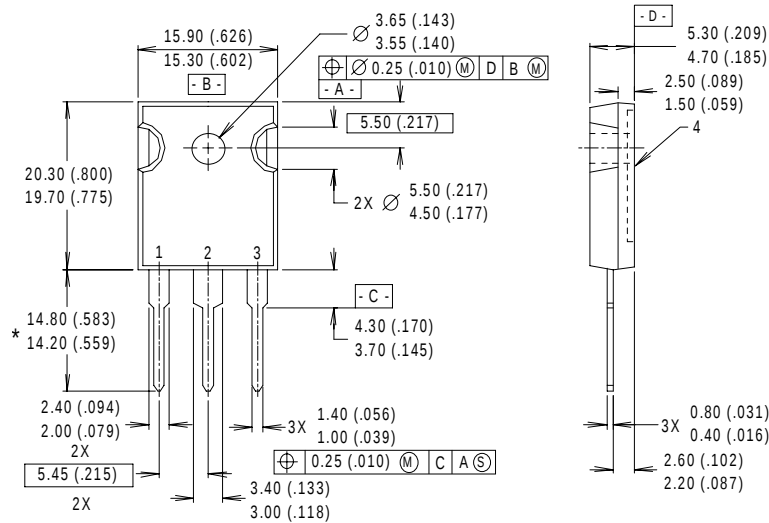


Fig. 14b - Switching Loss Waveforms

Case Outline and Dimensions — TO-247AC



NOTES:

- 1 DIMENSIONS & TOLERANCING PER ANSI Y14.5M, 1982.
- 2 CONTROLLING DIMENSION : INCH.
- 3 DIMENSIONS ARE SHOWN MILLIMETERS (INCHES).
- 4 CONFORMS TO JEDEC OUTLINE TO-247AC.

LEAD ASSIGNMENTS

- 1 - GATE
- 2 - COLLECTOR
- 3 - EMITTER
- 4 - COLLECTOR

* LONGER LEADED (20mm) VERSION AVAILABLE (TO-247AD) TO ORDER ADD "E" SUFFIX TO PART NUMBER

CONFORMS TO JEDEC OUTLINE TO-247AC (TO-3P)

Dimensions in Millimeters and (Inches)

International
IOR Rectifier

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EUROPEAN HEADQUARTERS: Hurst Green, Oxted, Surrey RH8 9BB, UK Tel: ++ 44 1883 732020

IR CANADA: 7321 Victoria Park Ave., Suite 201, Markham, Ontario L3R 2Z8, Tel: (905) 475 1897

IR GERMANY: Saalburgstrasse 157, 61350 Bad Homburg Tel: ++ 49 6172 96590

IR ITALY: Via Liguria 49, 10071 Borgaro, Torino Tel: ++ 39 11 451 0111

IR FAR EAST: K&H Bldg., 2F, 30-4 Nishi-Ikebukuro 3-Chome, Toshima-Ku, Tokyo Japan 171 Tel: 81 3 3983 0086

IR SOUTHEAST ASIA: 315 Outram Road, #10-02 Tan Boon Liat Building, Singapore 0316 Tel: 65 221 8371

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Note: For the most current drawings please refer to the IR website at:
<http://www.irf.com/package/>