Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 6) V_{GS} = -10V	Steady State	T_A = +25°C T_A = +70°C	I_D	-12 -10	A
	t<10s	T_A = +25°C T_A = +70°C	I_D	-22 -17	A
Continuous Drain Current (Note 6) V_{GS} = -4.5V	Steady State	T_A = +25°C T_A = +70°C	I_D	-10 -8	A
	t<10s	T_A = +25°C T_A = +70°C	I_D	-18 -14	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I_{DM}	-45	A
Maximum Body Diode Continuous Current			I_S	-4	A

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	T_A = +25°C	P_D	1.7	W
	T_A = +70°C		1.1	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	74	°C/W
	t<10s		22	
Total Power Dissipation (Note 6)	T_A = +25°C	P_D	2.2	W
	T_A = +70°C		1.4	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	56	°C/W
	t<10s		17	
Thermal Resistance, Junction to Case (Note 6)	Steady State	$R_{\theta JC}$	2.5	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1	µA	V_{DS} = -30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±1	µA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1.1	-1.6	-2.1	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	— —	6.3 7.9	7.5 10.2	mΩ	V_{GS} = -10V, I_D = -13A V_{GS} = -4.5V, I_D = -10A
Forward Transconductance	g_{fs}	—	26	—	S	V_{DS} = -15V, I_D = -13A
Diode Forward Voltage	V_{SD}	—	-0.7	-1.0	V	V_{GS} = 0V, I_S = -2.7A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	4965	—	pF	V_{DS} = -15V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{oss}	—	1487	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	711	—	pF	
Gate Resistance	R_G	—	7.3	—	Ω	V_{DS} = 0V, V_{GS} = 0V f = 1.0MHz
SWITCHING CHARACTERISTICS (Note 8)						
Total Gate Charge	Q_G	—	46	—	nC	V_{DS} = -15V, V_{GS} = -5V I_D = -13A
Gate-Source Charge	Q_{GS}	—	17	—		
Gate-Drain Charge	Q_{GD}	—	16	—		
Turn-On Delay Time	$t_{D(ON)}$	—	15	—	ns	V_{DS} = -15V, V_{GS} = -10V, I_D = -1A, R_G = 6.0Ω
Rise Time	t_R	—	9	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	160	—		
Fall Time	t_F	—	66	—		

Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

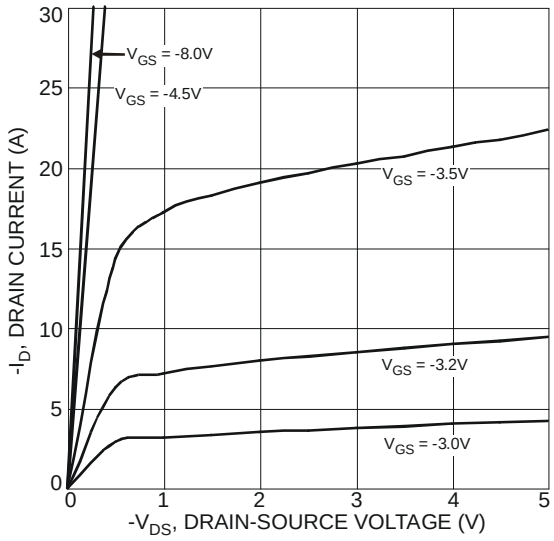


Fig. 1 Typical Output Characteristic

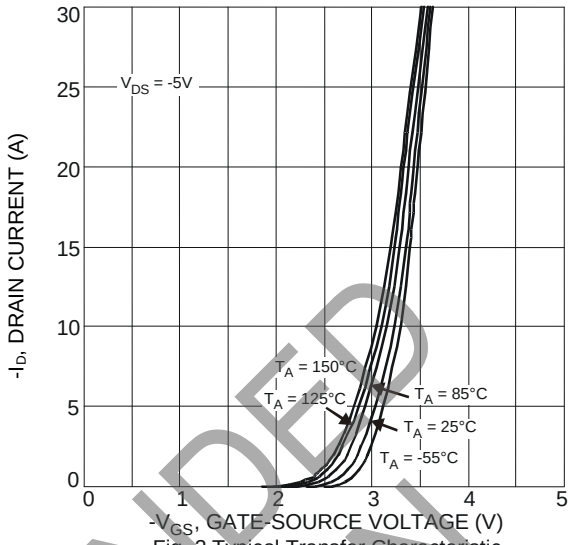


Fig. 2 Typical Transfer Characteristic

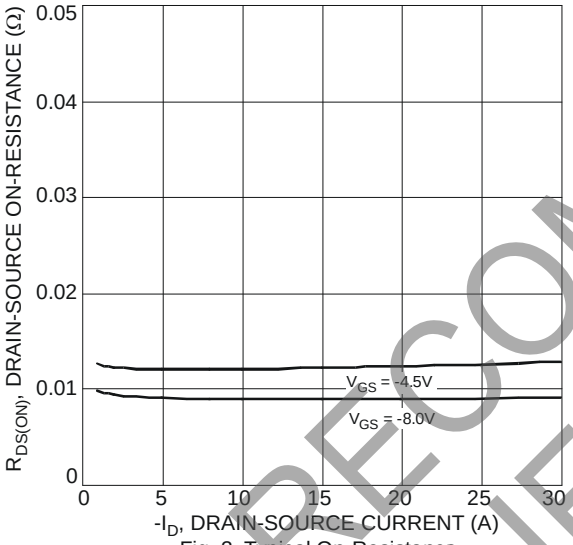


Fig. 3 Typical On-Resistance
vs. Drain Current and Gate Voltage

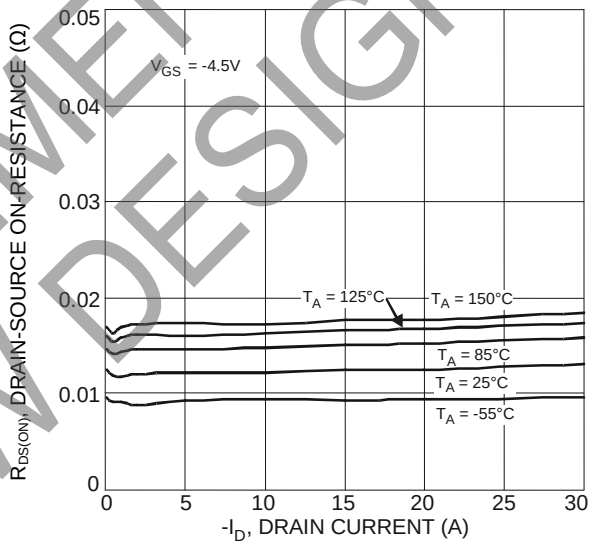


Fig. 4 Typical On-Resistance
vs. Drain Current and Temperature

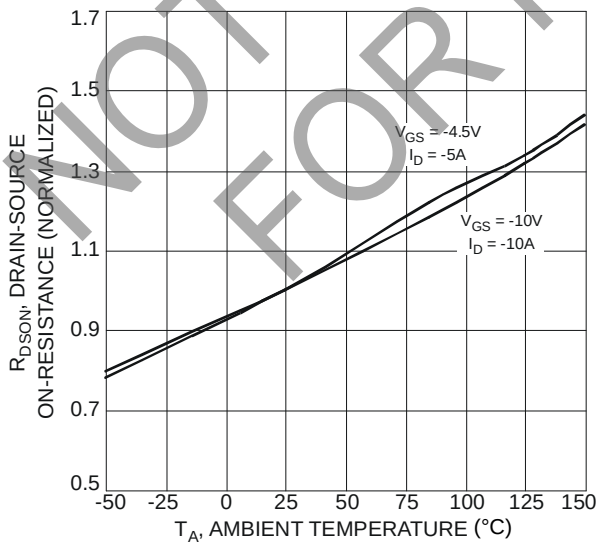


Fig. 5 On-Resistance Variation with Temperature

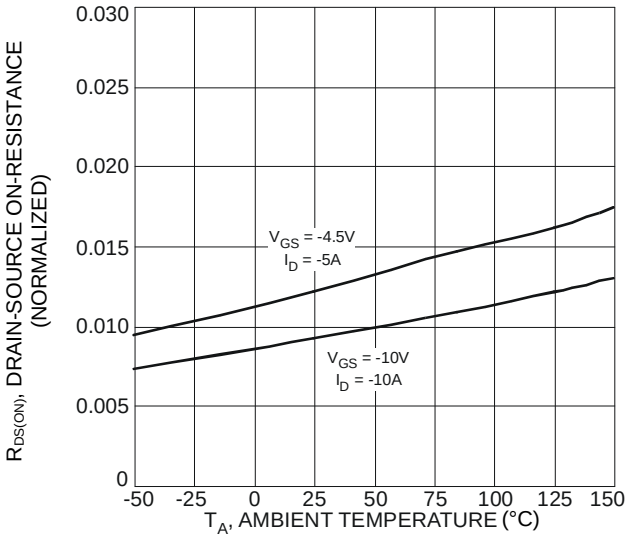


Fig. 6 On-Resistance Variation with Temperature

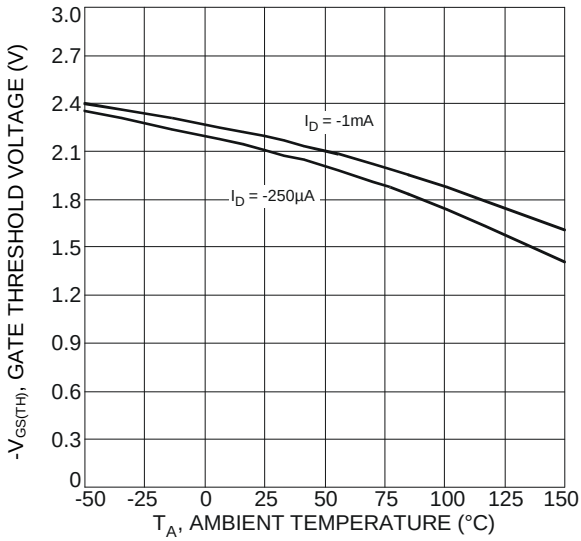


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

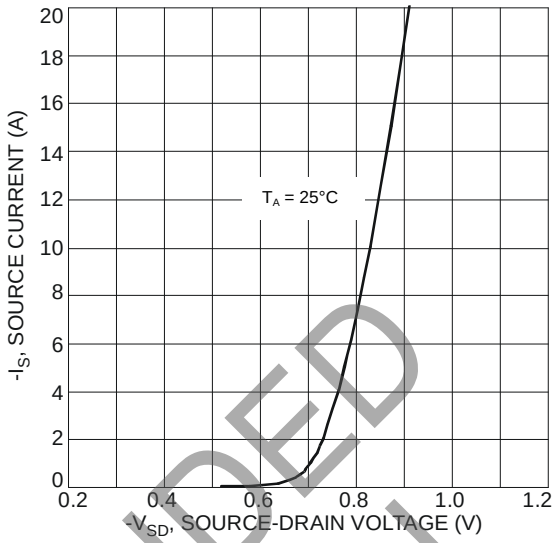


Fig. 8 Diode Forward Voltage vs. Current

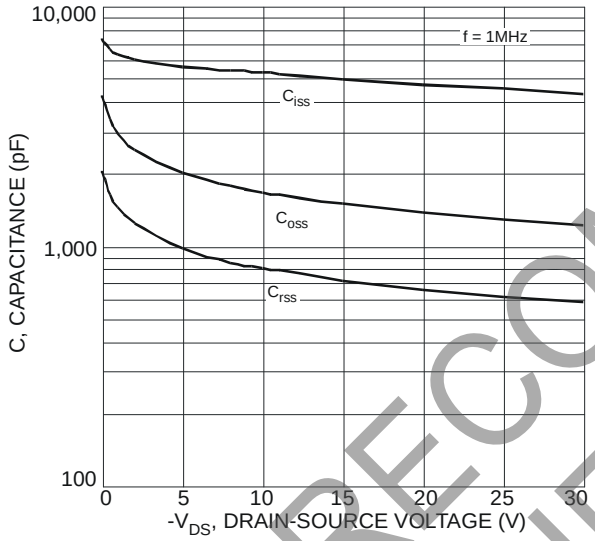


Fig. 9 Typical Total Capacitance

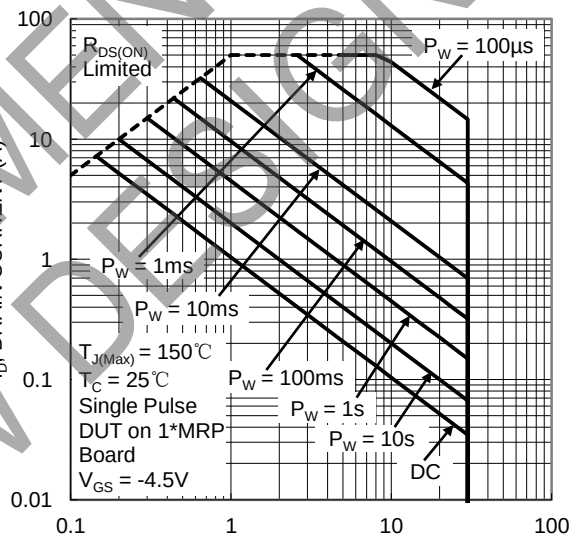
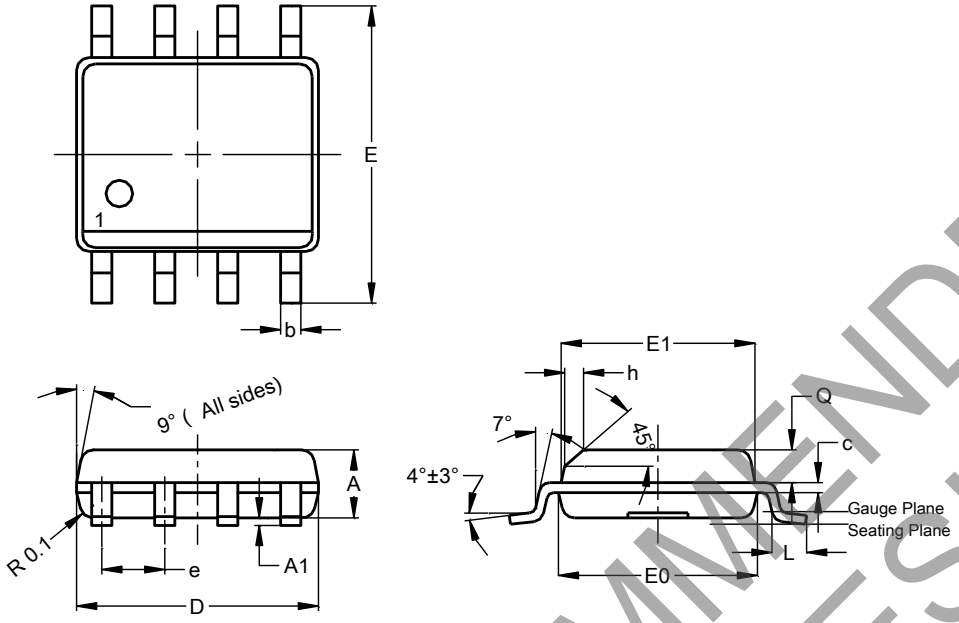


Fig. 10 SOA, Safe Operation Area

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8

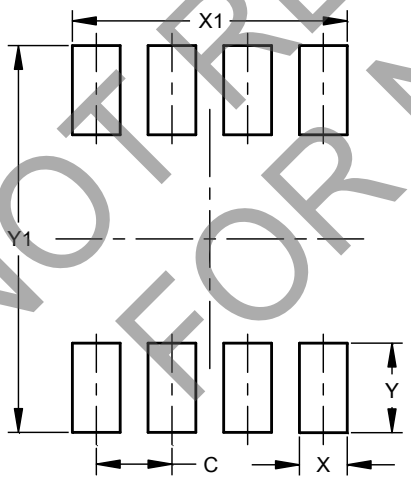


SO-8			
Dim	**Min**	**Max**	**Typ**
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	-	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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