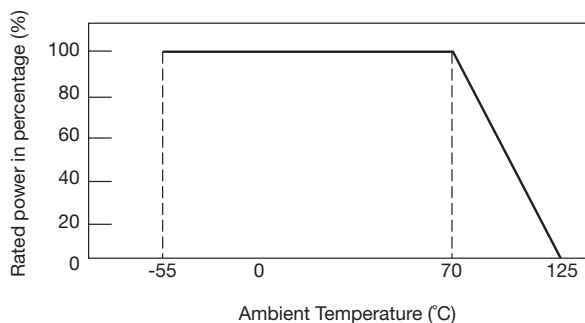


CAT/CAY 16 Series - Chip Resistor Arrays

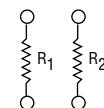
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Derating Curve

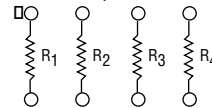


Schematics

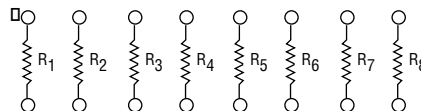
CAT16-J2
CAY16-J2



CAT16-F4, -J4
CAY16-F4, -J4



CAT16-F8, -J8
CAY16-J8

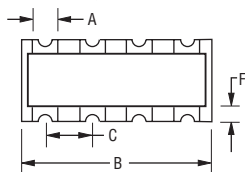


Dimensions

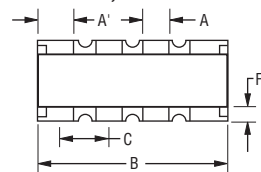
Model	A	A'	B	C	D	E	F
CAT16-F4	$\frac{0.40 \pm 0.15}{(.016 \pm .006)}$	—	$\frac{3.20 \pm 0.20}{(.126 \pm .008)}$	$\frac{0.80 \pm 0.10}{(.032 \pm .004)}$	$\frac{1.60 \pm 0.20}{(.063 \pm .008)}$	$\frac{0.50 \pm 0.10}{(.020 \pm .004)}$	$\frac{0.30 \pm 0.15}{(.012 \pm .006)}$
CAT16-J4	$\frac{0.40 \pm 0.15}{(.016 \pm .006)}$	—	$\frac{3.20 \pm 0.20}{(.126 \pm .008)}$	$\frac{0.80 \pm 0.10}{(.032 \pm .004)}$	$\frac{1.55 \pm 0.25}{(.061 \pm .0098)}$	$\frac{0.50 \pm 0.10}{(.020 \pm .004)}$	$\frac{0.30 \pm 0.20}{(.012 \pm .008)}$
CAY16-F4, -J4	$\frac{0.50 \pm 0.15}{(.020 \pm .006)}$	$\frac{0.70 \pm 0.10}{(.027 \pm .004)}$	$\frac{3.20 \pm 0.20}{(.126 \pm .008)}$	$\frac{0.80 \pm 0.05}{(.032 \pm .002)}$	$\frac{1.60 \pm 0.20}{(.063 \pm .008)}$	$\frac{0.50 \pm 0.10}{(.020 \pm .004)}$	$\frac{0.30 \pm 0.20}{(.012 \pm .008)}$
CAT16-J2	$\frac{0.40 \pm 0.15}{(.016 \pm .006)}$	—	$\frac{1.60 \pm 0.15}{(.063 \pm .006)}$	$\frac{0.80 \pm 0.05}{(.032 \pm .002)}$	$\frac{1.60 \pm 0.15}{(.063 \pm .006)}$	$\frac{0.60 \pm 0.15}{(.024 \pm .006)}$	$\frac{0.30 \pm 0.20}{(.012 \pm .008)}$
CAY16-J2	—	$\frac{0.60 \pm 0.15}{(.024 \pm .006)}$	$\frac{1.60 \pm 0.15}{(.063 \pm .006)}$	$\frac{0.76 \pm 0.10}{(.030 \pm .004)}$	$\frac{1.60 \pm 0.15}{(.063 \pm .006)}$	$\frac{0.45 \pm 0.15/-0.10}{(.018 \pm 0.006/-0.004)}$	$\frac{0.30 \pm 0.20}{(.012 \pm .008)}$
CAT16-F8, -J8	$\frac{0.40 \pm 0.15}{(.016 \pm .006)}$	—	$\frac{6.40 \pm 0.20}{(.252 \pm .008)}$	$\frac{0.80 \pm 0.15}{(.032 \pm .006)}$	$\frac{1.60 \pm 0.20}{(.063 \pm .008)}$	$\frac{0.60 \pm 0.15}{(.024 \pm .006)}$	$\frac{0.30 \pm 0.20}{(.012 \pm .008)}$
CAY16-J8	$\frac{0.30 \pm 0.15}{(.012 \pm .006)}$	$\frac{0.30 \pm 0.15}{(.012 \pm .006)}$	$\frac{3.80 \pm 0.20}{(.15 \pm .008)}$	$\frac{0.50 \pm 0.05}{(.02 \pm .002)}$	$\frac{1.60 \pm 0.20}{(.063 \pm .008)}$	$\frac{0.50 \pm 0.10}{(.020 \pm .004)}$	$\frac{0.30 \pm 0.15}{(.012 \pm .006)}$

Configurations

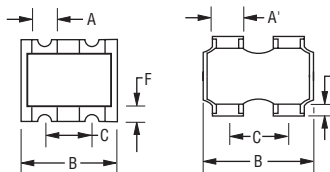
CAT16-F4, -J4



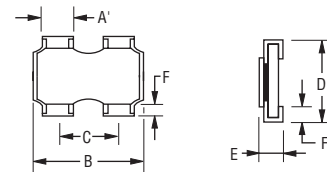
CAY16-F4, -J4



CAT16-J2

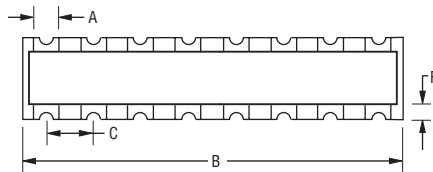


CAY16-J2

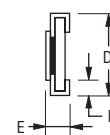
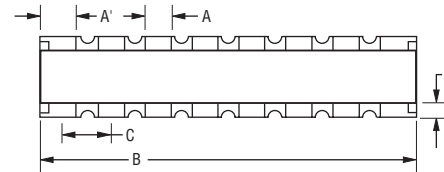


DIMENSIONS: $\frac{\text{MM}}{(\text{INCHES})}$

CAT16-F8, -J8



CAY16-J8



Specifications are subject to change without notice.

Users should verify actual device performance in their specific applications.

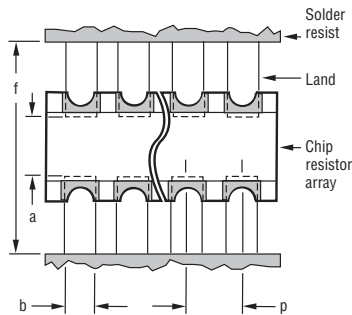
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CAT/CAY 16 Series - Chip Resistor Arrays

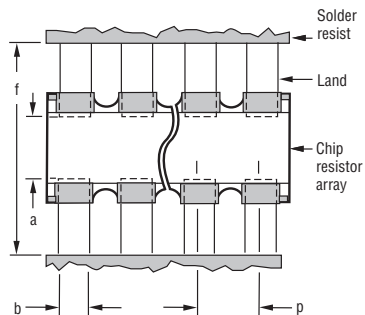
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Land Patterns

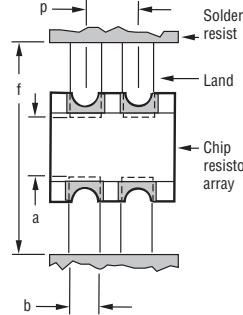
CAT16-F4, -J4, -F8, -J8



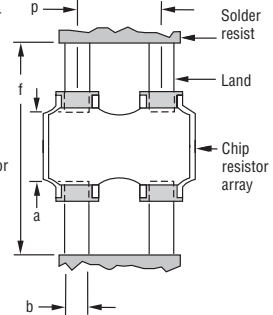
CAY16-F4, -J4, -J8



CAT16-J2

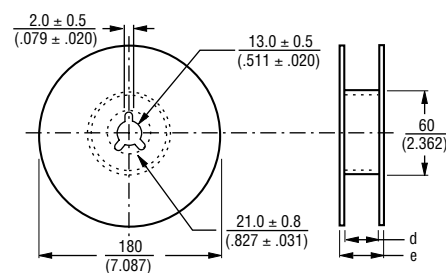
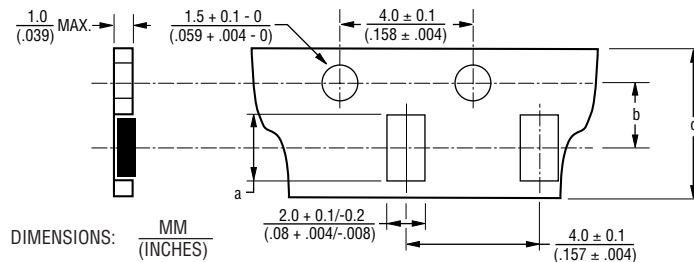


CAY16-J2



Model	a	b	p	f
CAT16-F4, -J4, -F8, -J8	$\frac{0.7 \text{ to } 0.9}{(.028 \text{ to } .035)}$	$\frac{0.4 \text{ to } 0.45}{(.016 \text{ to } .0178)}$	$\frac{0.80}{(.032)}$	$\frac{2.2 \text{ to } 2.6}{(.087 \text{ to } .102)}$
CAY16-F4, -J4	$\frac{0.7 \text{ to } 0.9}{(.028 \text{ to } .035)}$	$\frac{0.4 \text{ to } 0.45}{(.016 \text{ to } .0178)}$	$\frac{0.80}{(.032)}$	$\frac{2.4 \text{ to } 2.8}{(.094 \text{ to } .11)}$
CAY16-J8	$\frac{0.7 \text{ to } 0.9}{(.028 \text{ to } .035)}$	$\frac{0.3 \text{ to } 0.35}{(.012 \text{ to } .014)}$	$\frac{0.50}{(.020)}$	$\frac{2.0 \text{ to } 2.2}{(.079 \text{ to } .087)}$
CAT16-J2	$\frac{0.7 \text{ to } 0.9}{(.028 \text{ to } .035)}$	$\frac{0.4 \text{ to } 0.45}{(.016 \text{ to } .0178)}$	$\frac{0.80}{(.032)}$	$\frac{2.2 \text{ to } 2.6}{(.087 \text{ to } .102)}$
CAY16-J2	$\frac{0.7 \text{ to } 0.9}{(.028 \text{ to } .035)}$	$\frac{0.4 \text{ to } 0.5}{(.016 \text{ to } .020)}$	$\frac{0.80}{(.032)}$	$\frac{2.0 \text{ to } 2.6}{(.079 \text{ to } .102)}$

Packaging Dimensions



Model	a	b	c	d	e
CAT16-F4, -J4 & CAY16-F4, J4	$\frac{3.60 \pm 0.20}{(.142 \pm .008)}$	$\frac{3.50 \pm .005}{(.138 \pm .004)}$	$\frac{8.0 \pm 0.3}{(.315 \pm .012)}$	$\frac{9.0 \pm 0.3}{(.354 \pm .012)}$	$\frac{11.4 \pm 1.0}{(.449 \pm .040)}$
CAT16-J2 & CAY16-J2	$\frac{1.80 \pm 0.10}{(.070 \pm .004)}$	$\frac{3.50 \pm .005}{(.138 \pm .004)}$	$\frac{8.0 \pm 0.3}{(.315 \pm .012)}$	$\frac{9.0 \pm 0.3}{(.354 \pm .012)}$	$\frac{11.4 \pm 1.0}{(.449 \pm .040)}$
CAT16-F8, -J8	$\frac{6.90 \pm 0.20}{(.272 \pm .008)}$	$\frac{5.50 \pm 0.10}{(.217 \pm .004)}$	$\frac{12.0 \pm 0.2}{(.472 \pm .008)}$	$\frac{13.0 \pm 0.2}{(.512 \pm .008)}$	$\frac{15.4 \pm 1.0}{(.606 \pm .040)}$
CAY16-J8	$\frac{4.10 \pm 0.15}{(.161 \pm .012)}$	$\frac{3.50 \pm 0.05}{(.138 \pm .002)}$	$\frac{8.0 \pm 0.3}{(.315 \pm .012)}$	$\frac{9.0 \pm 0.3}{(.354 \pm .012)}$	$\frac{11.4 \pm 1.0}{(.449 \pm .040)}$

- 5,000 pcs. per reel (J2, J4, CAY16-J8)
- 4,000 pcs. per reel (CAT16-F8, -J8)
- Paper tape

REV. 12/20

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Users should verify actual device performance in their specific applications.

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Chip Resistor Arrays - Application Note

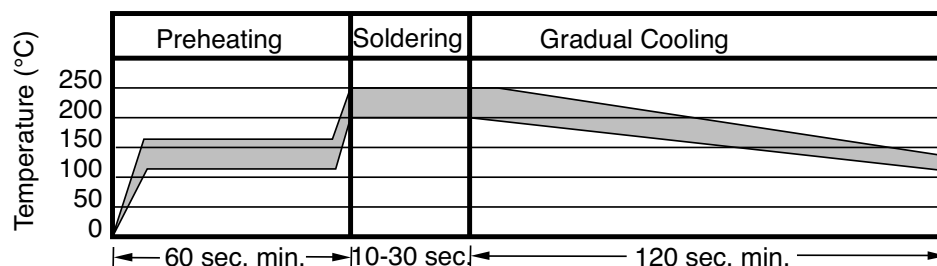
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Component Placement

- Reduce the mechanical stress to a minimum during and after placing of the unit in order not to damage the terminals and protective coating.
- Misplacement of components may cause solder bridges.

Soldering

- Reflow soldering: Recommendation is shown in the following chart.
- Wave soldering: Recommendation according to IEC standards.
- Hand soldering: Don't touch the protective coating of the part. Solder within 3 seconds when the temperature is over 280 °C.



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