

#### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V <sub>DSS</sub>	-100	V
Gate-Source Voltage		V <sub>GSS</sub>	±20	V
	T <sub>A</sub> = +25°C (Note 6)		-5.9	
Continuous Drain Current	$T_{A} = +70^{\circ}C$ (Note 6)	I <sub>D</sub>	-4.7	А
	$T_{A} = +25^{\circ}C$ (Note 5)		-3.8	
Pulsed Drain Current (Note 7)		I <sub>DM</sub>	-21.1	А
Continuous Source Current (Body Diode) (Note 6)		Is	-10	А
Pulsed Source Current (Body Diode) (Note 7)		I <sub>SM</sub>	-21.1	А

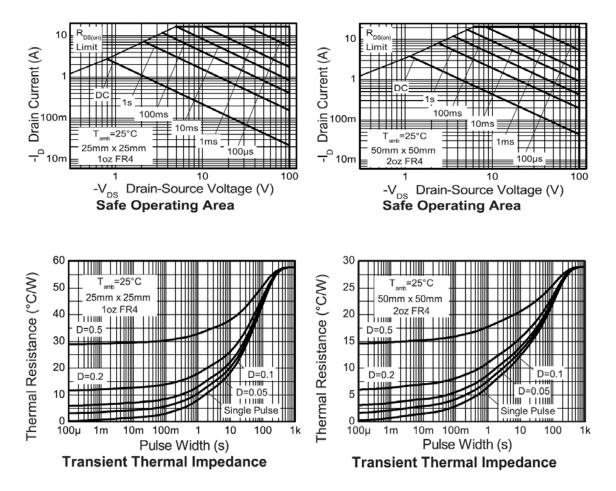
### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
	T <sub>A</sub> = +25°C (Note 5)		4.3	W	
		PD	34.4	mW/°C	
Total Power Dissipation (Note 5) Linear Derating Factor	T <sub>A</sub> = +25°C (Note 6)		10.2	W	
			81.3	mW/°C	
	T <sub>A</sub> = +25°C (Note 8)		2.17	W	
			17.4	mW/°C	
	(Note 5)		29		
Thermal Resistance, Junction to Ambient	(Note 6)	R <sub>eJA</sub>	12.3	°C/W	
	(Note 8)	0.	57.6		
Operating and Storage Temperature Range		T <sub>J.</sub> T <sub>STG</sub>	-55 to +150	°C	

Notes: 5. For a device surface mounted on 50mm x 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions. 6. For a device surface mounted on FR4 PCB measured at t ≤10 sec.

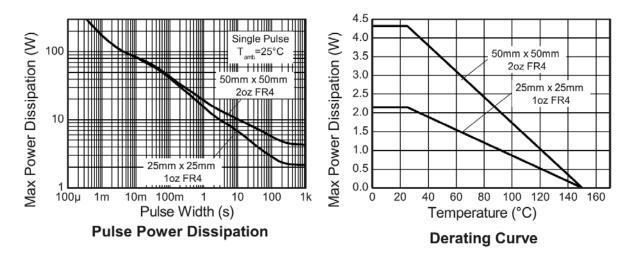
7. Repetitive rating on 50mm x 50mm x 1.6mm FR4 PCB, D=0.02, pulse width=300µs – pulse width limited by maximum junction temperature.
8. For a device surface mounted on 25mm x 25mm x 1.6mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

#### Thermal characteristics



ZXMP10A18K Document number: DS33599 Rev. 2 - 2 Downloaded from Arrow.com.





Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS					•		
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-100		_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_		-1	μA	$V_{DS} = -100V, V_{GS} = 0V$	
Gate-Source Leakage	IGSS	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-2	_	-4	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
Static Drain-Source On-Resistance (Note 9)	P		_	150 190	mΩ	$V_{GS} = -10V, I_D = -2.8A$	
Static Drain-Source On-Resistance (Note 9)	R <sub>DS (ON)</sub>					$V_{GS} = -6V, I_D = -2.4A$	
Forward Transconductance (Notes 9 & 11)	<b>g</b> fs		6		S	$V_{DS} = -15V, I_{D} = -2.8A$	
DYNAMIC CHARACTERISTICS (Note 11)							
Input Capacitance	Ciss	_	1055		pF	− V <sub>DS</sub> = -50V, V <sub>GS</sub> = 0V, − f = 1MHz	
Output Capacitance	Coss	_	90	—	pF		
Reverse Transfer Capacitance	C <sub>rss</sub>		76		pF		
SWITCHING CHARACTERISTICS (Notes 10 &	11)						
Turn-On Delay Time	t <sub>d(on)</sub>		4.9	—			
Rise Time	tr	_	6.8			$\label{eq:VDS} \begin{array}{l} V_{DS} = -50V, \ V_{GS} = -10V, \\ I_D = -1A, \ R_G = 6\Omega \end{array}$	
Turn-On Delay Time	t <sub>d(off)</sub>		33.9	_	ns		
Rise Time	t <sub>f</sub>	_	17.9	_			
Total Gate Charge	Qg	_	26.9	_			
Gate-Source Charge	Q <sub>gs</sub>		3.9		nC	$V_{DS} = -50V, V_{GS} = -10V,$	
Gate-Drain Charge	Q <sub>qd</sub>		10.2	_		$I_{\rm D} = -2.8 {\rm A}$	
SOURCE-DRAIN DIODE CHARACTERISTICS							
Diode Forward Voltage (Note 9)	V <sub>SD</sub>		-0.85	-0.95	V	$T_J = +25^{\circ}C$ , $V_{GS} = 0V$ , $I_S = -3.5A$	
Reverse Recovery Time (Note 11)	t <sub>rr</sub>		49		ns	T <sub>J</sub> = +25°C, I <sub>S</sub> = -2.8A, di/dt=100A/µs,	
Reverse Recovery Charge (Note 11)	Q <sub>rr</sub>	_	107		nC		

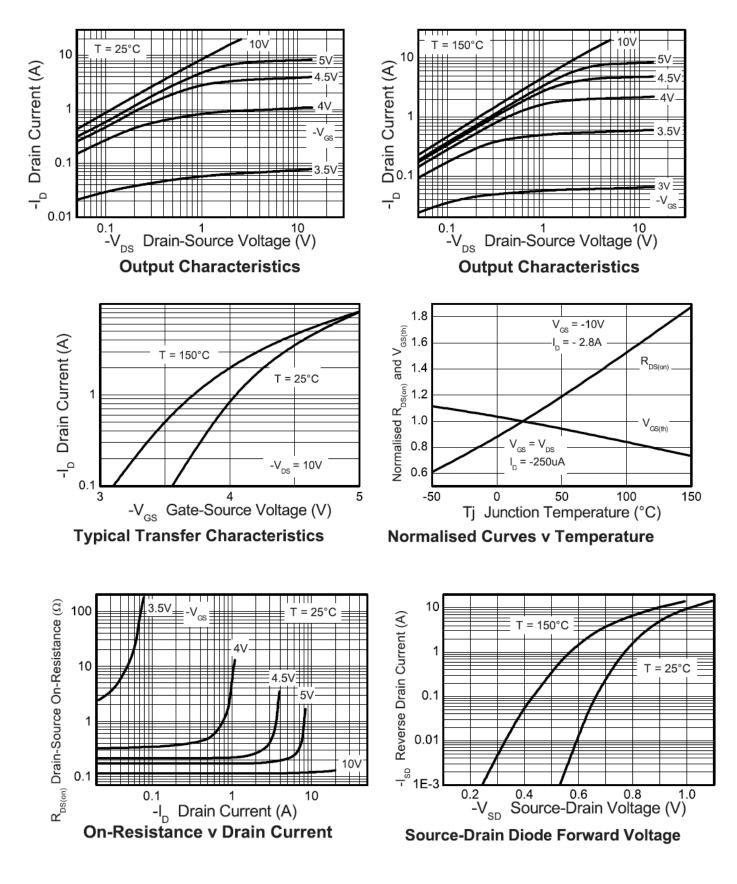
Notes:

9. Measured under pulsed conditions. Pulse width  $\leq$  300µs; duty cycle  $\leq$  2%. 10. Switching characteristics are independent of operating junction temperature.

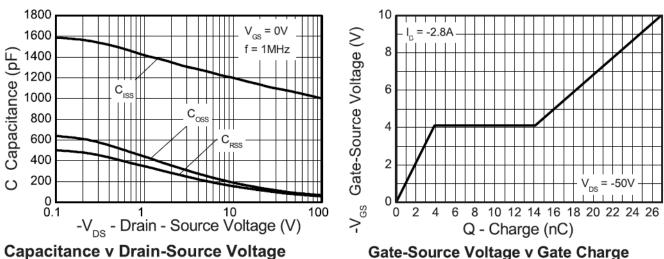
11. For design aid only, not subject to production testing.



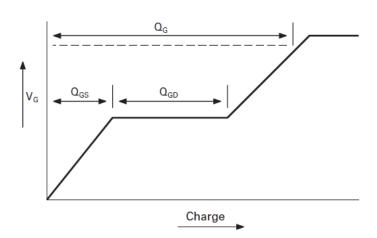
# **Typical characteristics**



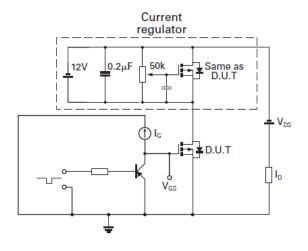




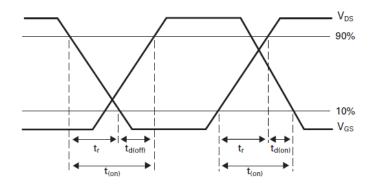
Gate-Source Voltage v Gate Charge



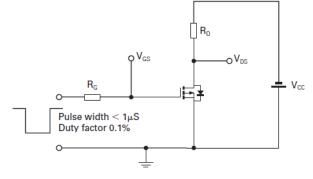
## Basic gate charge waveform



Gate charge test circuit



## Switching time waveforms

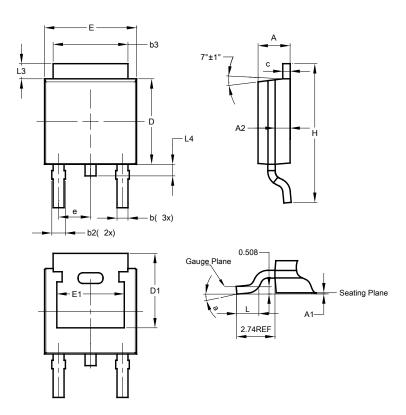


# Switching time test circuit



## Package Outline Dimensions

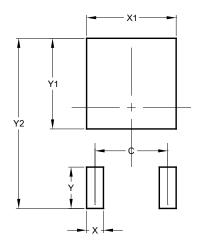
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	TO252 (DPAK)				
Dim	Min	Max	Тур		
Α	2.19	2.39	2.29		
A1	0.00	0.13	0.08		
A2	0.97	1.17	1.07		
b	0.64	0.88	0.783		
b2	0.76	1.14	0.95		
b3	5.21	5.46	5.33		
С	0.45	0.58	0.531		
D	6.00	6.20	6.10		
D1	5.21	-	-		
е	-	-	2.286		
Е	6.45	6.70	6.58		
E1	4.32	-	-		
Н	9.40	10.41	9.91		
L	1.40	1.78	1.59		
L3	0.88	1.27	1.08		
L4	0.64	1.02	0.83		
а	0°	10°	-		
All Dimensions in mm					

### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)		
С	4.572		
Х	1.060		
X1	5.632		
Y	2.600		
Y1	5.700		
Y2	10.700		



#### **IMPORTANT NOTICE**

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

#### LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
  - 1. are intended to implant into the body, or
  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2014, Diodes Incorporated

www.diodes.com