Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
C <sub>ISS</sub>	Input Capacitance	V <sub>GS</sub> = 0V		400		
C <sub>oss</sub>	Output Capacitance	V <sub>DS</sub> = 28V		375		pF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz		50		

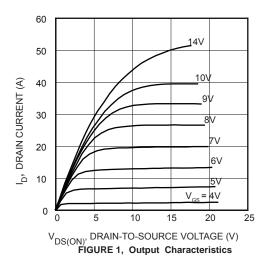
#### **Functional Characteristics**

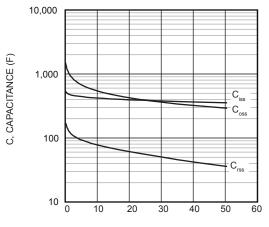
Symbol	Parameter	Min	Тур	Max	Unit
G <sub>PS</sub>	f = 175MHz,- V <sub>DD</sub> = 28V, I <sub>DQ</sub> = 500mA, P <sub>out</sub> = 300W	12	14		dB
$\eta_{\scriptscriptstyle D}$	f = 175MHz, V <sub>DD</sub> = 28V, I <sub>DQ</sub> = 500mA, P <sub>out</sub> = 300W	45	55		%
Ψ	$f = 175MHz$ , $V_{DD} = 28V$ , $I_{DQ} = 500mA$ , $P_{out} = 300W 5:1VSWR - All Phase Angles$	No Degradation in Output Power		Power	

<sup>1.</sup> To MIL-STD-1311 Version A, test method 2204B, Two Tone, Reference Each Tone

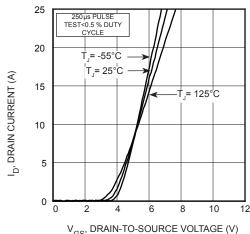
Microsemi reserves the right to change, without notice, the specifications and information contained herein.

### **Typical Performance Curves**

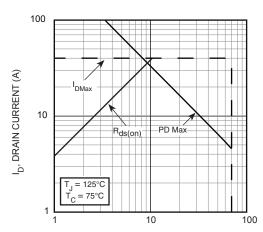




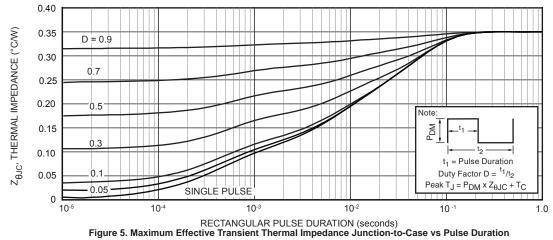
 ${
m V_{DS}}, {
m DRAIN\text{-}TO\text{-}SOURCE\ VOLTAGE\ (V)}$  FIGURE 3, Capacitance vs Drain-to-Source Voltage

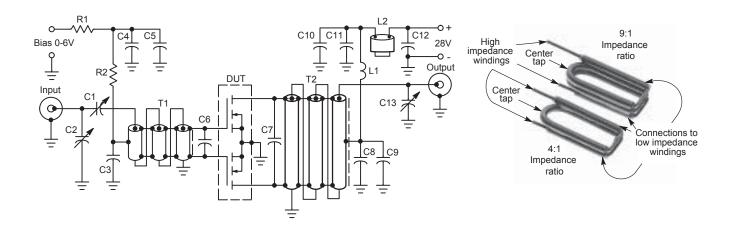


 ${\sf V}_{\sf GS}^{},$  DRAIN-TO-SOURCE VOLTAGE (V) FIGURE 2, Transfer Characteristics



V<sub>DS</sub>, DRAIN-TO-SOURCE VOLTAGE (V) FIGURE 4, Forward Safe Operating Area





C1 - Arco 402, 1.5 ±20 pF

C2 - Arco 406, 15 ±115 pF

C3, C4, C8, C9, C10 - 1000 pF Chip

C5, C11 - 0.1 mF Chip

C6 - 330 pF Chip

C7 - 200 pF and 180 pF Chips in Parallel

C12 - 0.47 mF Ceramic Chip, Kemet 1215 or Equivalent

C13 - Arco 403, 3.0 ±35 pF

L1 - 10 T urns AWG #16 Enameled Wire, Close Wound, 1/4, I.D.

L2 - Ferrite Beads of Suitable Material for 1.5±2.0 mH Total Inductance

R1 - 100 Ohms, 1/2 W R2 - 1.0 kOhm, 1/2 W

T1 - 9:1 RF Transformer. Can be made of 15±18 Ohms Semirigid Co-ax, 62 ±90 Mils O.D.

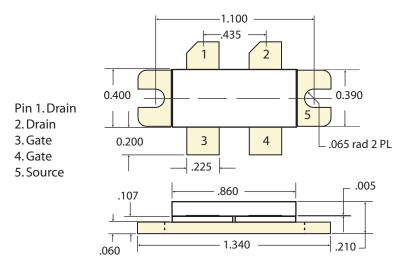
T2 - 1:9 RF Transformer . Can be made of 15±18 Ohms Semirigid Co-ax, 70 ±90 Mils O.D.

Board Material - 0.062 , Fiberglass (G10), 1 oz. Copper Clad, 2 Sides, e<sub>r</sub> = 5

NOTE: For stability, the input transformer T1 must be loaded with ferrite toroids or beads to increase the common mode inductance. For operation below 100 MHz. The same is required for the output transformer. See pictures for construction details.

Unless Otherwise Noted, All Chip Capacitors are ATC Type 100B or Equivalent.

Figure 7. 175 MHz Test Circuit



# Package Dimensions (inches) All Dimensions are ± .005

## HAZARDOUS MATERIAL WARNING

The ceramic portion of the device between leads and mounting flange is beryllium oxide. Beryllium oxide dust is highly toxic when inhaled. Care must be taken during handling and mounting to avoid damage to this area. These devices must never be thrown away with general industrial or domestic waste.

Microsemi's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 6,939,743 7,352,045 5,283,201 5,801,417 5,648,283 7,196,634 6,664,594 7,157,886 6,939,743 7,342,262 and foreign patents. US and Foreign patents pending. All Rights Reserved.



#### Microsemi Headquarters

One Enterprise, Aliso Viejo, CA 92656 USA Within the USA: +1 (800) 713-4113 Outside the USA: +1 (949) 380-6100 Sales: +1 (949) 380-6136 Fax: +1 (949) 215-4996 Email: sales.support@microsemi.com

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