

Table 2. Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
V_S	Supply Voltage	16	V
P_{tot}	Total Power Dissipation at $T_{amb} = 50\text{ }^{\circ}\text{C}$	1	W
I_O	Output Peak Current	1	A
T_{stg}, T_j	Storage and Junction Temperature	-40 to 150	$^{\circ}\text{C}$

Figure 3. PIN CONNECTIONS (top view)

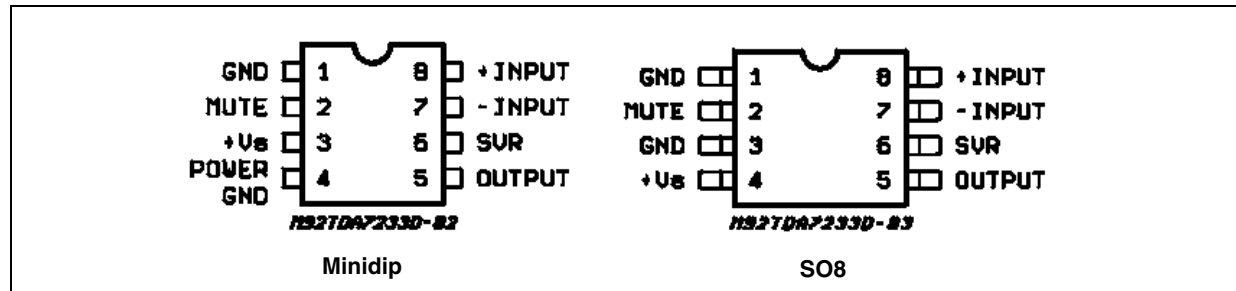


Table 3. Thermal Data

Symbol	Parameter		SO8	Minidip	Unit
$R_{th\ j-amb}$	Thermal Resistance Junction-ambient	Max.	200	100	$^{\circ}\text{C/W}$

Table 4. Electrical Characteristics ($V_S = 6\text{ V}$, $T_{amb} = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
V_S	Supply Voltage		1.8		15	V
V_O	Quiescent Output Voltage	$V_S = 3\text{ V}$ $V_S = 9\text{ V}$		2.7 1.2 4.2		V
I_d	Quiescent Drain Current	MUTE HIGH MUTE LOW		3.6 0.4	9	mA
I_b	Input Bias Current			100		nA
P_O	Output Power	$d = 10\%$; $f = 1\text{ kHz}$ $V_S = 12\text{ V}$; $R_L = 8\Omega$ $V_S = 9\text{ V}$; $R_L = 4\Omega$ $V_S = 9\text{ V}$; $R_L = 8\Omega$ $V_S = 6\text{ V}$; $R_L = 8\Omega$ $V_S = 6\text{ V}$; $R_L = 4\Omega$ $V_S = 3\text{ V}$; $R_L = 4\Omega$ $V_S = 3\text{ V}$; $R_L = 8\Omega$		1.9 1.6 1 0.4 0.7 110 70		W W W W W mW mW
d	Distortion	$P_O = 0.5\text{ W}$; $f = 1\text{ KHz}$; $R_L = 8\Omega$ $V_S = 9\text{ V}$		0.3		%
G_v	Closed Loop Voltage Gain	$f = 1\text{ KHz}$;		39		dB
R_{in}	Input Resistance	$f = 1\text{ KHz}$;	100			$\text{K}\Omega$
e_N	Total Input Noise	$R_S = 10\text{ K}\Omega$; B = Curve A $R_S = 10\text{ K}\Omega$; B = 22Hz to 22KHz		2 3		μV μV
SVR	Supply Voltage Rejection	$f = 100\text{ Hz}$; $R_g = 10\text{ K}\Omega$		45		dB
	MUTE Attenuation	$V_O = 1\text{ V}$; $f = 100\text{ Hz}$ to 10 KHz ;		70		dB
	MUTE Threshold			0.6		V
IM	MUTE Current	$V_S = 15\text{ V}$		0.4		mA

Figure 4. Output Power versus Supply Voltage

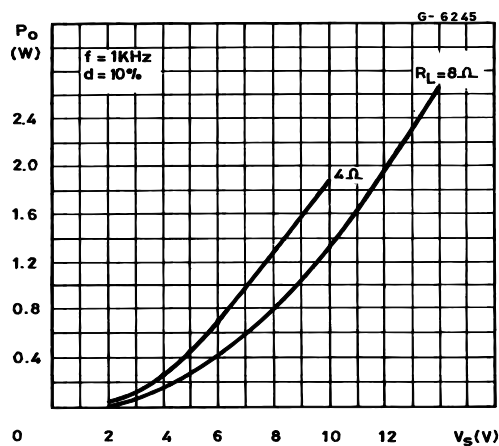


Figure 7. Quiescent Current versus Supply Voltage

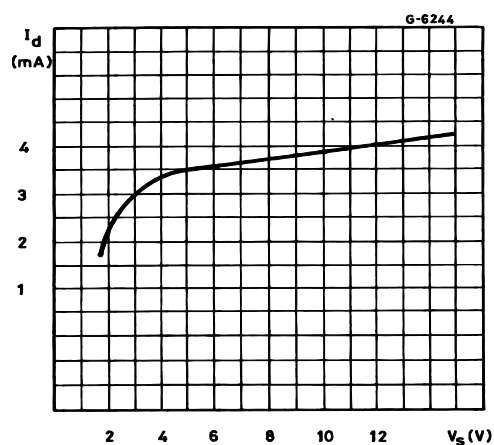


Figure 5. Supply Voltage Rejection versus Frequency

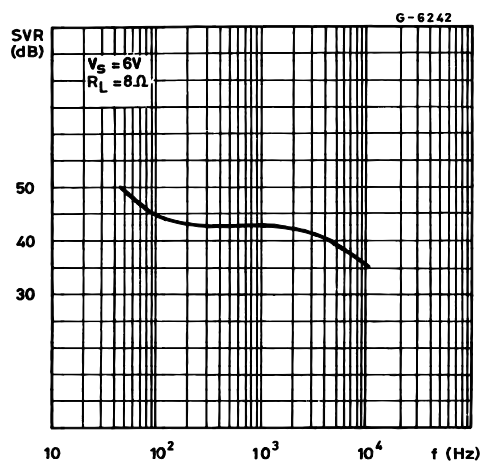


Figure 8. Total Power Dissipated versus Supply Voltage

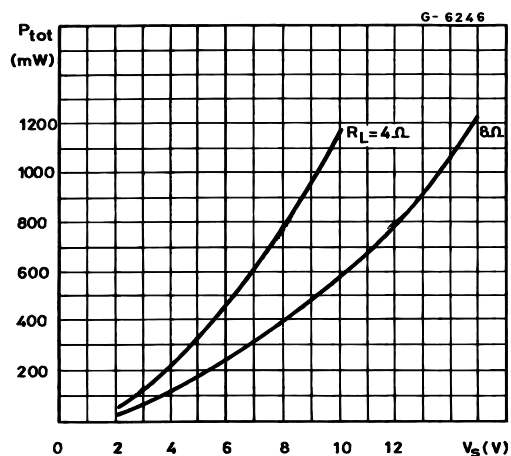
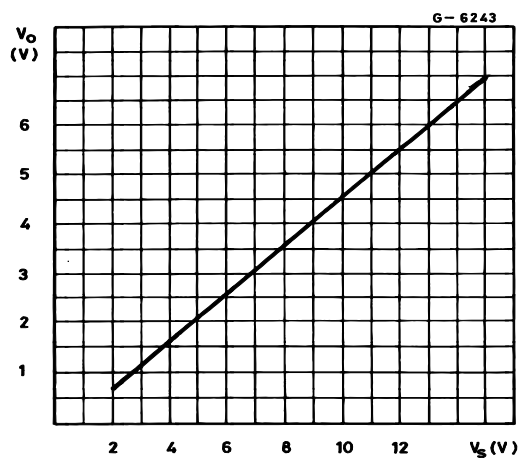


Figure 6. DC Output Voltage versus Supply Voltage



3 PACKAGE MECHANICAL DATA

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Figure 9. Minidip Mechanical Data & Package Dimensions

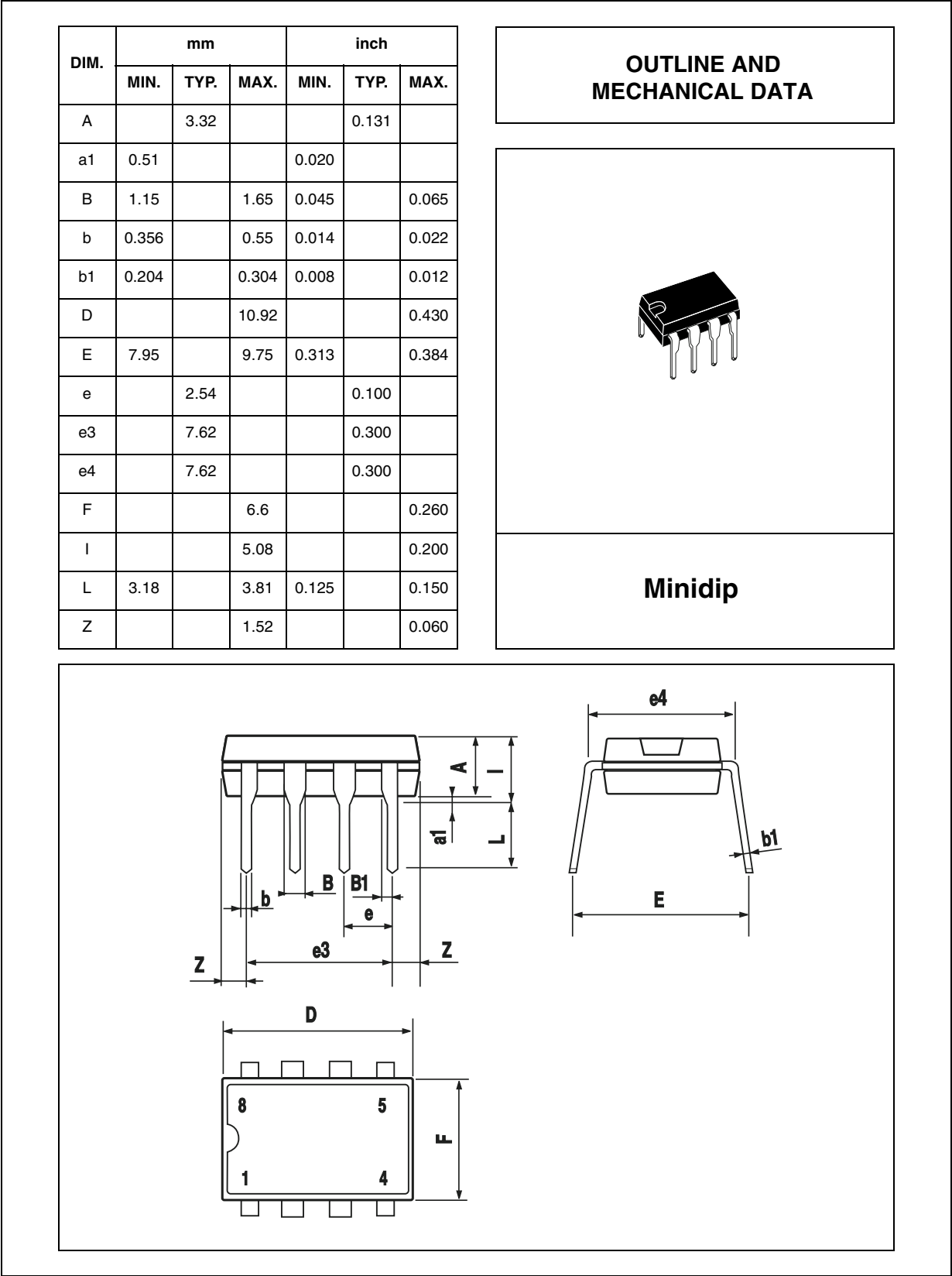
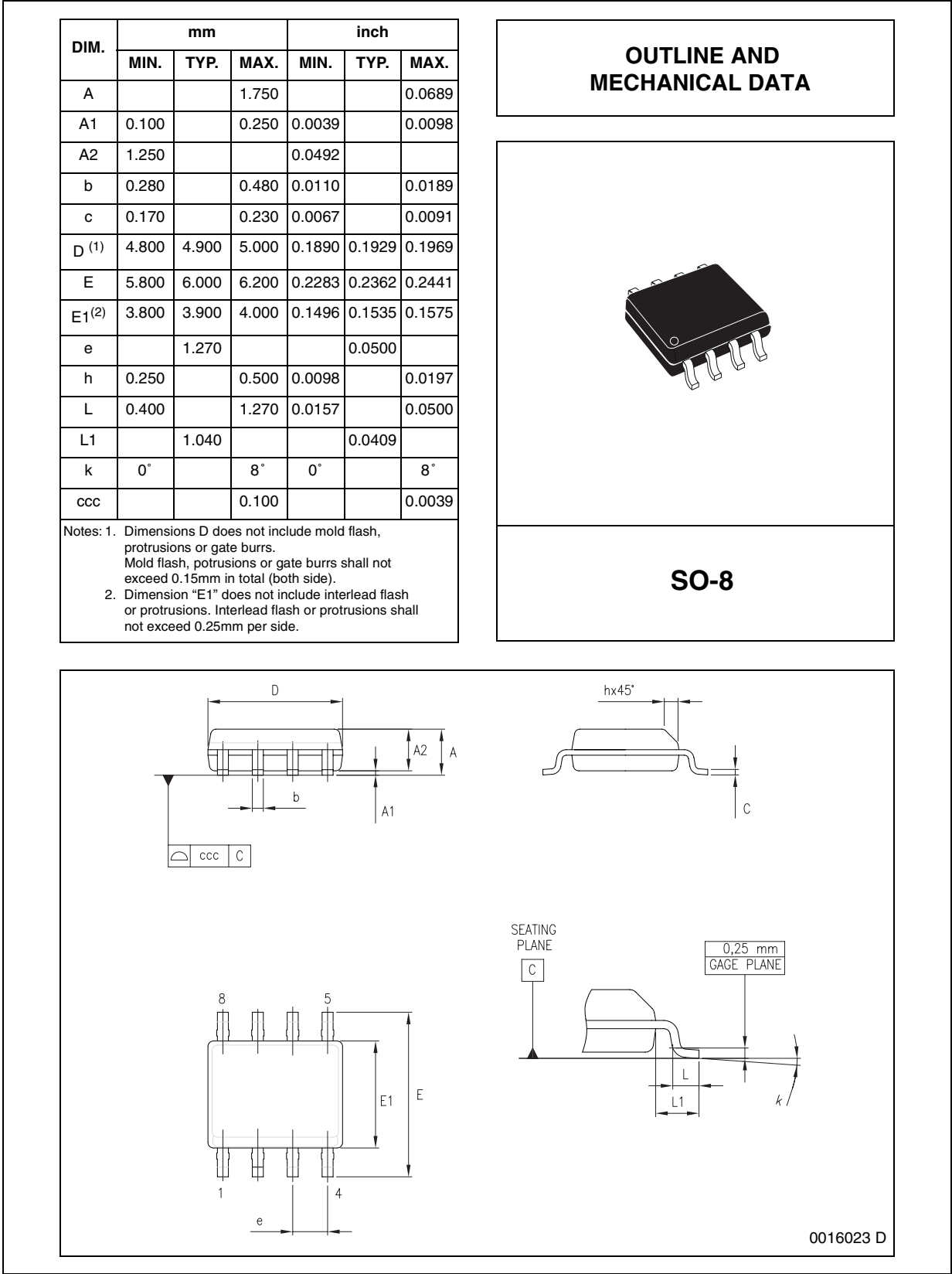


Figure 10. SO8 Mechanical Data & Package Dimensions



4 REVISION HISTORY

Table 5. Revision History

Date	Revision	Description of Changes
September 2003	3	No recorded changes
03-May-2010	4	Updated title and added environmental compliance statement for package

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