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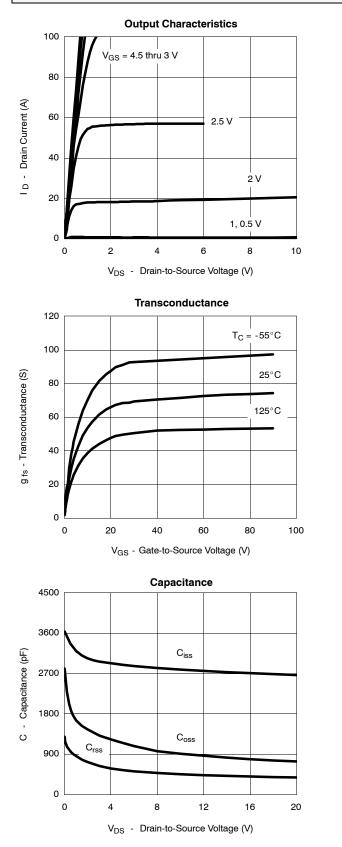


Parameter	Symbol	Test Condition	Min	Тур ^а	Мах	Unit
Static	-			1	1	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V_{GS} = 0 V, I_D = 250 μ A	20			v
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS},I_{D}=250\;\mu A$	0.6			
Gate-Body Leakage	I _{GSS}	V_{DS} = 0 V, V_{GS} = ±12 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1	μΑ
		V_{DS} = 20 V, V_{GS} = 0 V, T_J = 125 $^\circ C$			50	
On-State Drain Current ^b	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	40			А
Drain-Source On-State Resistance ^b		V_{GS} = 4.5 V, I_{D} = 20 A		0.0068	0.0085	Ω
	r _{DS(on)}	V_{GS} = 4.5 V, I_D = 20 A, T_J = 125 $^\circ C$		0.0104	0.013	
		V_{GS} = 2.5 V, I _D = 20 A		0.011	0.014	
Forward Transconductanceb	9 _{fs}	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 40 \text{ A}$	20			S
Dynamic ^a			•			
Input Capacitance	C _{iss}	V_{GS} = 0 V, V_{DS} = 20 V, f = 1 MHz		2660		pF
Output Capacitance	C _{oss}			730		
Reverse Transfer Capacitance	C _{rss}			375		
Total Gate Charge ^c	Qg	V_{DS} = 10 V, V_{GS} = 4.5 V, I _D = 40 A		26	35	nC
Gate-Source Charge ^c	Q _{gs}			5		
Gate-Drain Charge ^c	Q _{gd}			7		
Gate Resistance	Rg		1		3.7	Ω
Turn-On Delay Time ^c	t _{d(on)}	V_{DD} = 10 V, R _L = 0.25 Ω I _D ≅ 40 A, V _{GEN} = 4.5 V, R _G = 2.5 Ω		20	35	- ns
Rise Time ^c	tr			120	190	
Turn-Off Delay Time ^c	t _{d(off)}			45	70	
Fall Time ^c	tf			20	35	
Source-Drain Diode Ratings ar	d Characterist	ic (T _C = 25°C)	•			
Pulsed Current	I _{SM}				100	А
Diode Forward Voltage ^b	V _{SD}	I _F = 100 A, V _{GS} = 0 V		1.2	1.5	V
Source-Drain Reverse Recovery Time	t _{rr}	I _F = 40 A, di/dt = 100 A/μs		35	70	ns

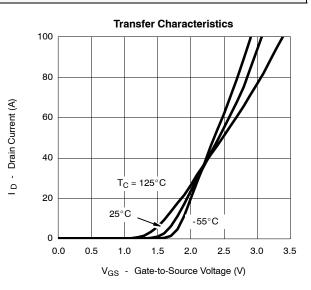


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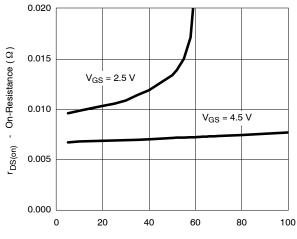
TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



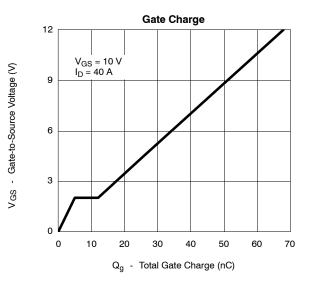
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On-Resistance vs. Drain Current



I_D - Drain Current (A)



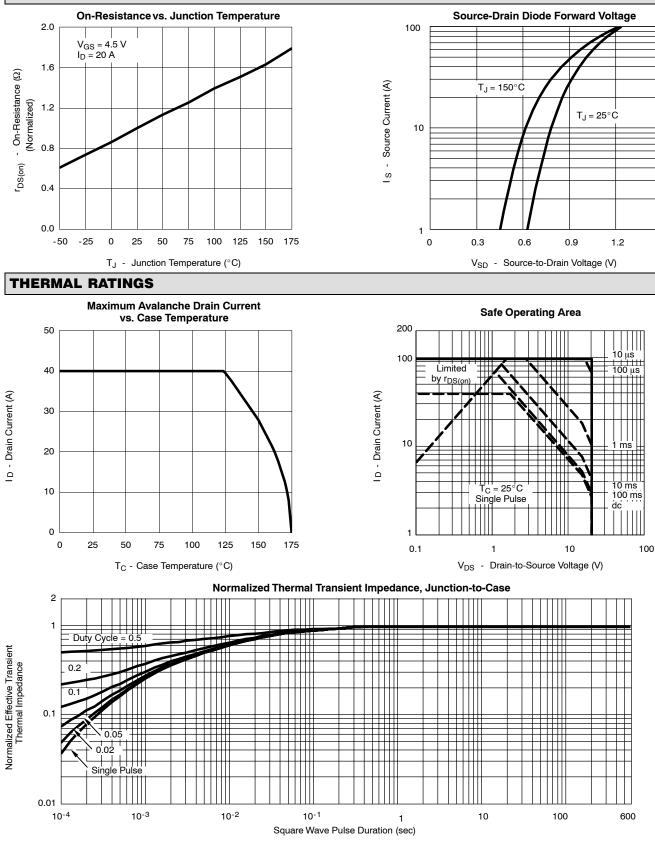
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TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



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