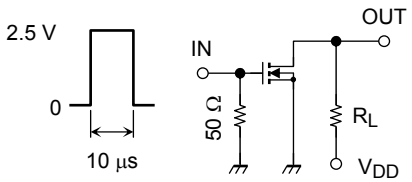
Electrical Characteristics (Ta = 25°C) (Q1, Q2 Common)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	V_{GS} = ±10 V, V_{DS} = 0V	—	—	±10	μA
Drain–source breakdown voltage		$V_{(BR)\ DSS}$	I_D = 0.1 mA, V_{GS} = 0V	20	—	—	V
Drain cutoff current		I_{DSS}	V_{DS} = 20 V, V_{GS} = 0V	—	—	1	μA
Gate threshold voltage		V_{th}	V_{DS} = 3 V, I_D = 1 mA	0.4	—	1.0	V
Forward transfer admittance		$\|Y_{fs}\|$	V_{DS} = 3 V, I_D = 50 mA (Note 2)	115	—	—	mS
Drain–source ON-resistance		$R_{DS\ (ON)}$	I_D = 50 mA, V_{GS} = 4 V (Note 2)	—	1.5	3	Ω
			I_D = 50 mA, V_{GS} = 2.5 V (Note 2)	—	2	4	
			I_D = 5 mA, V_{GS} = 1.5 V (Note 2)	—	3	8	
			I_D = 5 mA, V_{GS} = 1.2 V (Note 2)	—	5	20	
Input capacitance		C_{iss}	V_{DS} = 3 V, V_{GS} = 0V, f = 1 MHz	—	9.5	—	pF
Reverse transfer capacitance		C_{rss}		—	4.1	—	
Output capacitance		C_{oss}		—	9.5	—	
Switching time	Turn-on time	t_{on}	V_{DD} = 3 V, I_D = 50 mA, V_{GS} = 0 to 2.5 V	—	115	—	ns
	Turn-off time	t_{off}		—	300	—	
Drain–source forward voltage		V_{DSF}	I_D = - 180 mA, V_{GS} = 0V (Note 2)	—	-0.9	-1.2	V

Note 2: Pulse test

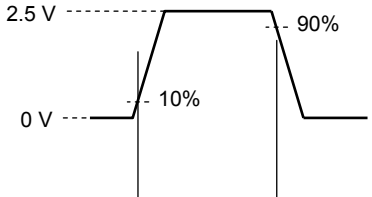
Switching Time Test Circuit (Q1, Q2 Common)

(a) Test Circuit

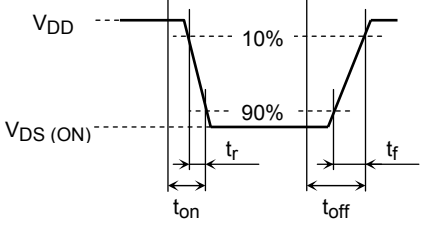


V_{DD} = 3 V
Duty ≤ 1%
V_{IN}: t_r, t_f < 5 ns
(Z_{out} = 50 Ω)
Common Source
Ta = 25°C

(b) V_{IN}



(c) V_{OUT}



Usage Considerations

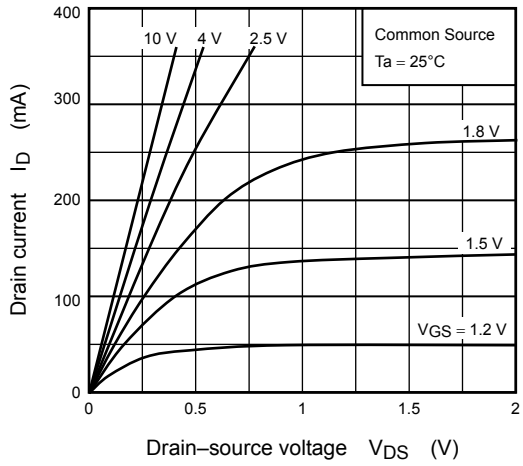
Let V_{th} be the voltage applied between gate and source that causes the drain current (I_D) to below (1 mA for the SSM6N35FE). Then, for normal switching operation, $V_{GS(on)}$ must be higher than V_{th}, and $V_{GS(off)}$ must be lower than V_{th}. This relationship can be expressed as: $V_{GS(off)} < V_{th} < V_{GS(on)}$.

Take this into consideration when using the device.

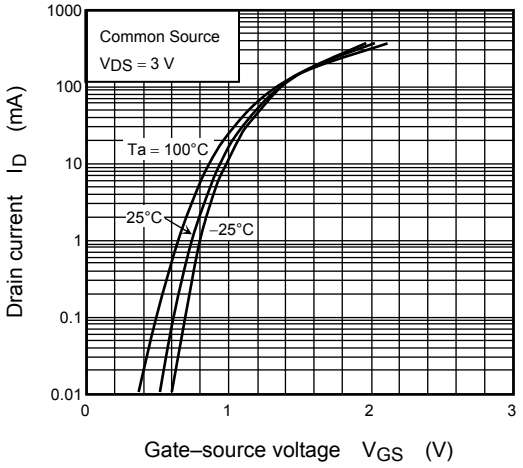
Handling Precaution

When handling individual devices that are not yet mounted on a circuit board, make sure that the environment is protected against electrostatic discharge. Operators should wear antistatic clothing, and containers and other objects that come into direct contact with devices should be made of antistatic materials.

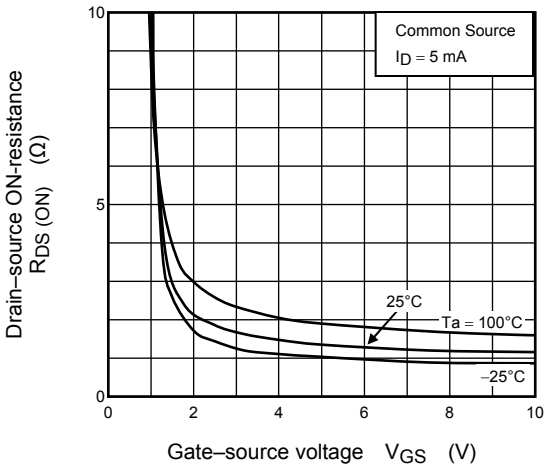
$I_D - V_{DS}$



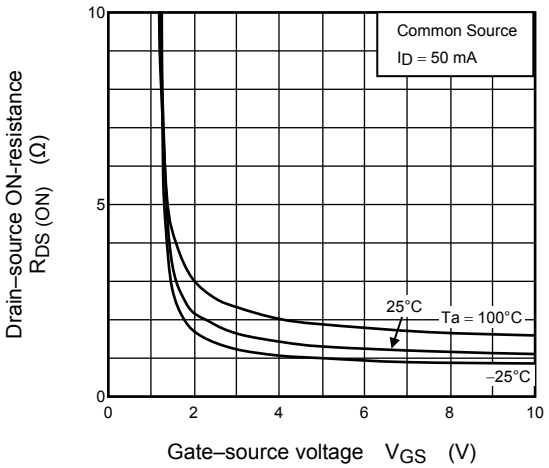
$I_D - V_{GS}$



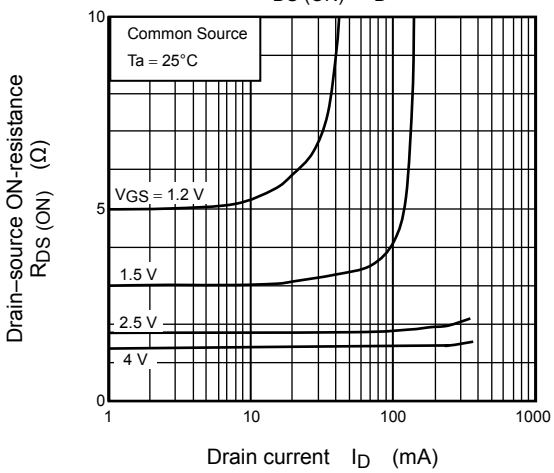
$R_{DS\ (ON)} - V_{GS}$



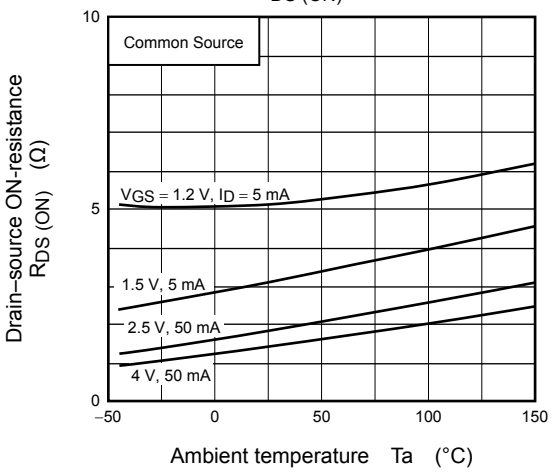
$R_{DS\ (ON)} - V_{GS}$



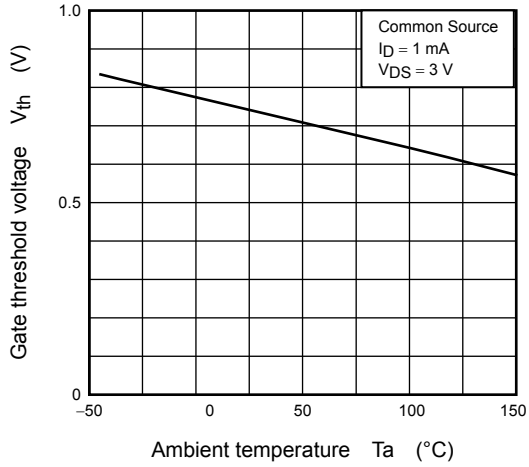
$R_{DS\ (ON)} - I_D$



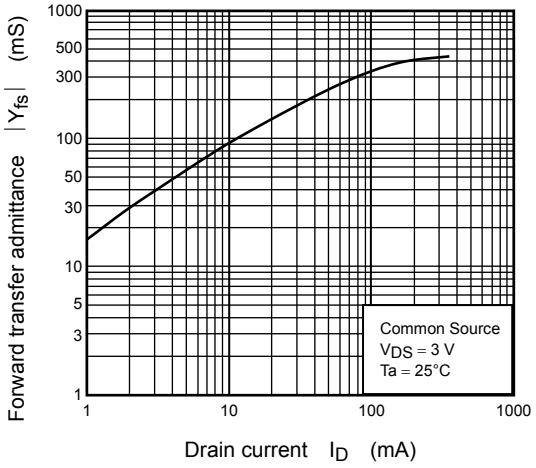
$R_{DS\ (ON)} - Ta$



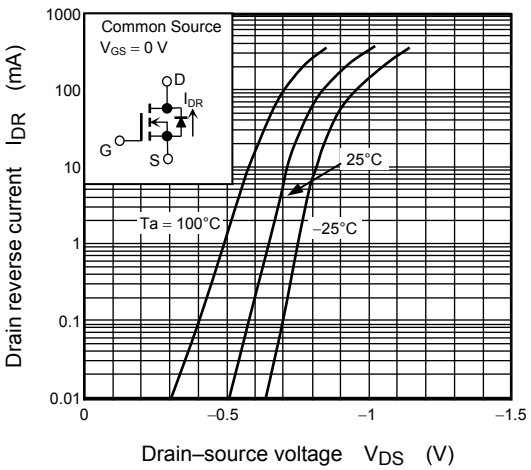
V_{th} – Ta



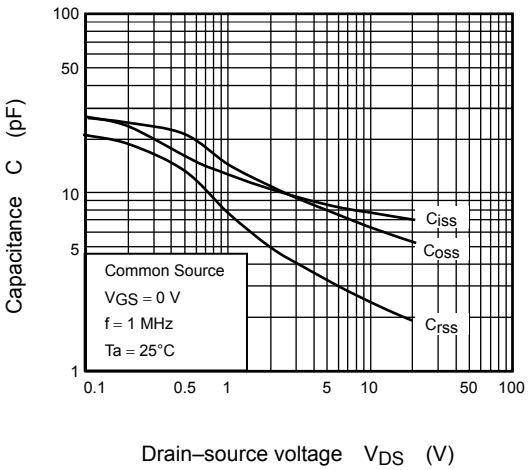
$|Y_{fs}|$ – I_D



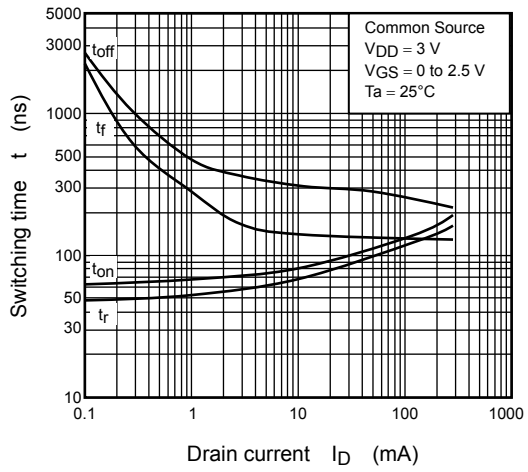
I_{DR} – V_{DS}



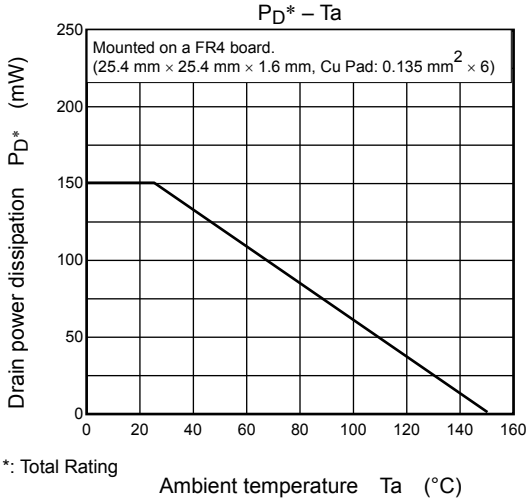
C – V_{DS}



t – I_D



P_D* – Ta



*: Total Rating

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