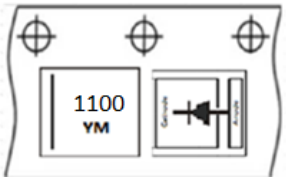
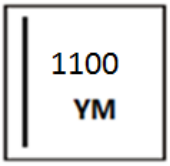
Marking Information





1100 = Product Type Marking Code
YM = Date Code Marking
I = Year (ex: I = 2021)
M = Month (ex: 6 = June)
Bar = Cathode

Date Code Key

Year	2004	……	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	R	……	I	J	K	L	M	N	O	P	R	S

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_{RM}	100	V
Average Rectified Output Current	I_O	1	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I_{FSM}	40	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Case (Note 5)	$R_{\theta JC}$	16	°C/W
Typical Thermal Resistance Junction to Ambient (Note 5)	$R_{\theta JA}$	65	°C/W
Operating Temperature Range	T_J	-55 to +175	°C
Storage Temperature Range	T_{STG}	-55 to +175	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage	$V_{(BR)R}$	100	—	—	V	I_R=1mA
Forward Voltage (Note 6)	V_F	—	—	0.77	V	I_F = 1A, T_J = +25°C
		—	0.58	0.62		I_F = 1A, T_J = +125°C
		—	—	0.86		I_F = 2A, T_J = +25°C
		—	0.65	0.70		I_F = 2A, T_J = +125°C
Leakage Current (Note 6)	I_R	—	—	0.1	µA	V_R = 50V, T_J = +25°C
		—	—	0.015	mA	V_R = 50V, T_J = +85°C
		—	—	0.35	µA	V_R = 100V, T_J = +25°C
		—	—	0.35	mA	V_R = 100V, T_J = +125°C
Total Capacitance	C_T	—	40	—	pF	V_R = 5V, f = 1MHz

Notes: 5. Device mounted 1inch sq. copper pad, 2oz.
6. Short duration pulse test used to minimize self-heating effect.

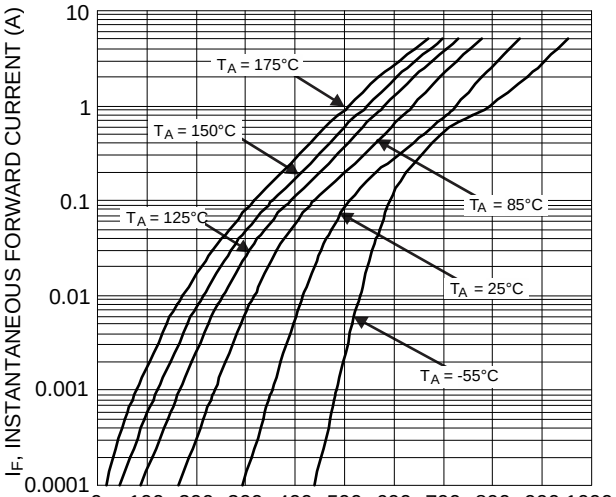


Figure 1 Typical Forward Characteristics

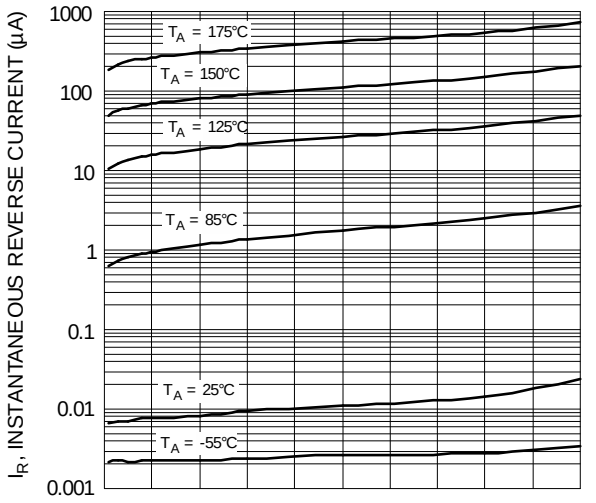


Figure 2 Typical Reverse Characteristics

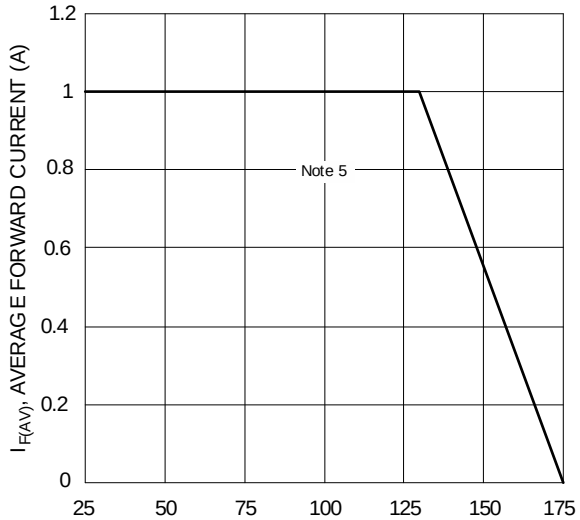


Figure 3 DC Forward Current Derating Curve

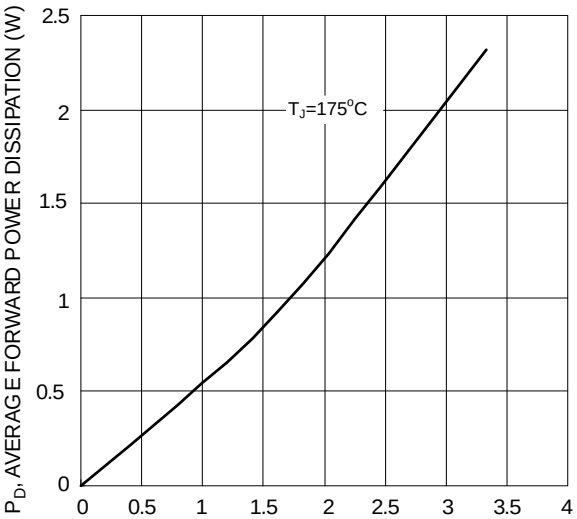


Figure 4 Forward Power Dissipation

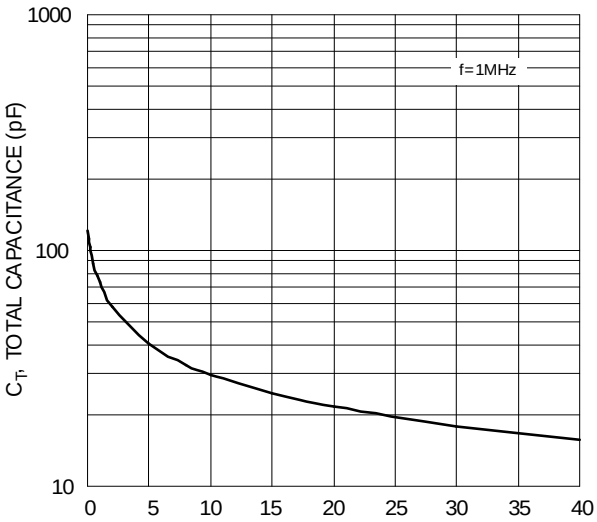
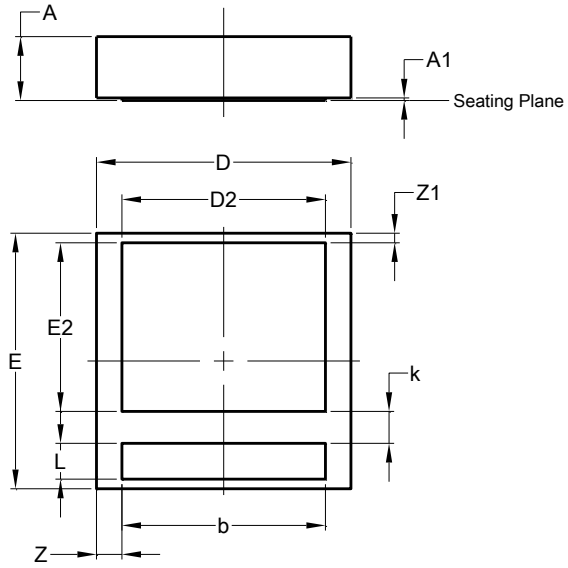


Figure 5 Total Capacitance vs. Reverse Voltage

Package Outline Dimensions

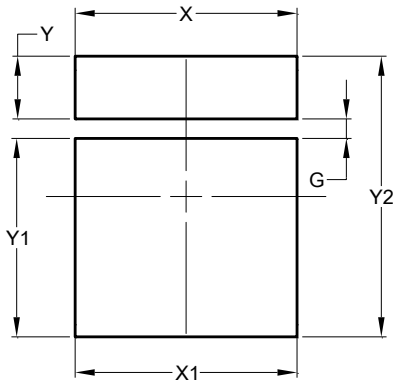
Please see http://www.diodes.com/package-outlines.html for the latest version.



U-DFN2020-2 (Type B)			
Dim	**Min**	**Max**	**Typ**
A	0.47	0.53	0.50
A1	0.00	0.05	0.02
b	1.55	1.65	1.60
D	1.95	2.05	2.00
D2	1.50	1.70	1.60
E	1.95	2.05	2.00
E2	1.22	1.42	1.32
k	0.25 BSC		
L	0.23	0.33	0.28
Z	0.20 BSC		
Z1	0.075 BSC		
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
G	0.150
X	1.700
X1	1.700
Y	0.480
Y1	1.520
Y2	2.150

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