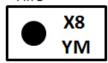


Marking Information

X3-WLB1406-2

Pin 1



X8=Product Type Marking Code YM=Date Code Marking Y=Year (ex: G=2019) M=Month (ex: N=November) Dot Denotes Cathode Pin

Date Code Key

Date Code Ney												
Year	201	4	2015		2016	20	17	2018		2019		2020
Code	В		С		D	I	E	F		G		Н
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	20	V
Average Rectified Output Current	Io	1.0	Α
Repetitive Peak Forward Current (Pulse Wave = 1 Sec, Duty Cycle = 66%)	I _{FRM}	5.0	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	18	А

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	R _{θJA}	140	°C/W
Typical Thermal Resistance Junction to Ambient (Note 6)	R _{θJA}	73	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

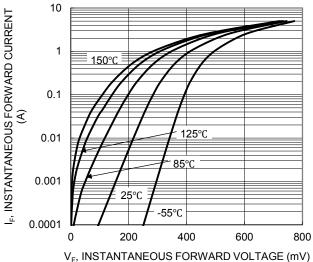
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Dren	.,	1	_	0.39	V	I _F = 0.5A
Forward Voltage Drop	V _F	_	_	0.44		I _F = 1.0A
Reverse Current (Note 7)	1-	_	_	25	IIA	V _R = 10V
Reverse Current (Note 1)	I _R		_	100		V _R = 20V
Junction Capacitance	CJ		76	_	pF	V _R = 4V, f = 1.0MHz

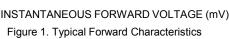
Notes: 5. Device mounted on FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/package-outlines.html.

^{6.} Device mounted on FR-4 PCB, 2oz. 1 square inch Copper.

^{7.} Short duration pulse test used to minimize self-heating effect.







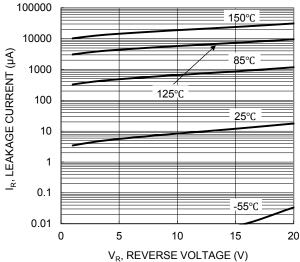


Figure 2. Typical Reverse Characteristics

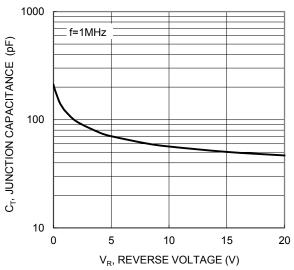


Figure 3. Typical Junction Capacitance

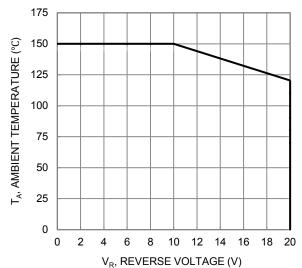


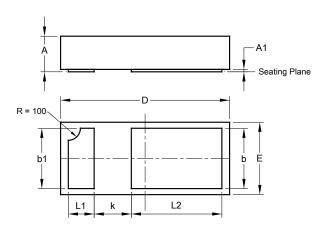
Figure 4. Operating Temperature Derating



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

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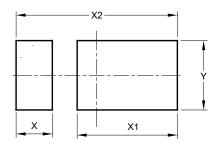


X3-WLB1406-2					
Dim	Min	Max	Тур		
Α	0.250	0.300	0.275		
A1	0.000	0.015	-		
b	0.45	0.55	-		
b1	0.45	0.55	-		
D	1.37	1.43	1.40		
Е	0.57	0.63	0.60		
k	-	-	0.30		
L1	0.20	0.26	-		
L2	0.70	0.80	-		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X3-WLB1406-2



Dimensions	Value		
Dilliensions	(in mm)		
Х	0.304		
X1	0.840		
X2	1.352		
Y	0.580		



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