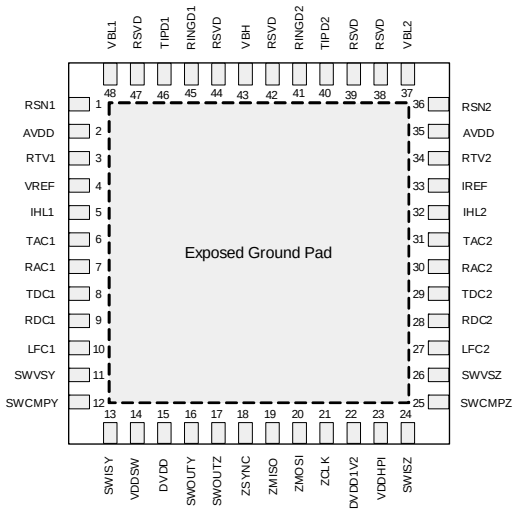
Selected Electrical Specifications

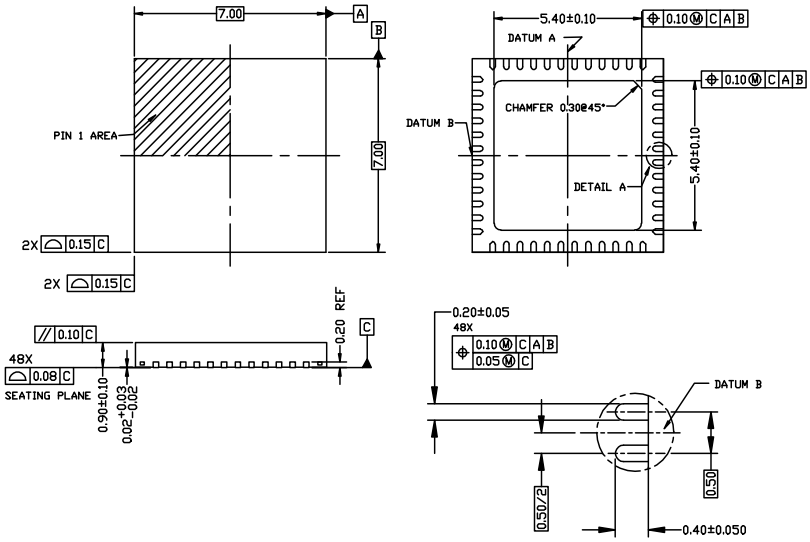
Description	Symbol	Test Conditions	Min	Typ	Max	Unit
Ambient Temperature, under Bias	T_A		-40		+85	°C
Digital and Analog Supply Voltages	DVDD, AVDD		3.135	3.3	3.465	V_{DC}
Operating Limits: VBH VBL			VBH = -($V_{SWITCHER}$ + (2 * |VBL|) – 2V) VBL = -40 V_{DC} to -25 V_{DC}			V_{DC}
Line Current:	ILA		18	25	30	mA
Ringing Voltage	V_{RING}	5 REN		50	60	V_{RMS}
Two-Wire Return Loss	R_L	200 to 3400 Hz		30		dB
Longitudinal Balance		1 kHz		58		dB
Device Power Dissipation, Continuous	$P_{D(max)}$	T_A = 85°C		1.5		W
Junction to Ambient Thermal Resistance	θ_{JA}			29		°C/W

Device Power Consumption (BBABS)	Symbol	Test Conditions	Power	Typ	Unit
Shutdown	P_D	Switcher off	Per Channel	5	mW
Disconnect				25	
Low Power Idle Mode (LPIM)		On-Hook		47	
Idle		On-Hook		99	
Active		Off-Hook, 300 Ω, ILA = 25 mA		520	
Ringing		50 V_{RMS}, 5 REN, other channel in LPIM mode	Both Channels	840	

Device Pinout



Package Drawings



Related Collateral

www.microsemi.com/voice-line-circuits

- *Le9641 Tracking Battery miSLIC™ Line Circuit Preliminary Data Sheet*, Document ID# 148556
  - *ZLR964122L SM2 Line Module, ZSI, 1 FXS, Buck-Boost ABS, 85-V_{PK}, 5 REN*
  - *ZLR964124L SM2 Line Module, PCM/SPI, 1 FXS Buck-Boost and 1 FXS Inverting-Boost, 85-V_{PK}, 5 REN*
- *Le9642 Shared Battery Dual miSLIC™ Line Circuit Preliminary Data Sheet*, Document ID# 148557
  - *ZLR964222L SM2 Line Module, ZSI, 2 FXS, Buck-Boost ABS, 85-V_{PK}, 5 REN*

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