

IRF7313

International
IR Rectifier

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	30	—	—	V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	0.022	—	V/ $^\circ\text{C}$	Reference to 25°C , $I_D = 1\text{mA}$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	0.023	0.029	Ω	$V_{GS} = 10V, I_D = 5.8A$ ④
		—	0.032	0.046		$V_{GS} = 4.5V, I_D = 4.7A$ ④
$V_{GS(th)}$	Gate Threshold Voltage	1.0	—	—	V	$V_{DS} = V_{GS}, I_D = 250\mu A$
g_{fs}	Forward Transconductance	—	14	—	S	$V_{DS} = 15V, I_D = 5.8A$
I_{DSS}	Drain-to-Source Leakage Current	—	—	1.0	μA	$V_{DS} = 24V, V_{GS} = 0V$
		—	—	25		$V_{DS} = 24V, V_{GS} = 0V, T_J = 55^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{GS} = 20V$
	Gate-to-Source Reverse Leakage	—	—	-100		$V_{GS} = -20V$
Q_g	Total Gate Charge	—	22	33	nC	$I_D = 5.8A$
Q_{gs}	Gate-to-Source Charge	—	2.6	3.9		$V_{DS} = 15V$
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	6.4	9.6		$V_{GS} = 10V$, See Fig. 10 ④
$t_{d(on)}$	Turn-On Delay Time	—	8.1	12	ns	$V_{DD} = 15V$
t_r	Rise Time	—	8.9	13		$I_D = 1.0A$
$t_{d(off)}$	Turn-Off Delay Time	—	26	39		$R_G = 6.0\Omega$
t_f	Fall Time	—	17	26		$R_D = 15\Omega$ ④
C_{iss}	Input Capacitance	—	650	—	pF	$V_{GS} = 0V$
C_{oss}	Output Capacitance	—	320	—		$V_{DS} = 25V$
C_{rss}	Reverse Transfer Capacitance	—	130	—		$f = 1.0\text{MHz}$, See Fig. 9

Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	2.5	A	MOSFET symbol showing the integral reverse p-n junction diode.
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	30		
V_{SD}	Diode Forward Voltage	—	0.78	1.0	V	$T_J = 25^\circ\text{C}, I_S = 1.7A, V_{GS} = 0V$ ③
t_{rr}	Reverse Recovery Time	—	45	68	ns	$T_J = 25^\circ\text{C}, I_F = 1.7A$
Q_{rr}	Reverse Recovery Charge	—	58	87	nC	$di/dt = 100A/\mu s$ ③

Notes:

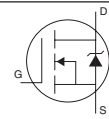
① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)

② Starting $T_J = 25^\circ\text{C}$, $L = 10\text{mH}$
 $R_G = 25\Omega$, $I_{AS} = 4.0A$.

③ $I_{SD} \leq 4.0A$, $di/dt \leq 74A/\mu s$, $V_{DD} \leq V_{(BR)DSS}$,
 $T_J \leq 150^\circ\text{C}$

④ Pulse width $\leq 300\mu s$; duty cycle $\leq 2\%$.

⑤ Surface mounted on FR-4 board, $t \leq 10\text{sec}$.



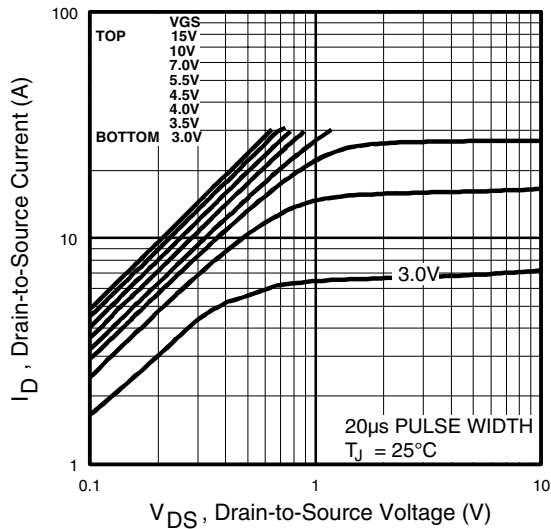


Fig 1. Typical Output Characteristics

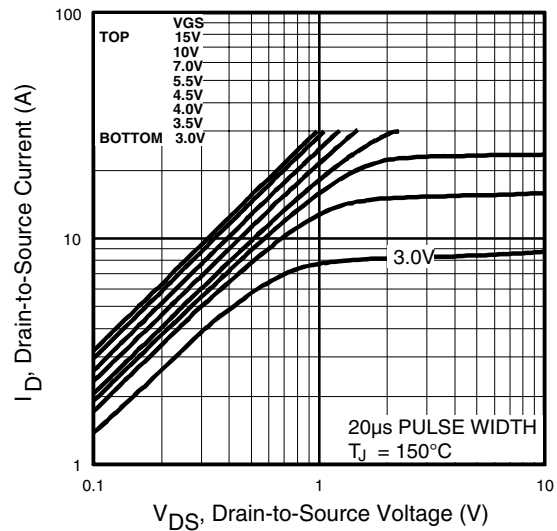


Fig 2. Typical Output Characteristics

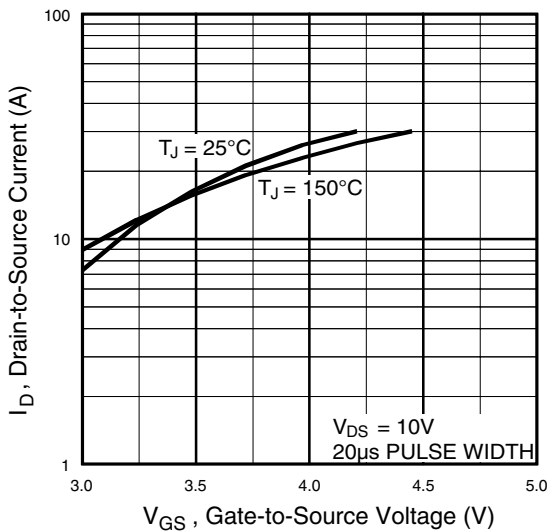


Fig 3. Typical Transfer Characteristics

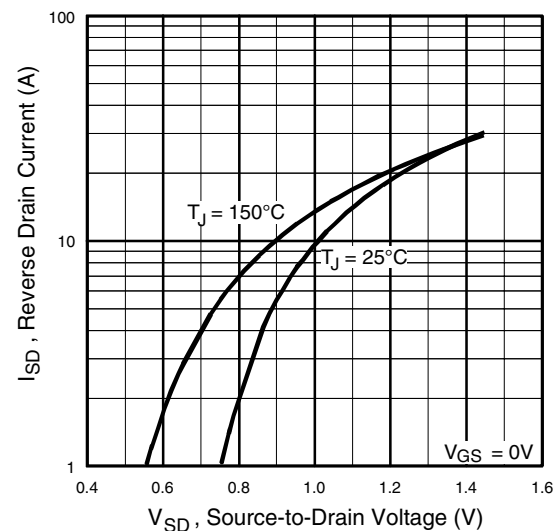


Fig 4. Typical Source-Drain Diode Forward Voltage

V_DS

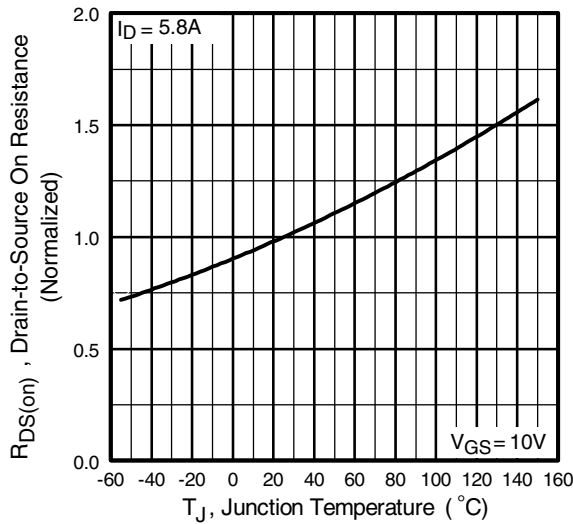


Fig 5. Normalized On-Resistance Vs. Temperature

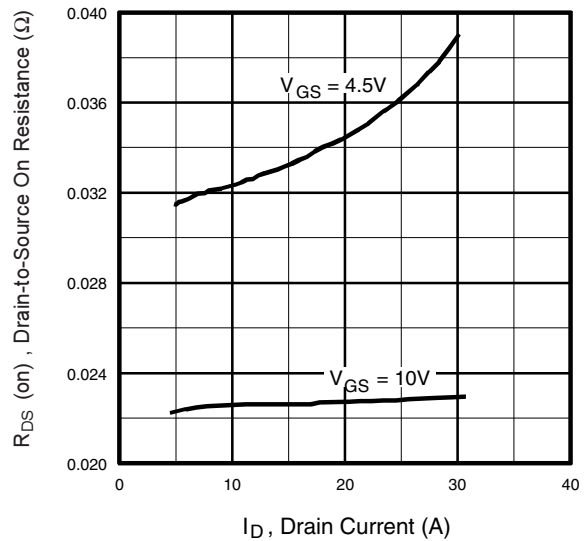


Fig 6. Typical On-Resistance Vs. Drain Current

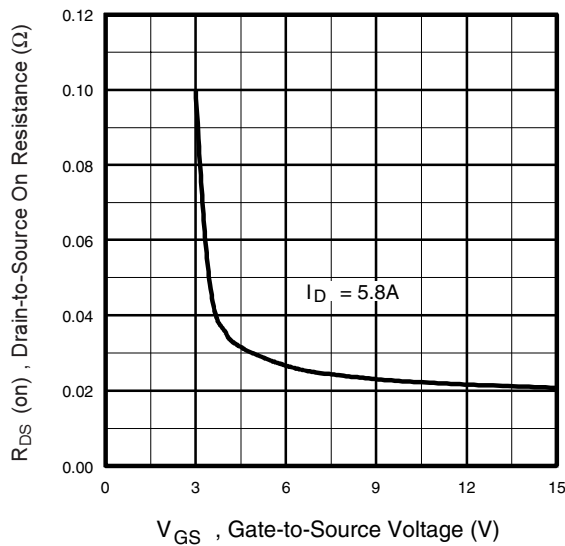


Fig 7. Typical On-Resistance Vs. Gate Voltage

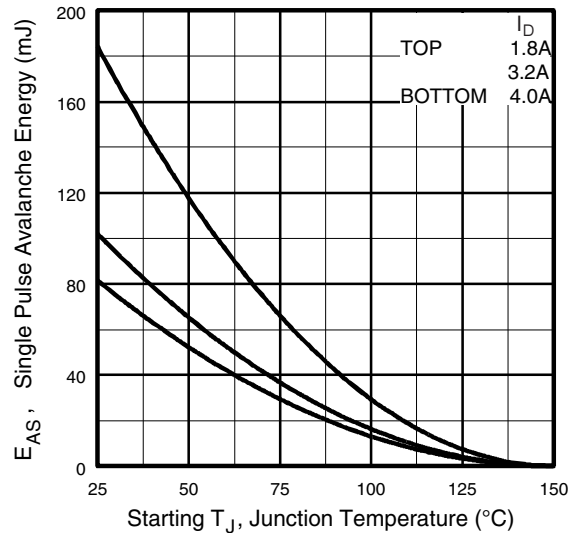


Fig 8. Maximum Avalanche Energy Vs. Drain Current

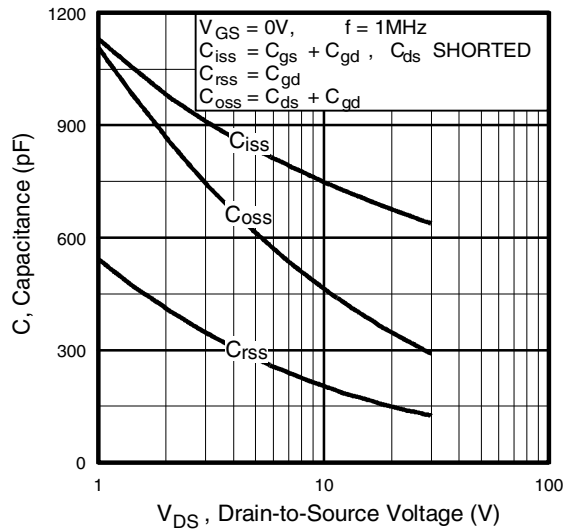


Fig 9. Typical Capacitance Vs. Drain-to-Source Voltage

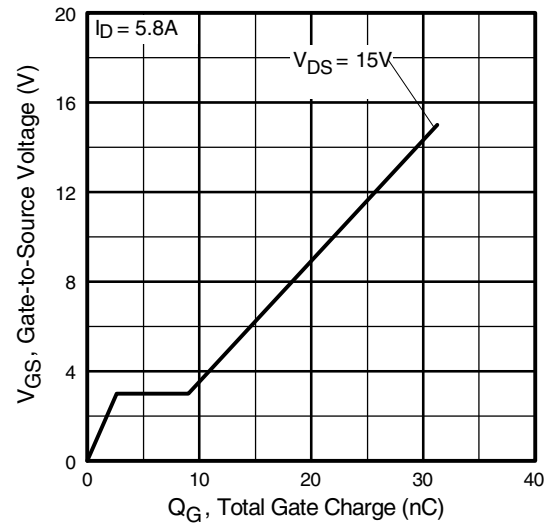


Fig 10. Typical Gate Charge Vs. Gate-to-Source Voltage

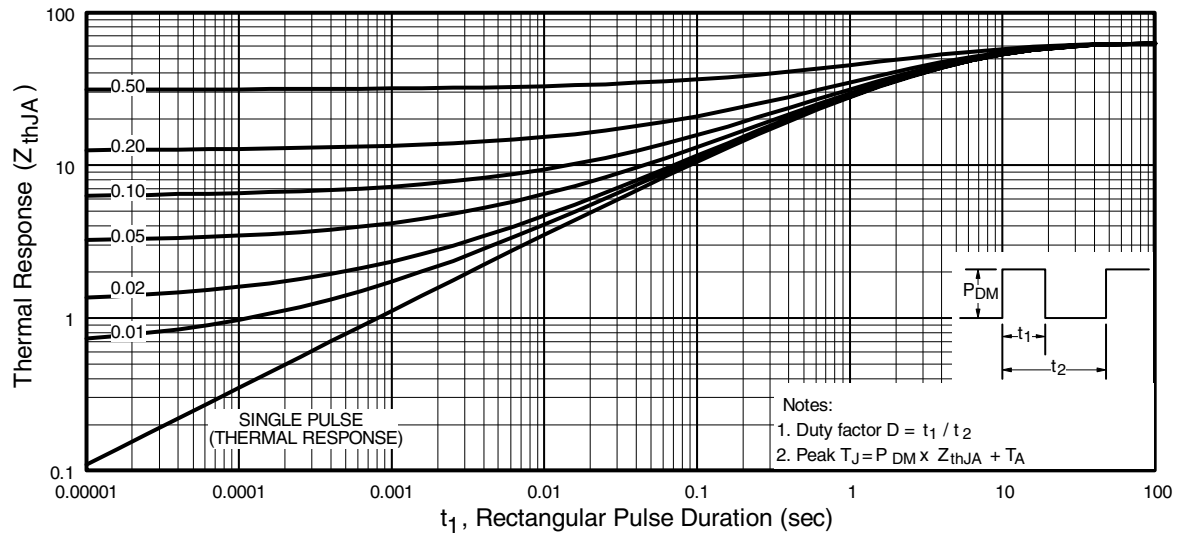


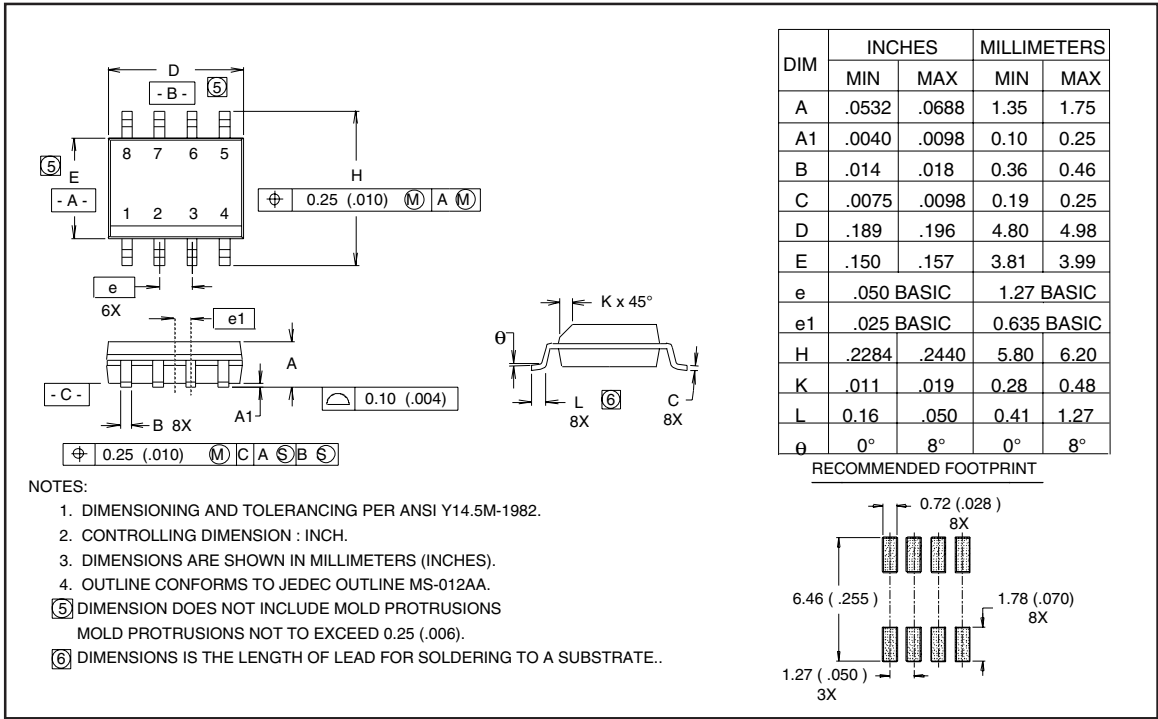
Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

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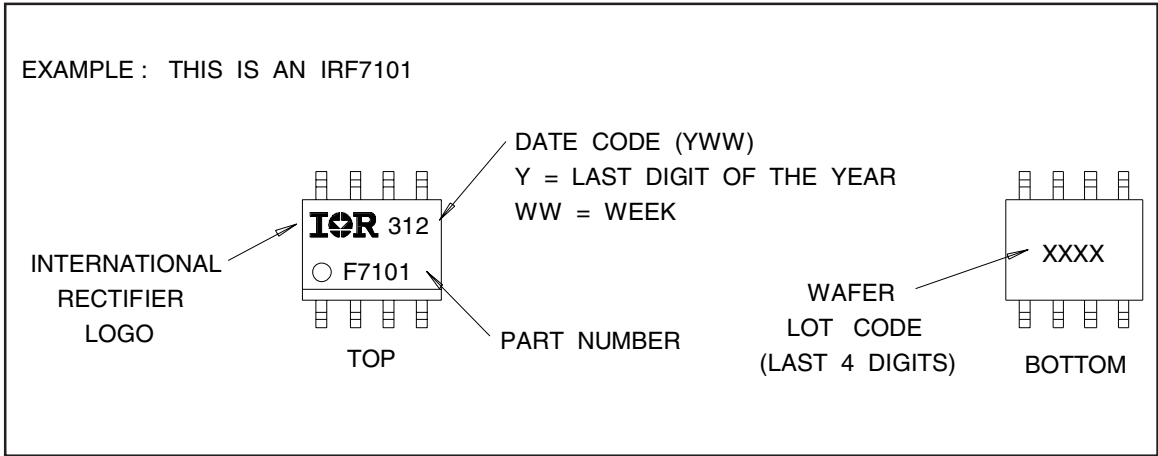
Package Outline

SO8 Outline



Part Marking Information

SO8



Tape & Reel Information

S08

Dimensions are shown in millimeters (inches)

