

IPS521/IPS521S

International
IR Rectifier

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to GROUND lead. ($T_j = 25^{\circ}\text{C}$ unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units	Test Conditions
V_{out}	Maximum output voltage	$V_{\text{CC}}-50$	$V_{\text{CC}}+0.3$	V	
V_{offset}	Maximum logic ground to load ground offset	$V_{\text{CC}}-50$	$V_{\text{CC}}+0.3$		
V_{in}	Maximum Input voltage	-0.3	7		
$I_{\text{in, max}}$	Maximum positive IN current	-5	10	mA	
V_{dg}	Maximum diagnostic output voltage	-0.3	7	V	
$I_{\text{dg, max}}$	Maximum diagnostic output current	-1	10	mA	
$I_{\text{sd cont.}}$	Diode max. permanent current ⁽¹⁾ ($r_{\text{th}}=62^{\circ}\text{C/W}$)	—	2.2	A	
$I_{\text{sd pulsed}}$	Diode max. pulsed current ⁽¹⁾	—	10		
ESD1	Electrostatic discharge voltage (Human Body)	—	4	kV	C=100pF, R=1500 Ω ,
ESD2	Electrostatic discharge voltage (Machine Model)	—	0.5		C=200pF, R=0 Ω , L=10 μH
P_d	Maximum power dissipation ⁽¹⁾ ($T_C=25^{\circ}\text{C}$) IPS521	—	40	W	
	($T_C=25^{\circ}\text{C}$) IPS521S	—	1.56		
$T_j \text{ max.}$	Max. storage & operating junction temp.	-40	+150	$^{\circ}\text{C}$	
T_{lead}	Lead temperature (soldering 10 seconds)	—	300		
$V_{\text{CC max.}}$	Maximum Vcc voltage	—	50	V	

Thermal Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
$R_{\text{th 1}}$	Thermal resistance junction to case	—	3	—	$^{\circ}\text{C/W}$	TO-220
$R_{\text{th 2}}$	Thermal resistance junction ambient	—	60	—		
$R_{\text{th 1}}$	Thermal resistance with standard footprint	—	60	—		D ² PAK (SMD220)
$R_{\text{th 2}}$	Thermal resistance with 1" square footprint	—	35	—		
$R_{\text{th 3}}$	Thermal resistance junction to case	—	3	—		

(1) Limited by junction temperature (pulsed current limited also by internal wiring)

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
V _{CC}	Continuous V _{CC} voltage	5.5	35	V
V _{IH}	High level input voltage	4	5.5	
V _{IL}	Low level input voltage	-0.3	0.9	
I _{out}	Continuous output current (T _{amb} = 85°C, T _j = 125°C, R _{th} = 60°C/W,) IPS521	—	2.2	A
I _{out}	Continuous output current (T _{amb} = 85°C, T _j = 125°C, R _{th} = 80°C/W,) IPS521S	—	1.9	
R _{in}	Recommended resistor in series with IN pin	4	6	kΩ
R _{dg}	Recommended resistor in series with DG pin	10	20	

Static Electrical Characteristics

(T_j = 25°C, V_{CC} = 14V unless otherwise specified.)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R _{ds(on)} @T _j =25°C	ON state resistance T _j = 25°C	—	55	80	mΩ	V _{in} = 5V, I _{out} = 5A
R _{ds(on)} (V _{CC} =6V)	ON state resistance @ V _{CC} = 6V	—	55	80		V _{in} = 5V, I _{out} = 2.5A
R _{ds(on)} @T _j =150°C	ON state resistance T _j = 150°C	—	110	140		V _{in} = 5V, I _{out} = 5A
V _{CC} oper.	Operating voltage range	5.5	—	35	V	
V _{clamp 1}	V _{CC} to OUT clamp voltage 1	50	55	—		I _d = 10mA (see Fig.1 & 2)
V _{clamp 2}	V _{CC} to OUT clamp voltage 2	—	56	65		I _d = I _{sd} (see Fig.1 & 2)
V _f	Body diode forward voltage	—	0.9	1.2		I _d = 2.5A, V _{in} = 0V
I _{CC} off	Supply current when OFF	—	16	50	μA	V _{in} = 0V, V _{out} = 0V
I _{CC} on	Supply current when ON	—	0.6	2	mA	V _{in} = 5V
I _{CC} ac	Ripple current when ON (AC RMS)	—	20	—	μA	V _{in} = 5V
V _{dgl}	Low level diagnostic output voltage	—	0.15	0.4	V	I _{dg} = 1.6 mA
I _{oh}	Output leakage current	—	50	120	μA	V _{out} = 6V
I _{ol}	Output leakage current	0	—	25		V _{out} = 0V
I _{dg} leakage	Diagnostic output leakage current	—	—	10		V _{dg} = 5.5V
V _{ih}	IN high threshold voltage	—	2.2	3	V	
V _{il}	IN low threshold voltage	1	1.9	—		
I _{in} on	On state IN positive current	—	70	200	μA	V _{in} = 5V
I _n hyst	Input hysteresis	0.1	0.25	0.5	V	

Switching Electrical Characteristics

V_{CC} = 14V, Resistive Load = 2.8Ω, T_J = 25°C, (unless otherwise specified).

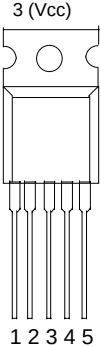
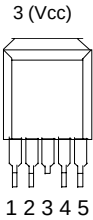
Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
T _{don}	Turn-on delay time	—	10	40	μs	See figure 3
T _{r1}	Rise time to V _{out} = V _{CC} - 5V	—	25	60		
T _{r2}	Rise time V _{CC} - 5V to V _{out} = 90% of V _{CC}	—	130	200		
dV/dt (on)	Turn ON dV/dt	—	0.7	2	V/μs	
E _{on}	Turn ON energy	—	1500	—	μJ	
T _{doff}	Turn-off delay time	—	35	70	μs	See figure 4
T _f	Fall time to V _{out} = 10% of V _{CC}	—	25	50		
dV/dt (off)	Turn OFF dV/dt	—	0.9	3	V/μs	
E _{off}	Turn OFF energy	—	250	—	μJ	
T _{diag}	V _{out} to V _{diag} propagation delay	—	5	15	μs	See figure 6

Protection Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
I _{lim}	Internal current limit	7	10	14	A	V _{out} = 0V
T _{sd+}	Over-temp. positive going threshold	—	165	—	°C	See fig. 2
T _{sd-}	Over-temp. negative going threshold	—	158	—		See fig. 2
V _{sc}	Short-circuit detection voltage (3)	2	3	4	V	See fig. 2
V _{open load}	Open load detection threshold	2	3	4		

(3) Referenced to V_{CC}

Lead Assignments

	
5 Lead - TO220	5 Lead - D ² PAK (SMD220)
IPS521	IPS521S
Part Number	

Functional Block Diagram

All values are typical

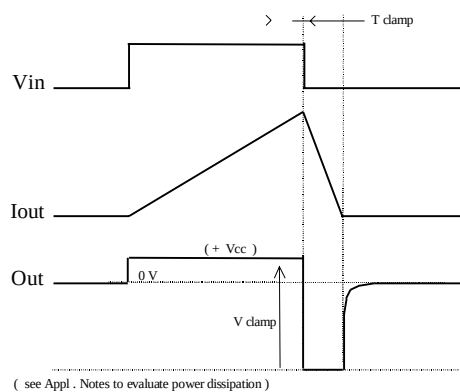
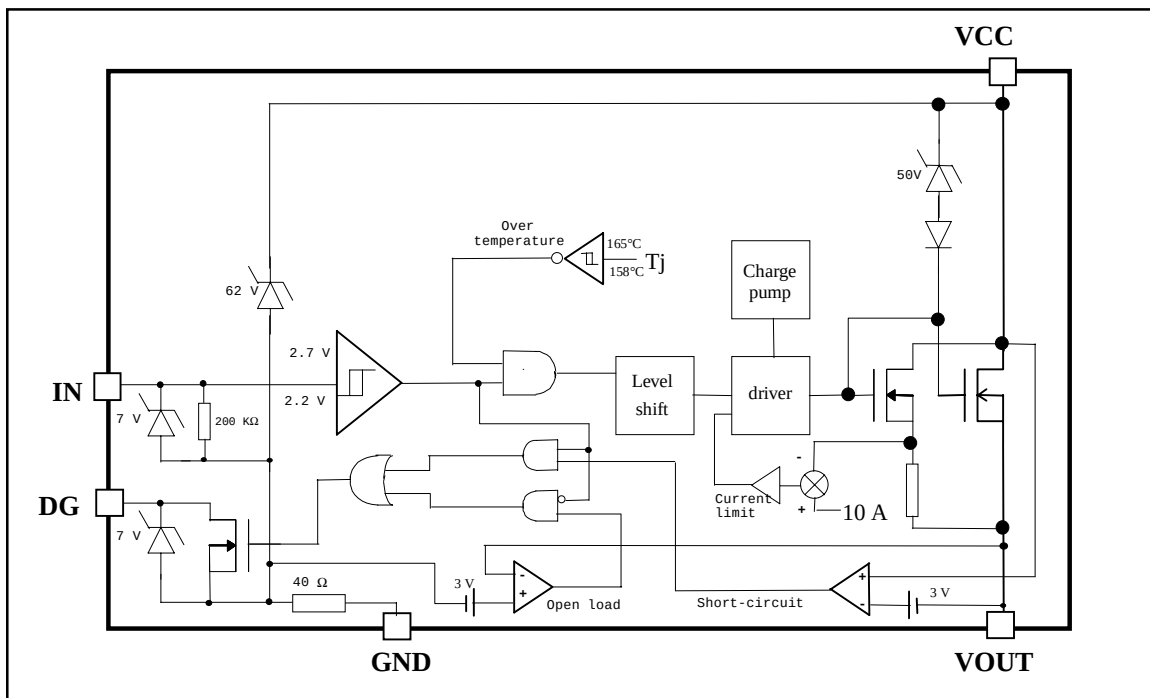


Figure 1 - Active clamp waveforms

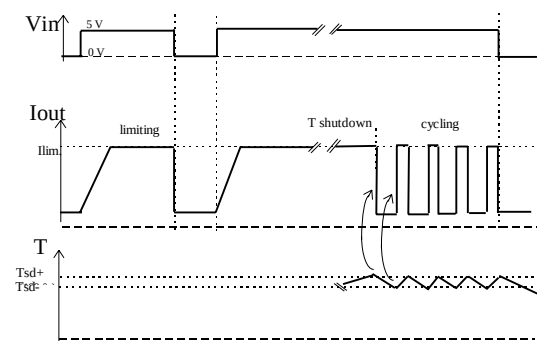


Figure 2 - Protection timing diagram

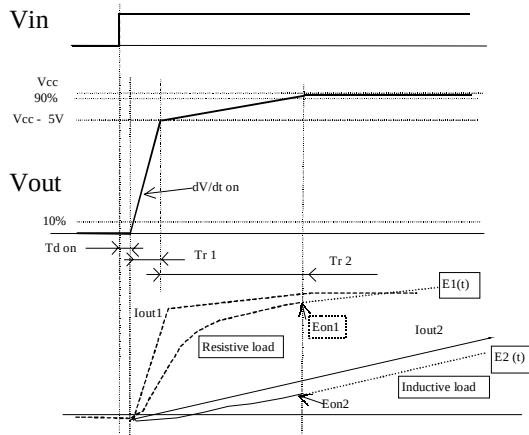


Figure 3 - Switching times definition (turn-on)
Turn on energy with a resistive or an inductive load

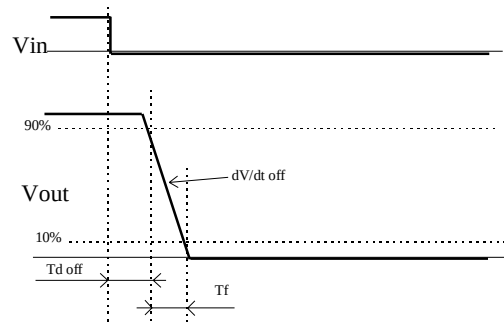


Figure 4 - Switching times definition (turn-off)

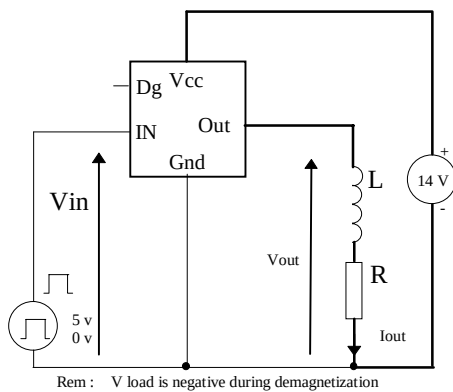


Figure 5 - Active clamp test circuit

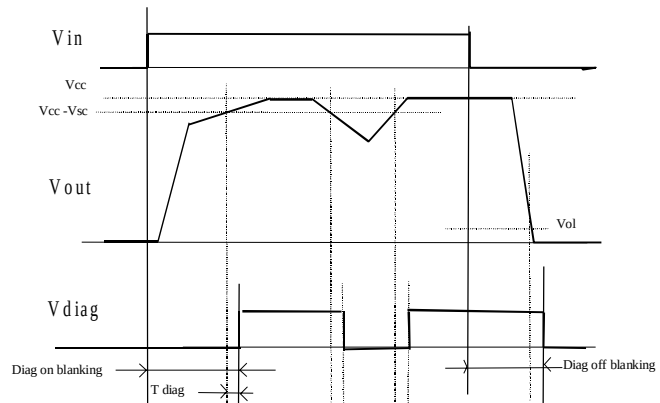


Figure 6 - Diagnostic delay definitions

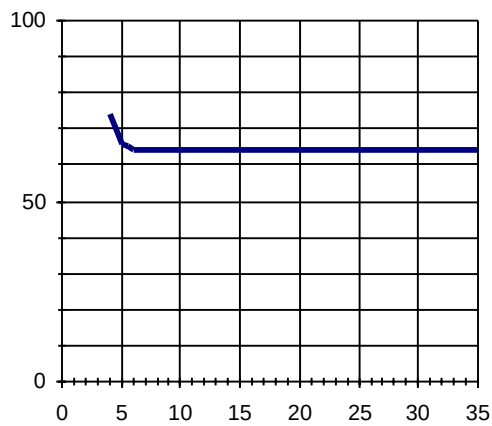


Figure 7 - R_{ds(on)} (mΩ) Vs V_{cc} (V)

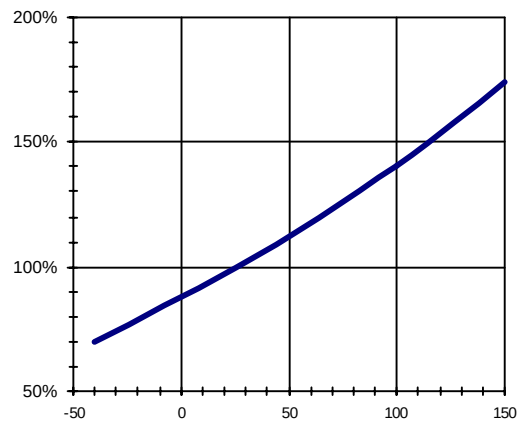


Figure 8 - Normalized R_{ds(on)} (%) Vs T_j (°C)

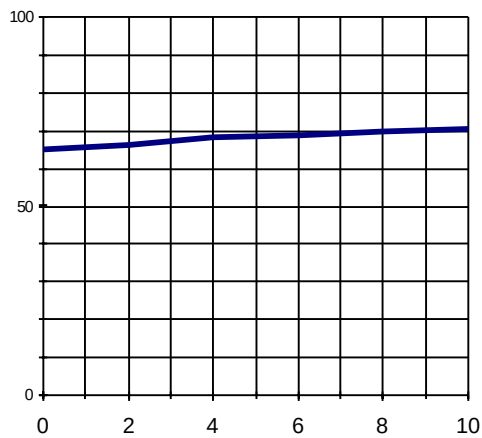


Figure 9 - R_{ds(on)} (mΩ) Vs I_{out} (A)

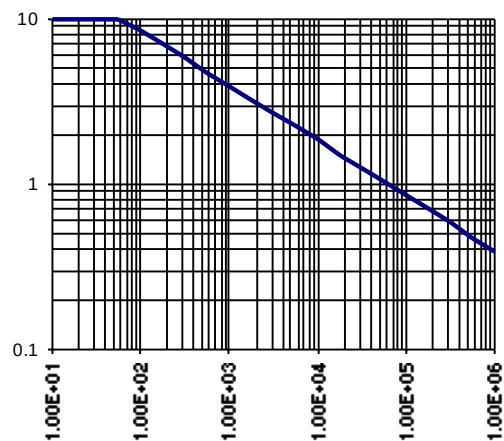


Figure 10 - Max. I_{out} (A) Vs Load Inductance (μH)

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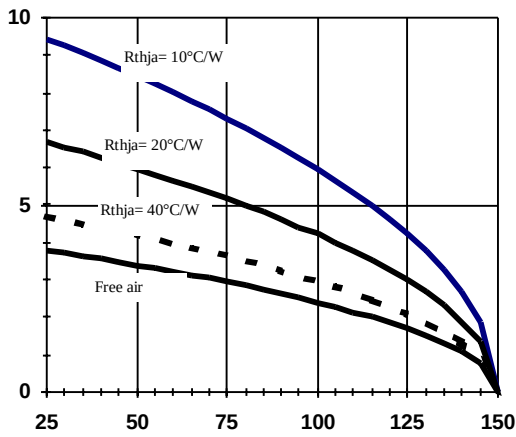


Figure 11a - Max load current (A) Vs Tamb (°C)
IPS521

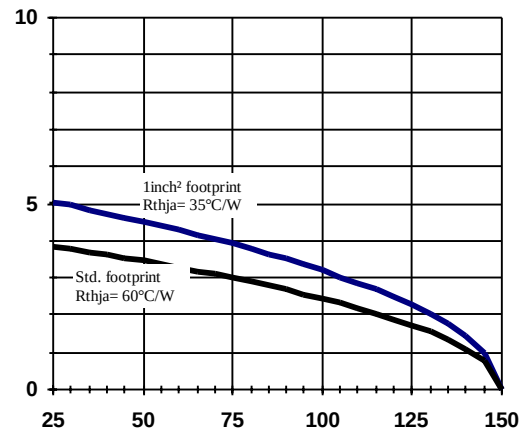


Figure 11b - Max load current (A) Vs Tamb (°C)
IPS521S

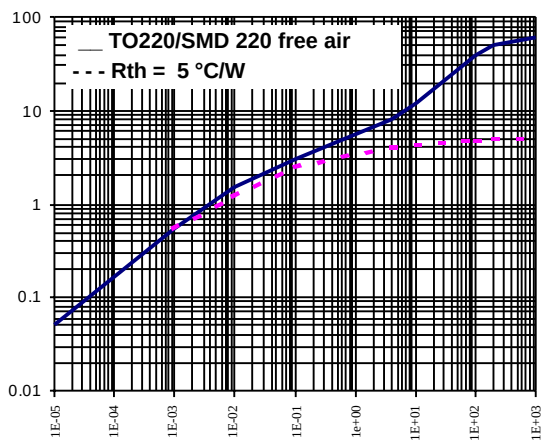


Figure 12 - Transient Thermal Impedance (°C/W)
Vs Time (S)

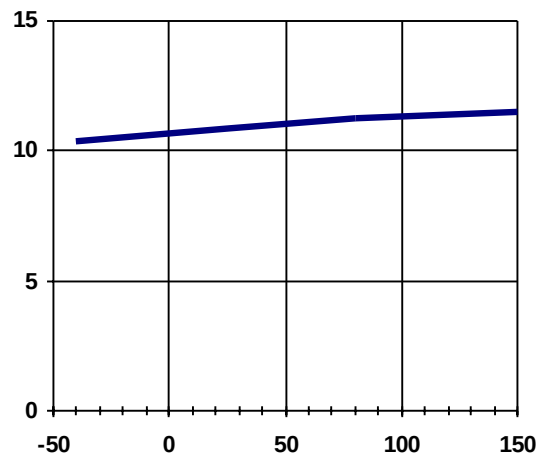


Figure 13 - I_{lim} (A) Vs T_J (°C)

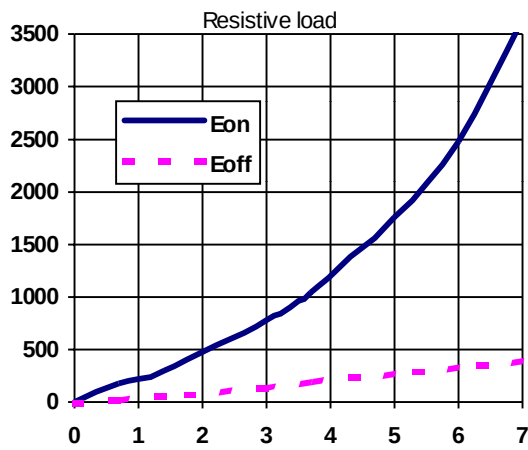


Figure 14 - E_{on} , E_{off} (μJ) Vs I_{out} (A)

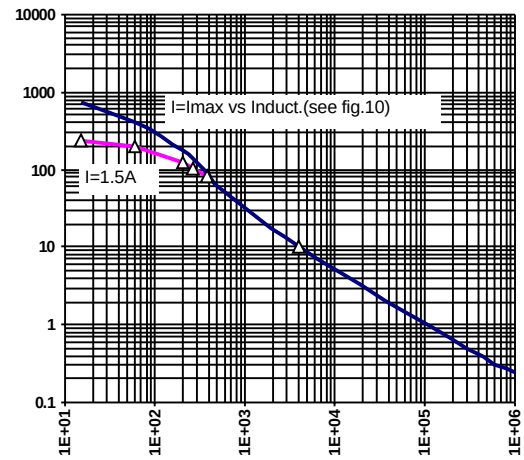


Figure 15 - E_{on} (μJ) Vs Load Inductance (μH)
(see Fig. 3)

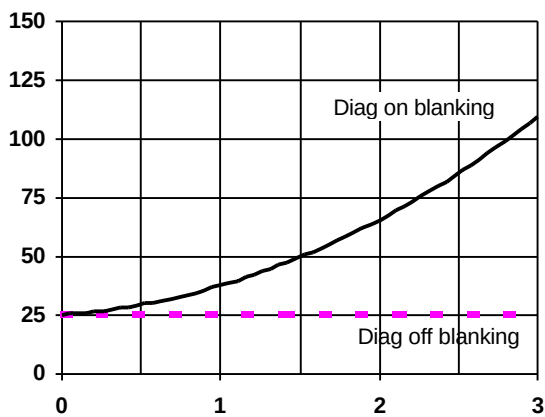


Figure 16 - Diag Blanking time (μS) Vs I_{out} (A)
(resistive load - see Fig. 6)

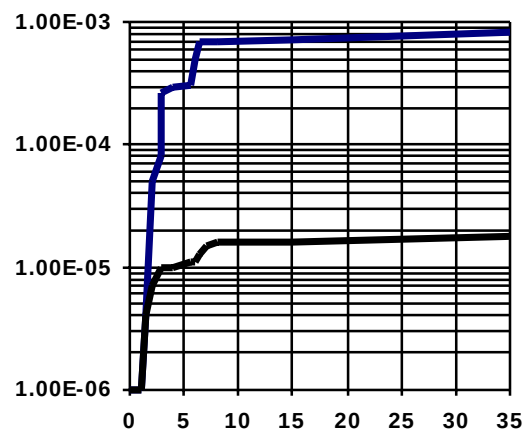


Figure 17 - I_{CC} (mA) Vs V_{CC} (V)

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[illegible]

Technical drawing of a circular metal part, likely a flange or end plate, showing multiple views and dimensions in millimeters and inches.

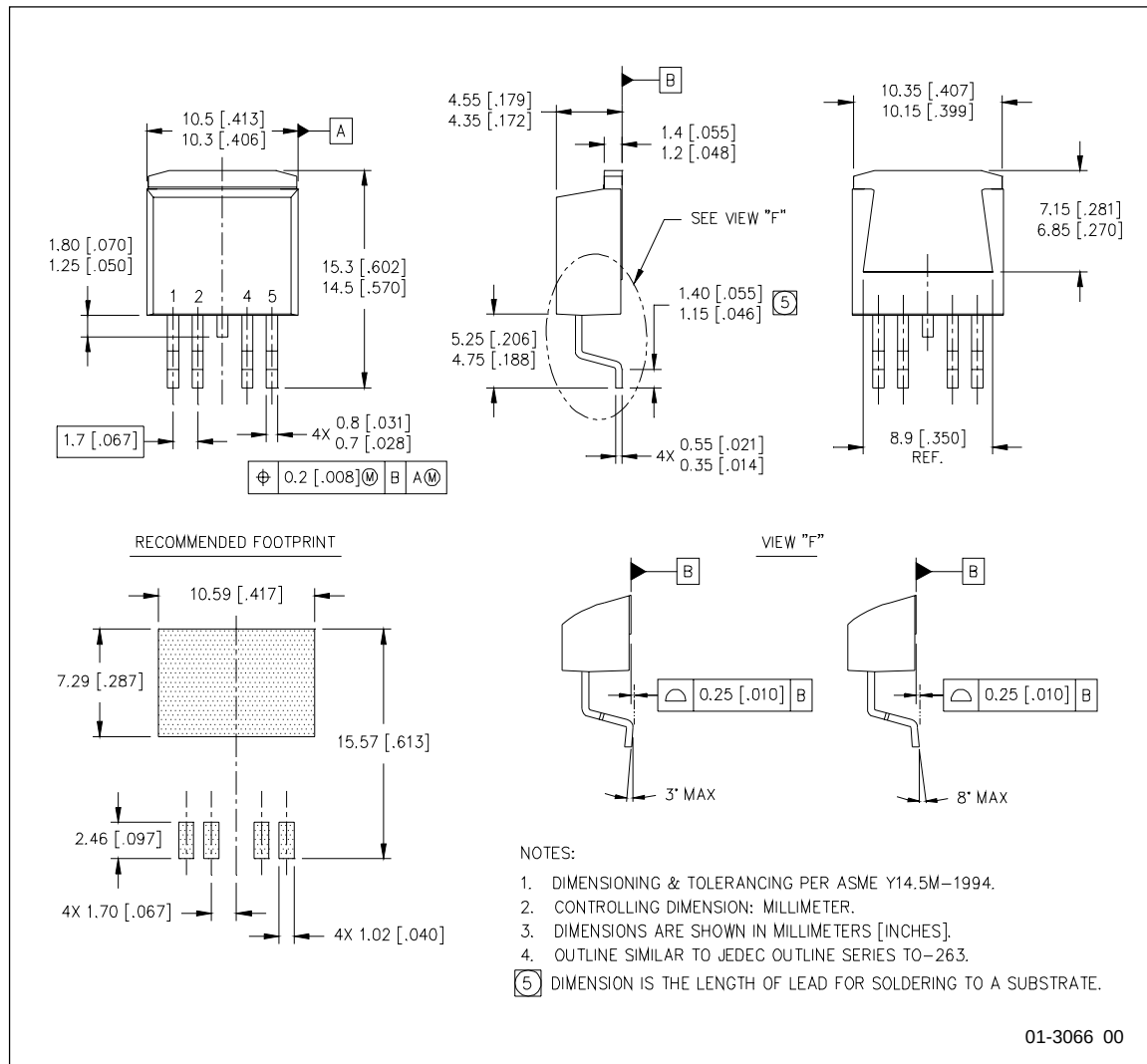
Views:

- Top View:** Shows a circular part with a central hole and three large triangular cutouts. Dimensions include:
 - Overall diameter: $\phi 360.00$ [14.173] MAX.
 - Central hole diameter: $\phi 13.52$ [.532] / $\phi 12.80$ [.504]
 - Distance from center to the start of the triangular cutouts: 10.90 [.429] / 10.70 [.421]
 - Distance from the start of the cutouts to the outer edge: 16.10 [.634]
- Front View:** Shows the profile of the part with a central raised section and two side flanges. Dimensions include:
 - Top flange thickness: 4.10 [.161] / 3.90 [.153]
 - Central raised section height: 1.60 [.063] / 1.50 [.059]
 - Side flange thickness: 1.85 [.073] / 1.65 [.065]
 - Side flange outer diameter: $\phi 1.60$ [.063] / $\phi 1.50$ [.059]
 - Side flange inner diameter: $\phi 1.75$ [.069] / $\phi 1.25$ [.049]
 - Side flange width: 11.60 [.457] / 11.40 [.449]
 - Side flange height: 15.42 [.609] / 15.22 [.601]
 - Side flange outer radius: 24.30 [.957] / 23.90 [.941]
 - Side flange inner radius: 4.72 [.186] / 4.52 [.178]
- Side View:** Shows the profile of the part with a central raised section and two side flanges. Dimensions include:
 - Top flange thickness: 0.368 [.0145] / 0.342 [.0135]
 - Side flange thickness: 15.42 [.609] / 15.22 [.601]
 - Side flange outer radius: 24.30 [.957] / 23.90 [.941]
 - Side flange inner radius: 4.72 [.186] / 4.52 [.178]
- Bottom View:** Shows the profile of the part with a central raised section and two side flanges. Dimensions include:
 - Central raised section height: 1.60 [.063] / 1.50 [.059]
 - Side flange thickness: 1.85 [.073] / 1.65 [.065]
 - Side flange outer diameter: $\phi 1.60$ [.063] / $\phi 1.50$ [.059]
 - Side flange inner diameter: $\phi 1.75$ [.069] / $\phi 1.25$ [.049]
 - Side flange width: 11.60 [.457] / 11.40 [.449]
 - Side flange height: 15.42 [.609] / 15.22 [.601]
 - Side flange outer radius: 24.30 [.957] / 23.90 [.941]
 - Side flange inner radius: 4.72 [.186] / 4.52 [.178]

Notes:

1. OUTLINE CONFORMS TO EIA-481 & EIA-541.
2. DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].

Case Outline 5 Lead - D²PAK (SMD220)



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Data and specifications subject to change without notice. 3/27/2000

Note: For the most current drawings please refer to the IR website at:
<http://www.irf.com/package/>