



Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance,	D		0.9	
junction – case	R_{thJC}		0.9	
Diode thermal resistance,	Б		1 5	K/W
junction – case	R_{thJCD}		1.5	
Thermal resistance,	D		40	
junction – ambient	R_{thJA}		40	

Electrical Characteristic, at $T_i = 25$ °C, unless otherwise specified

Danamatan	Cumbal	Canditiana	Value			I Imit	
Parameter	Symbol	Conditions	min.	Тур.	max.	Unit	
Static Characteristic	•						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{\rm GE} = 0 \text{V}, I_{\rm C} = 0.2 \text{mA}$	600	-	-		
Collector-emitter saturation voltage		$V_{\rm GE} = 15 \rm V, \ I_{\rm C} = 20 \rm A$					
	$V_{\text{CE(sat)}}$	<i>T</i> _j =25°C	-	1.5	2.05		
		<i>T</i> _j =175°C	-	1.9	-		
Diode forward voltage		$V_{GE} = 0V, I_{F} = 20A$				V	
	V_{F}	<i>T</i> _j =25°C	-	1.65	2.05		
		<i>T</i> _j =175°C	-	1.6	-		
Gate-emitter threshold voltage	$V_{\rm GE(th)}$	$I_{\rm C} = 290 \mu {\rm A}, V_{\rm CE} = V_{\rm GE}$	4.1	4.9	5.7		
Zero gate voltage collector current		$V_{CE} = 600 \text{V}, \ V_{GE} = 0 \text{V}$					
	I _{CES}	<i>T</i> _j =25°C	-	-	40	μΑ	
		<i>T</i> _j =175°C	-	-	1500		
Gate-emitter leakage current	I _{GES}	$V_{\text{CE}}=0\text{V}, V_{\text{GE}}=20\text{V}$	-	-	100	nA	
Transconductance	g_{fs}	$V_{CE} = 20V, I_{C} = 20A$	-	11	-	S	
Integrated gate resistor	R _{Gint}			-		Ω	

Dynamic Characteristic

Input capacitance	Ciss	V _{CE} =25V,	-	1100	-	
Output capacitance	Coss	$V_{GE}=0V$,	-	71	-	pF
Reverse transfer capacitance	Crss	f=1MHz	-	32	-	
Gate charge	Q _{Gate}	$V_{CC} = 480 \text{ V}, I_{C} = 20 \text{ A}$ $V_{GE} = 15 \text{ V}$	-	120	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	LE	PG-TO247-3	-	13	-	nH
Short circuit collector current ¹⁾	I _{C(SC)}	$V_{\text{GE}} = 15 \text{V}, t_{\text{SC}} \le 5 \mu \text{s}$ $V_{\text{CC}} = 400 \text{V},$ $T_{\text{j}} \le 150^{\circ} \text{C}$	-	183.3	-	А

 $^{^{1)}}$ Allowed number of short circuits: <1000; time between short circuits: >1s.





Switching Characteristic, Inductive Load, at T_j =25 °C

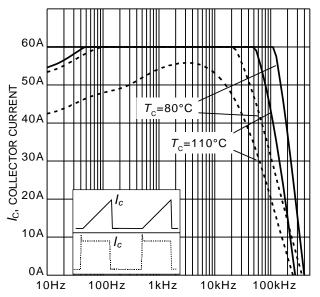
Dovementor	Symbol Conditions		Value			11
Parameter	Symbol	Conditions	min.	Тур.	max.	Unit
IGBT Characteristic	•					
Turn-on delay time	$t_{d(on)}$	T _j =25°C,	-	18	-	
Rise time	t_{r}	$V_{CC} = 400 \text{ V}, I_{C} = 20 \text{ A},$ $V_{GE} = 0/15 \text{ V}, I_{G} = 12 \Omega,$	-	14	-	ns
Turn-off delay time	$t_{d(off)}$	L_{σ} =131nH, C_{σ} =31pF	-	199	-	
Fall time	t_{f}		-	42	-	
Turn-on energy	Eon	L_{σ} , C_{σ} from Fig. E Energy losses include	-	0.31	-	mJ
Turn-off energy	E _{off}	"tail" and diode reverse	-	0.46	-	
Total switching energy	E _{ts}	recovery.	-	0.77	-	
Anti-Parallel Diode Characteristic	•					
Diode reverse recovery time	t_{rr}	<i>T</i> _j =25°C,	-	41	-	ns
Diode reverse recovery charge	Q _{rr}	V_{R} =400V, I_{F} =20A,	-	0.31	-	μC
Diode peak reverse recovery current	I _{rrm}	$di_{\rm F}/dt$ =880A/ μ s	-	13.3	-	Α
Diode peak rate of fall of reverse recovery current during $t_{\rm b}$	di _{rr} /dt		-	711	-	A/μs

Switching Characteristic, Inductive Load, at T_j =175 °C

Donomoton	Cumbal	Conditions	Value			l lmi4
Parameter	Symbol Conditions		min.	Тур.	max.	Unit
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	T _j =175°C,	-	18	-	
Rise time	t _r	$V_{CC} = 400 \text{ V}, I_{C} = 20 \text{ A},$ $V_{GE} = 0/15 \text{ V}, I_{G} = 12 \Omega,$	-	18	-	ns
Turn-off delay time	$t_{d(off)}$	L_{σ} =131nH, C_{σ} =31pF	-	223	-	
Fall time	t _f]	-	76	-	
Turn-on energy	Eon	L_{σ} , C_{σ} from Fig. E Energy losses include	-	0.51	-	mJ
Turn-off energy	E _{off}	"tail" and diode reverse	-	0.64	-	
Total switching energy	Ets	recovery.	-	1.15	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t _{rr}	<i>T</i> _j =175°C	-	176	-	ns
Diode reverse recovery charge	Q _{rr}	V_{R} =400V, I_{F} =20A,	-	1.46	-	μC
Diode peak reverse recovery current	I _{rrm}	<i>di_F/dt</i> =880A/μs	-	18.9	-	Α
Diode peak rate of fall of reverse recovery current during $t_{\rm b}$	di _{rr} /dt		-	467	-	A/μs

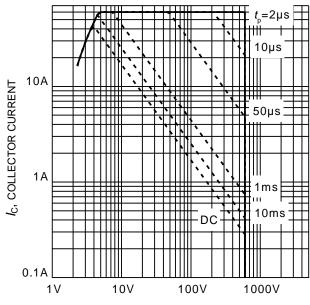






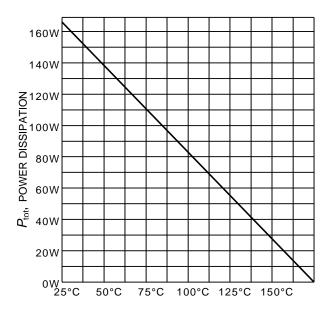
f, SWITCHING FREQUENCY

Figure 1. Collector current as a function of switching frequency $(T_j \le 175^{\circ}\text{C}, D = 0.5, V_{\text{CE}} = 400\text{V}, V_{\text{GE}} = 0/15\text{V}, r_{\text{G}} = 12\Omega)$



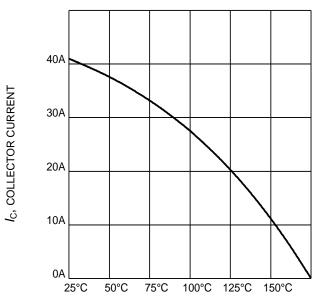
 V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 2. Safe operating area $(D=0, T_C=25^{\circ}\text{C}, T_j \leq 175^{\circ}\text{C}; V_{GE}=0/15\text{V})$



 $T_{\rm C}$, CASE TEMPERATURE Figure 3. Power dissipation as a function of case temperature

 $(T_i \le 175^{\circ}C)$



 $T_{\rm C}$, CASE TEMPERATURE

Figure 4. Collector current as a function of case temperature

 $(V_{GE} \ge 15V, T_{j} \le 175^{\circ}C)$





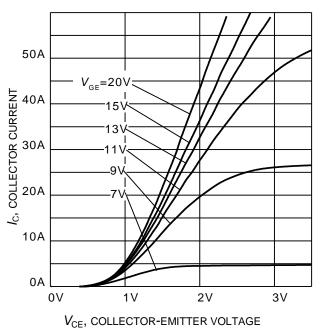


Figure 5. Typical output characteristic $(T_i = 25^{\circ}C)$

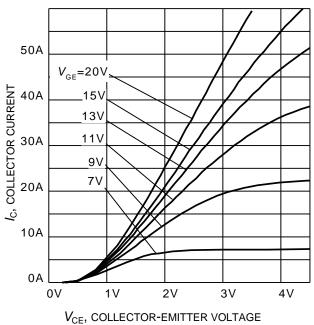


Figure 6. Typical output characteristic $(T_i = 175^{\circ}C)$

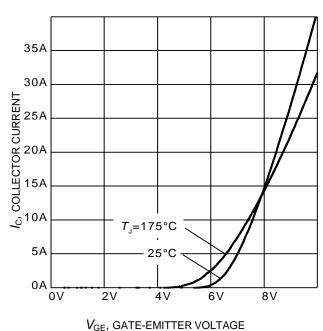
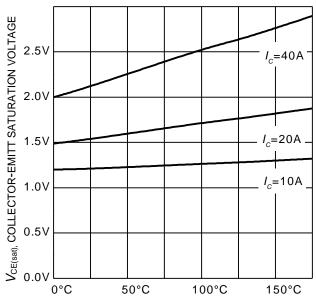


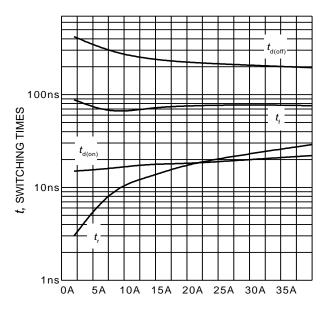
Figure 7. Typical transfer characteristic $(V_{CE}=10V)$



 $T_{\rm J}$, JUNCTION TEMPERATURE Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature $(V_{\rm GE}=15\rm V)$

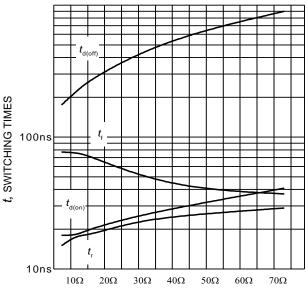






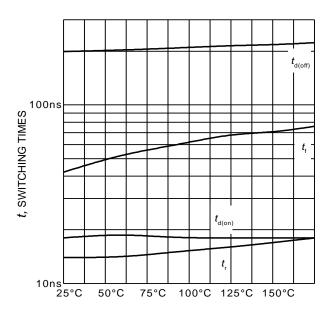
 $I_{\rm C}$, COLLECTOR CURRENT

Figure 9. Typical switching times as a function of collector current (inductive load, T_J =175°C, V_{CE} = 400V, V_{GE} = 0/15V, r_G = 12 Ω , Dynamic test circuit in Figure E)



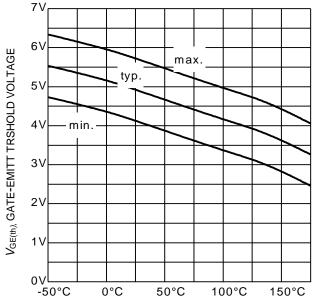
R_G, GATE RESISTOR

Figure 10. Typical switching times as a function of gate resistor (inductive load, T_J = 175°C, V_{CE} = 400V, V_{GE} = 0/15V, I_C = 20A, Dynamic test circuit in Figure E)



 $T_{\rm J}$, JUNCTION TEMPERATURE

Figure 11. Typical switching times as a function of junction temperature (inductive load, $V_{\text{CE}} = 400\text{V}$, $V_{\text{GE}} = 0/15\text{V}$, $I_{\text{C}} = 20\text{A}$, $I_{\text{C}} = 20\text{A}$, Dynamic test circuit in Figure E)

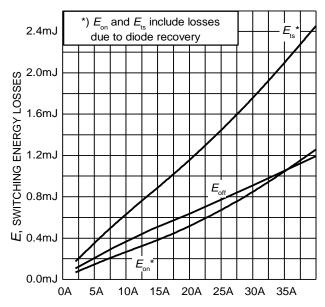


 $T_{\rm J}$, JUNCTION TEMPERATURE

Figure 12. Gate-emitter threshold voltage as a function of junction temperature $(I_C = 0.29 \text{mA})$

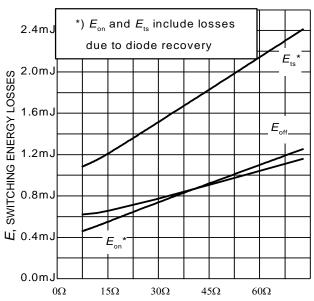






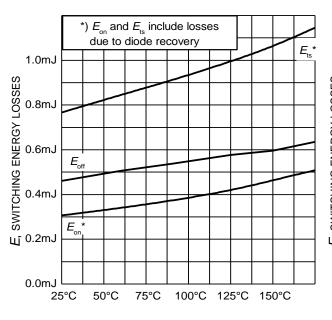
 I_{C} , COLLECTOR CURRENT

Figure 13. Typical switching energy losses as a function of collector current (inductive load, $T_J = 175$ °C, $V_{CE} = 400$ V, $V_{GE} = 0/15$ V, $r_G = 12\Omega$, Dynamic test circuit in Figure E)



R_G, GATE RESISTOR

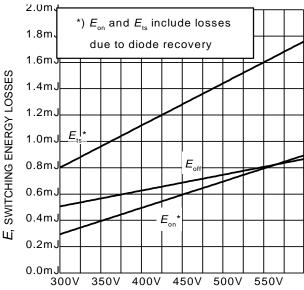
Figure 14. Typical switching energy losses as a function of gate resistor (inductive load, $T_J = 175$ °C, $V_{CE} = 400$ V, $V_{GE} = 0/15$ V, $I_C = 20$ A, Dynamic test circuit in Figure E)



 T_{J} , JUNCTION TEMPERATURE

Figure 15. Typical switching energy losses as a function of junction temperature

(inductive load, $V_{\rm CE}$ = 400V, $V_{\rm GE}$ = 0/15V, $I_{\rm C}$ = 20A, $I_{\rm G}$ = 12 Ω , Dynamic test circuit in Figure E)



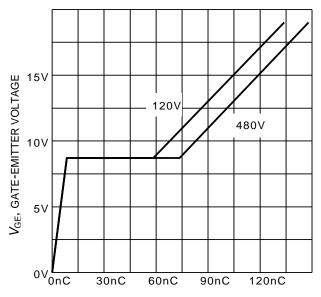
 V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 16. Typical switching energy losses as a function of collector emitter voltage

(inductive load, T_J = 175°C, V_{GE} = 0/15V, I_C = 20A, r_G = 12 Ω , Dynamic test circuit in Figure E)

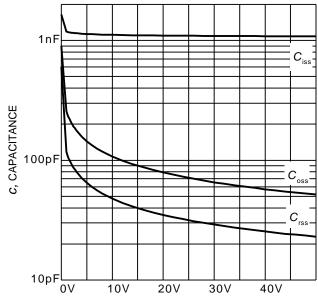






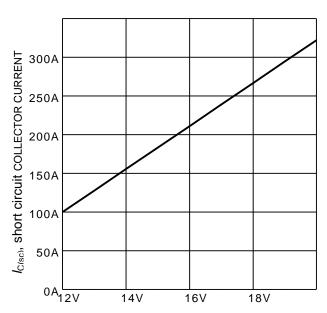
 $Q_{\rm GE}$, GATE CHARGE

Figure 17. Typical gate charge $(I_C=20 \text{ A})$



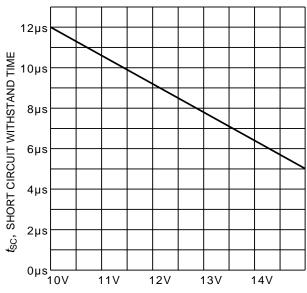
 $V_{\rm CE}$, COLLECTOR-EMITTER VOLTAGE

Figure 18. Typical capacitance as a function of collector-emitter voltage $(V_{GE}=0V, f=1 \text{ MHz})$



 $V_{\rm GE}$, gate-emittetr voltage

Figure 19. Typical short circuit collector current as a function of gate-emitter voltage $(V_{CE} \le 400 \text{V}, T_i \le 150 ^{\circ}\text{C})$



 $V_{\rm GE}$, gate-emitetr voltage

Figure 20. Short circuit withstand time as a function of gate-emitter voltage (V_{CE} =400V, start at T_{J} =25°C, T_{Jmax} <150°C)





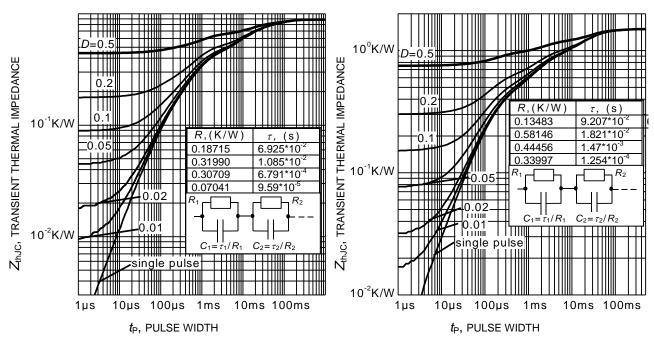


Figure 21. IGBT transient thermal impedance $(D = t_p / T)$

Figure 22. Diode transient thermal impedance as a function of pulse width $(D=t_P/T)$

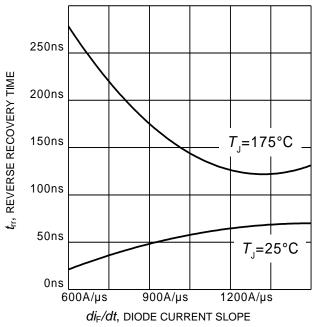
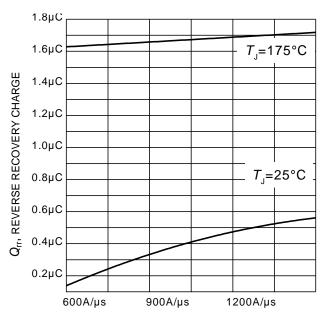


Figure 23. Typical reverse recovery time as a function of diode current slope $(V_R=400V, I_F=20A,$ Dynamic test circuit in Figure E)



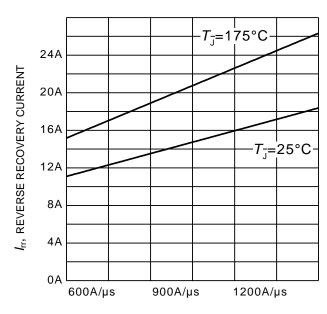
di_F/dt, DIODE CURRENT SLOPE

Figure 24. Typical reverse recovery charge as a function of diode current slope

($V_R = 400V$, $I_F = 20A$, Dynamic test circuit in Figure E)







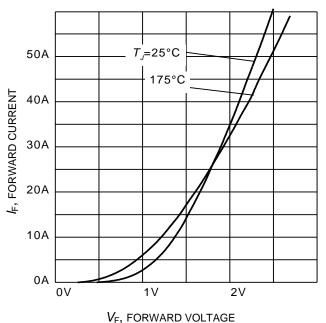
-750A/μs

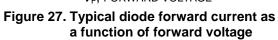
di_F/dt, DIODE CURRENT SLOPE

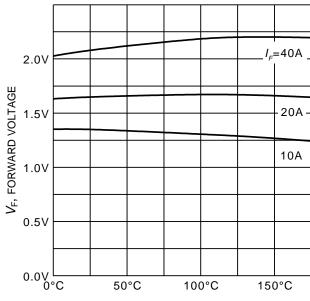
Figure 25. Typical reverse recovery current as a function of diode current slope

 $(V_R = 400V, I_F = 20A,$ Dynamic test circuit in Figure E) di_F/dt, DIODE CURRENT SLOPE

Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope (V_R=400V, I_F=20A, Dynamic test circuit in Figure E)



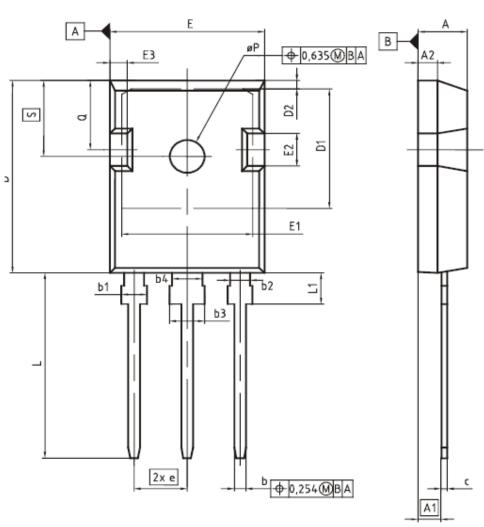




T_J, JUNCTION TEMPERATURE
 Figure 28. Typical diode forward voltage as a function of junction temperature



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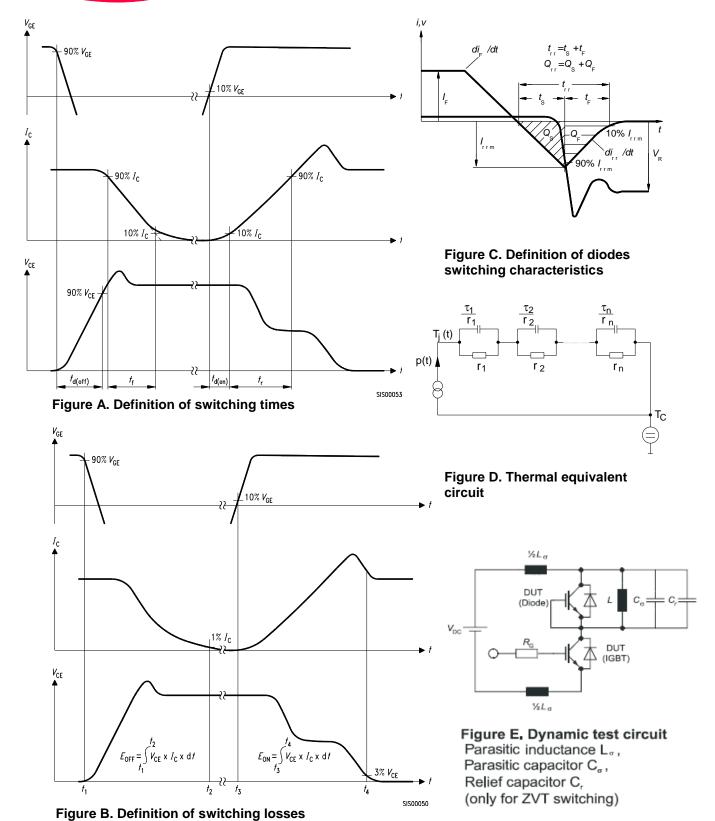


DBM	MILLIM	ETERS	NC	HES
DIM	MIN	MAX	MIN	MAX
A	4.83	5,21	0.190	0,205
A1	2,27	2,54	0.089	0.100
A2	1.85	2.16	0,073	0,085
b	1.07	1.33	0,042	0.052
b1	1.90	2.41	0,075	0.095
b2	1.90	2.16	0.075	0.085
b3	2,87	3,38	0.113	0.133
b4	2,87	3.13	0.113	0.123
С	0.55	0.68	0,022	0,027
D	20,80	21,10	0,819	0.831
D1	16,25	17.65	0,640	0,695
D2	0.95	1.35	0.037	0.053
E	15.70	16.13	0,618	0.635
E1	13.10	14.15	0,516	0,557
E2	3,68	5.10	0.145	0,201
E3	1.00	2,60	0,039	0.102
e	5.	44 (BSC)	0.2	214 (BSC)
N		3		3
L	19,80	20,32	0.780	0.800
L1	4.10	4.47	0.161	0.176
øΡ	3,50	3,70	0.138	0.146
Q	5.49	6.00	0,216	0,236
s	6.04	6.30	0.238	0,248

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0 5 5 7.5mm
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