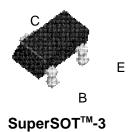


# **FMMT449**



## **NPN Low Saturation Transistor**

These devices are designed with high current gain and low saturation voltage with collector currents up to 2A continuous. Sourced from Process NB.

Absolute Maximum Ratings* TA	= 25°C unless otherwise not
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Symbol	Parameter	FMMT449	Units
Оуппоот	i didilicici	1 111111 1 4 4 0	Oilles
$V_{\text{CEO}}$	Collector-Emitter Voltage	30	V
V <sub>CBO</sub>	Collector-Base Voltage	50	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
Ic	Collector Current - Continuous	1	Α
10	- Peak Pulse Current	2	
T <sub>J, T<sub>stg</sub></sub> Operating and Storage Junction Temperature Range		-55 to +150	°C

<sup>\*</sup>These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### NOTES:

- 1) These ratings are based on a maximum junction temperature of 150°C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

# Thermal Characteristics TA = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		FMMT449	
P <sub>D</sub>	Total Device Dissipation* Derate above 25°C	500 4	mW mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	250	°C/W

<sup>\*</sup>Device mounted on FR-4 PCB 4.5" X 5"; mounting pad 0.02 in<sup>2</sup> of 2oz copper.

AIDAI I	0 - 1	<b></b>	
INPN L	.ow Satura	ation I ra	nsistor

(continued)

## **Electrical Characteristics**

Symbol	Parameter	Test Conditions	Min	Max	Units
OFF CHAI	RACTERISTICS				
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = 10 mA	30		V
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 1mA	50		V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = 100 μA	5		V
I <sub>CBO</sub>	Collector Cutoff Current	V <sub>CB</sub> = 40 V V <sub>CB</sub> = 40 V, Ta=100°C		100 10	nA uA
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 4V		100	nA
ON CHAR	ACTERISTICS*			Т	
h <sub>FE</sub>	DC Current Gain	$I_{C} = 50 \text{ mA}, V_{CE} = 2V$ $I_{C} = 500 \text{ mA}, V_{CE} = 2V$ $I_{C} = 1A, V_{CE} = 2V$ $I_{C} = 2A, V_{CE} = 2V$	70 100 80 40	300	-
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 1 A, I <sub>B</sub> = 100 mA I <sub>C</sub> = 2 A, I <sub>B</sub> = 200 mA	40	500	mV V
V <sub>BE(sat)</sub>	Base-Emitter Saturation Voltage	I <sub>C</sub> = 1 A, I <sub>B</sub> = 100 mA		1.25	V
V <sub>BE(on)</sub>	Base-Emitter On Voltage	I <sub>C</sub> = 1 A, V <sub>CE</sub> = 2 V		1	V
SMALL SI	GNAL CHARACTERISTICS				
C <sub>obo</sub>	Output Capacitance	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1MHz		15	pF
f <sub>T</sub>	Transition Frequency	I <sub>C</sub> = 50mA,V <sub>CE</sub> = 10 V, f=100MHz	150		MHz

\*Pulse Test: Pulse Width  $\leq$  300  $\mu$ s, Duty Cycle  $\leq$  2.0%

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FACT<sup>TM</sup> QS<sup>TM</sup>

 $\begin{array}{lll} \mathsf{FACT} \ \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} & \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} \\ \mathsf{FAST}^{\circledcirc} & \mathsf{Super} \mathsf{SOT^{\mathsf{TM}}}\text{--}3 \\ \mathsf{FASTr^{\mathsf{TM}}} & \mathsf{Super} \mathsf{SOT^{\mathsf{TM}}}\text{--}6 \\ \mathsf{GTO^{\mathsf{TM}}} & \mathsf{Super} \mathsf{SOT^{\mathsf{TM}}}\text{--}8 \\ \mathsf{Hi} \mathsf{SeC^{\mathsf{TM}}} & \mathsf{TinyLogic^{\mathsf{TM}}} \\ \end{array}$ 

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