Sim Models & Software

Sim Models



Verilog: 4Gb Mobile LPDDR3 2E0F (ZIP)

Verilog simulation model for 4Gb Mobile LPDDR3

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Updated: 09/07/2016



IBIS: 253-Ball 2E0F QDP Mobile LPDDR3 SDRAM (ZIP)

Rev 1.1 253-ball, QDP, Dual-Channel, LPDDR3 SDRAM

File Type: ZIP **Updated:** 09/02/2014

Mobile LPDDR3 Mobile Memory Solutions

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DRAM

Mobile LPDDR3

RoHS Certificates

RoHS Certificates

RoHS Certificate of Compliance (PDF)

Part-specific certification of how this product meets the requirements of the current DIRECTIVE 2002/95/EC, a.k.a. Restriction of Hazardous Substances (RoHS) Directive.

File Type: (PDF)

Updated: 01/2017

EDFA164A2MA-GD-F

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RoHS Certificates

China RoHS Certificate (PDF)

Part-specific certification as required by China's Management Methods for Controlling Pollution by

Electronic Information Products.

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Technical Notes

SEARCH (9) LPDRAM TECHNICAL NOTES

Technical Notes

Technical Note: Low-Power Function of Mobile RAM - Deep Power Down (DPD) (PDF)

DRAM LPDRAM

(E0598E21) This technical note describes Deep Power Down (DPD), one of the low-power functions that have been adapted to Mobile DRAM.

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Technical Note: Low-Power Function of Mobile RAM - Partial Array Self Refresh (PASR) (PDF)

DRAM LPDRAM

(E0597E10) This technical note describes Partial Array Self Refresh (PASR), one of the low-power functions that have been adapted to Mobile DRAM.

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Technical Note: Low-Power Function of Mobile RAM - Auto Temperature Compensated Self Refresh (ATCSR) (PDF)

DRAM **LPDRAM**

(E0599E20) This technical note describes Auto Temperature Compensated Self Refresh (ATCSR), one of the low-power functions that have been adapted to Mobile DRAM.

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Technical Notes

TN-00-01: Moisture Sensitivity of Plastic Packages (PDF)

DDR SDRAM DDR2 SDRAM **DDR3 SDRAM** DDR3L-RS See More Tags

(TN-00-01) This technical note describes shipping procedures for preventing memory devices from absorbing moisture and recommendations for baking devices exposed to excessive moisture.

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Technical Notes

TN-46-23: Backward Compatibility for Faster LPDDR SDRAM (PDF)

DRAM **LPDRAM Mobile Memory Solutions**

(TN-46-23) This technical note reviews the timing differences between the faster and slower LPDDR SDRAM speeds and explains how faster parts function compatibly with slower parts.

Updated: 02/04/2013

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Parts with the same Data Sheet (3)

EDFA164A2MA-GD-F (Current)		EDFA164A2MA-JD-F	EDF8164A3MA-GD-F	EDF8164A3MA-JD-F		
Part Status	Production	Production	Contact Factory	Contact Factory		
Technology	LPDDR3	LPDDR3	LPDDR3	LPDDR3 8Gb Yes		
Density	16Gb	16Gb	8Gb			
RoHS	Yes	Yes	Yes			
Depth	256Mb	256Mb	128Mb	128Mb		
Width	x64	x64	x64	x64		
Voltage			1.2V	1.2V VFBGA		
Package			VFBGA			
Pin Count	253-ball	253-ball	253-ball	253-ball		
Clock Rate	800 MHz	933 MHz	800 MHz	933 MHz		
Data Rate	DDR3-1600	DDR3-1866	DDR3-1600	DDR3-1866		
Cycle Time						
Op. Temp.	-30C to +85C	-30C to +85C	-30C to +85C	-30C to +85C		
Brand	Micron	Micron	Micron	Micron		

Where to Buy

Orderable Parts

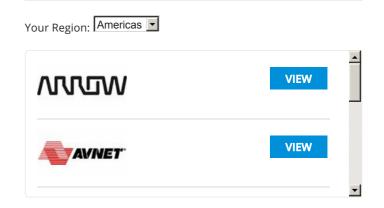
		Status	Media	FBGA Code	SPD Data	Chipset Validation	PLP	Start Date	Alternative Part
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