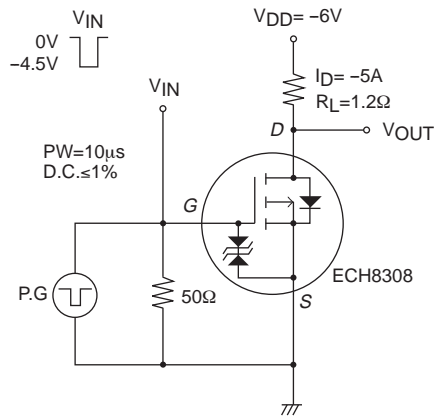


ECH8308

Electrical Characteristics at Ta=25°C

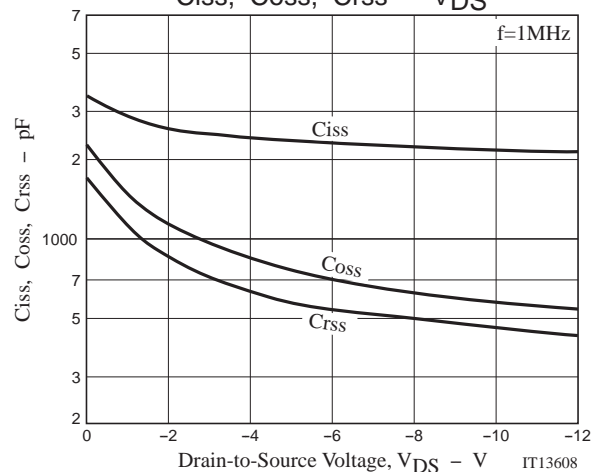
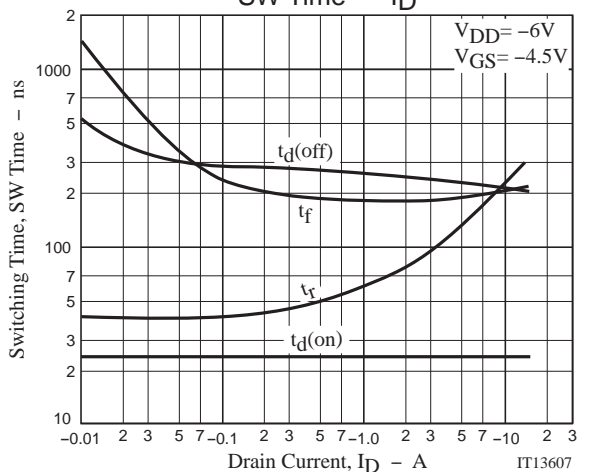
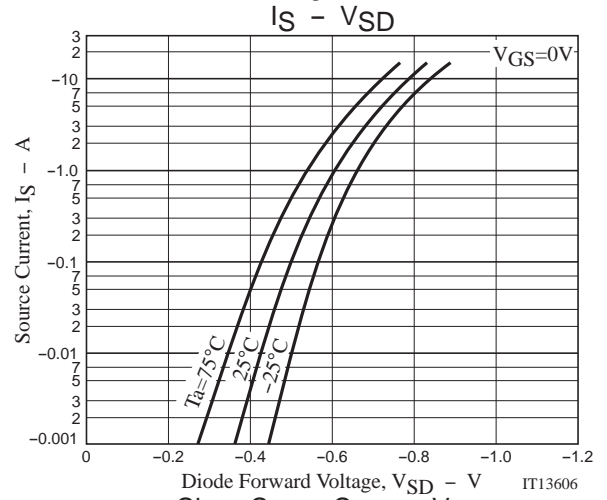
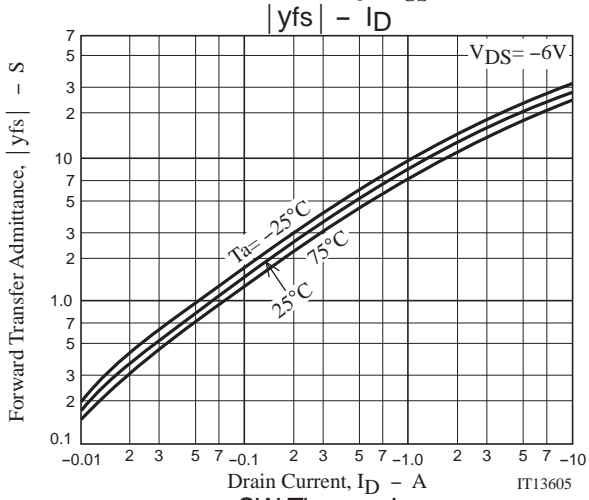
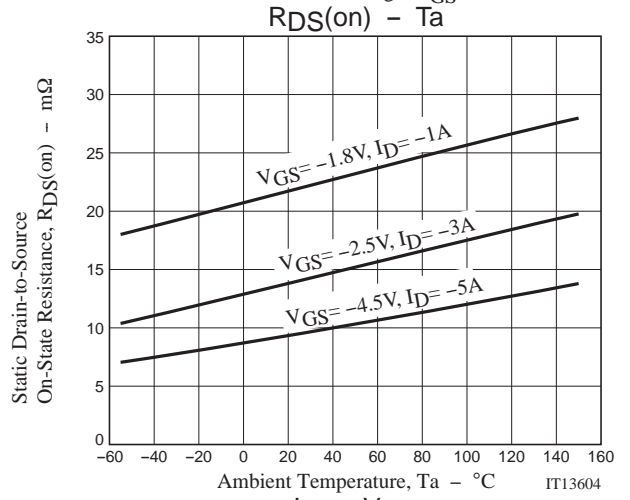
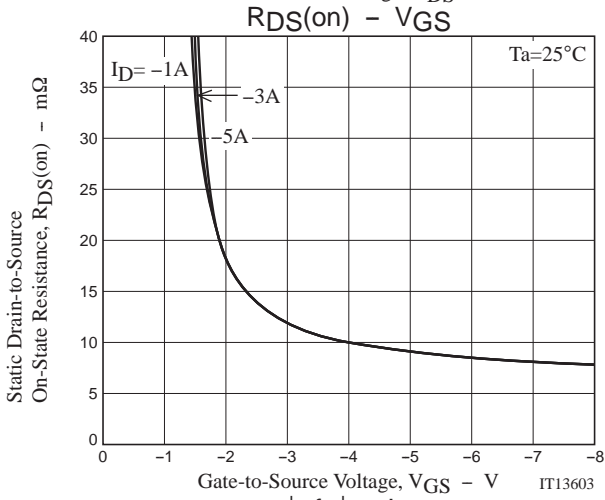
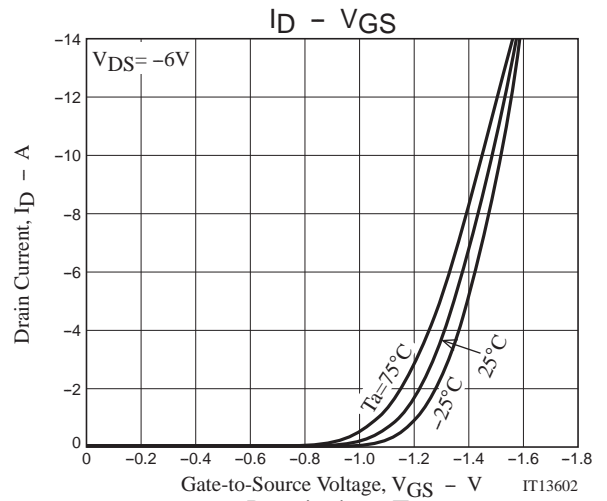
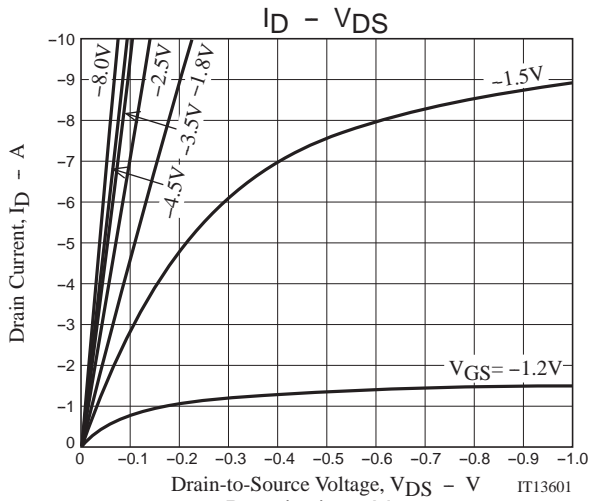
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =-1mA, V _{GS} =0V	-12			V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} =-12V, V _{GS} =0V			-10	μA
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0V			±10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} =-6V, I _D =-1mA	-0.4		-1.3	V
Forward Transfer Admittance	y _{fs}	V _{DS} =-6V, I _D =-4.5A	12	21		S
Static Drain-to-Source On-State Resistance	R _{DS(on)1}	I _D =-4.5A, V _{GS} =-4.5V		9.2	12.5	mΩ
	R _{DS(on)2}	I _D =-2A, V _{GS} =-2.5V		14	20	mΩ
	R _{DS(on)3}	I _D =-1A, V _{GS} =-1.8V		22	33	mΩ
Input Capacitance	C _{iss}	V _{DS} =-6V, f=1MHz		2300		pF
Output Capacitance	C _{oss}			720		pF
Reverse Transfer Capacitance	C _{rss}			550		pF
Turn-ON Delay Time	t _{d(on)}		See specified Test Circuit.		24	
Rise Time	t _r			130		ns
Turn-OFF Delay Time	t _{d(off)}			230		ns
Fall Time	t _f			195		ns
Total Gate Charge	Q _g	V _{DS} =-6V, V _{GS} =-4.5V, I _D =-10A			26	
Gate-to-Source Charge	Q _{gs}			4.0		nC
Gate-to-Drain "Miller" Charge	Q _{gd}			7.1		nC
Diode Forward Voltage	V _{SD}	I _S =-10A, V _{GS} =0V		-0.79	-1.2	V

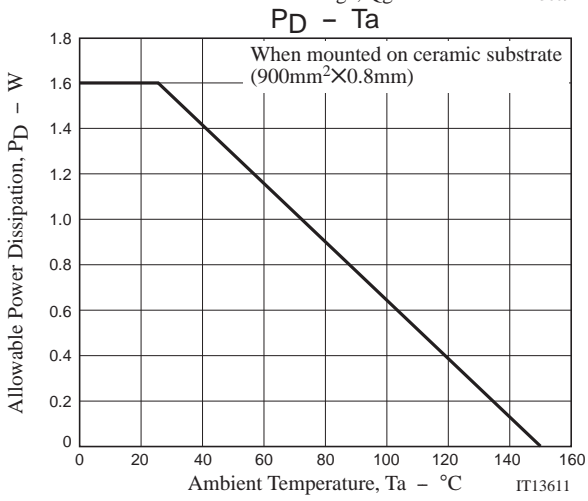
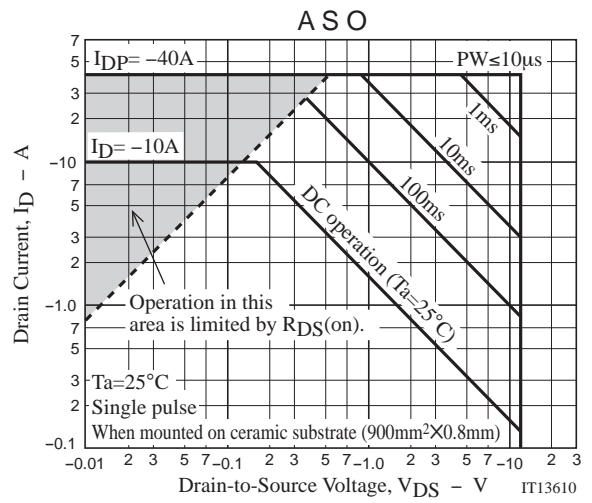
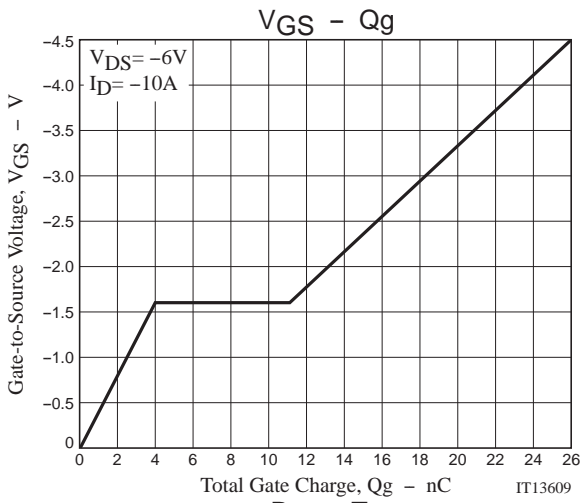
Switching Time Test Circuit



Ordering Information

Device	Package	Shipping	memo
ECH8308-TL-H	ECH8	3,000pcs./reel	Pb Free and Halogen Free





Embossed Taping Specification

ECH8308-TL-H

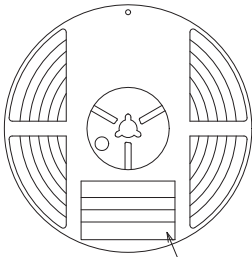
1. Packing Format

Package Name	Carrier Tape Type	Maximum Number of devices contained (pcs)			Packing format	
		Reel	Inner box	Outer box	Inner BOX (C-1)	Outer BOX (A-7)
ECH8	CPH6	3,000	15,000	90,000	5 reels contained Dimensions:mm (external) 183×72×185	6 inner boxes contained Dimensions:mm (external) 440×195×210

Reel label, Inner box label
(unit :mm)

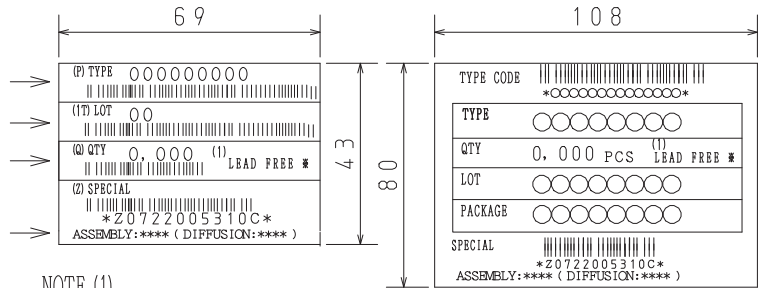
Outer box label
It is a label at the time of factory shipments.
The form of a label may change in physical distribution process.

Packing method



Reel label

Type No.
LOT No.
Quantity
Origin



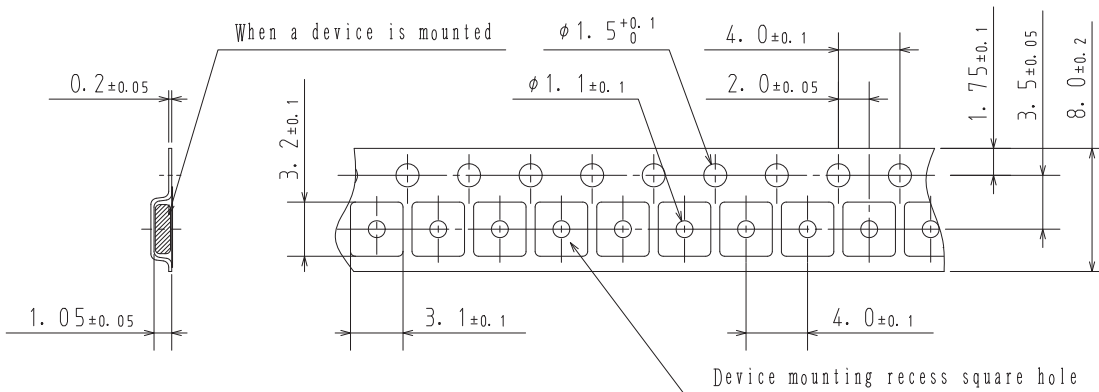
NOTE (1)

The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

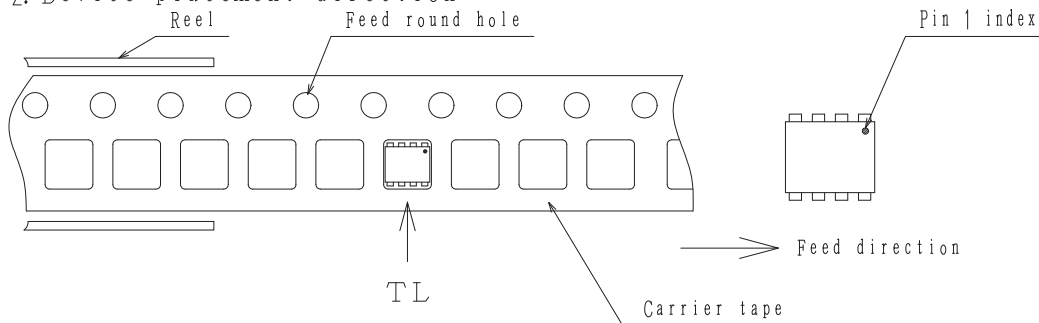
Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping configuration

2-1. Carrier tape size (unit:mm)



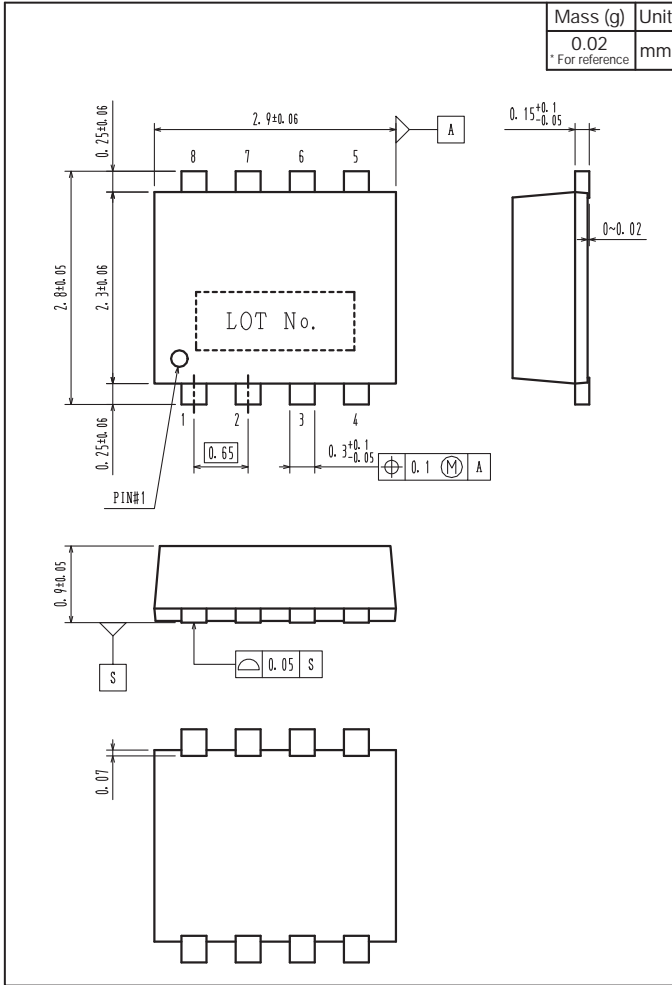
2-2. Device placement direction



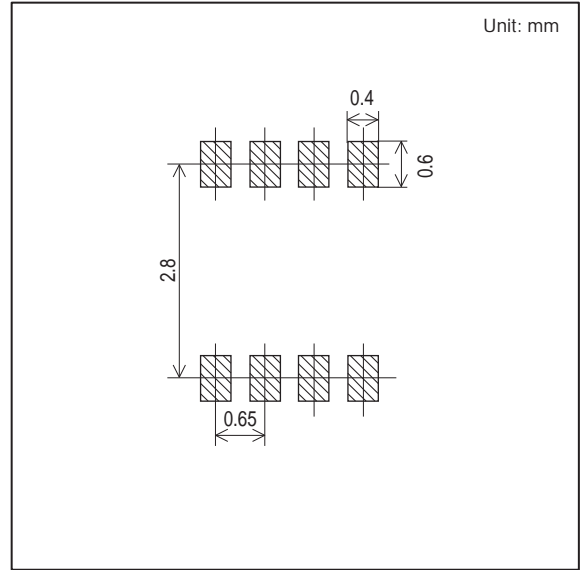
Those with pin 1 index on the feed hole side.....TL

ECH8308

Outline Drawing ECH8308-TL-H



Land Pattern Example



Note on usage : Since the ECH8308 is a MOSFET product, please avoid using this device in the vicinity of highly charged objects.

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