

# **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Forward Voltage @ I <sub>F</sub> = 10mA	$V_{F}$	0.9	V

### **Thermal Characteristics**

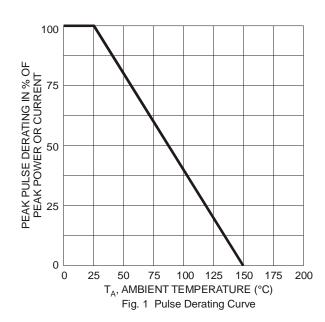
Characteristic	Symbol	Value	Unit
Power Dissipation (Notes 4 & 5)	P <sub>D</sub>	380	mW
Peak Power Dissipation, 8x20µS Waveform (Note 6)	P <sub>pk</sub>	20	W
Thermal Resistance, Junction-to-Ambient (Note 4)	$R_{ hetaJA}$	327	°C/W
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to +150	°C

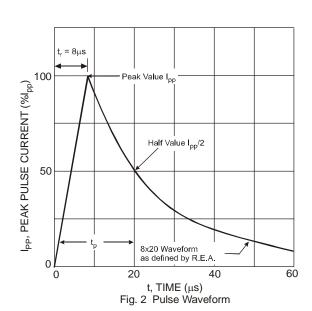
## Electrical Characteristics @TA = 25°C unless otherwise specified

Туре	Marking	Brea			Leakage Current (Note 7)		Volt	ping age te 6)	Capac @0V B (Not	ias(pF)	Capac @3V B (Not	,
Number	Code	V <sub>B</sub>	V <sub>BR</sub> @ I <sub>T</sub> = 1mA		I <sub>RM</sub> @	V <sub>RM</sub>	V <sub>C</sub> Max @ I <sub>PP</sub>		C <sub>T</sub>		C <sub>T</sub>	
		Min (V)	Nom (V)	Max (V)	Max(μA)	(V)	V <sub>C</sub> (V)	I <sub>PP</sub> (A)	Тур	Max	Тур	Max
DZQA6V8AXV5	T62	6.47	6.8	7.14	1	4.3	13	1.6	12.5	15	7.6	9.5

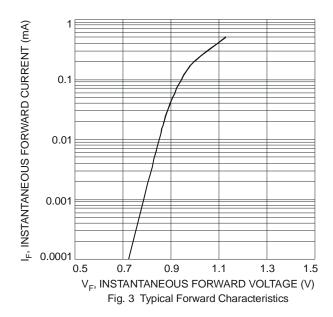
Notes:

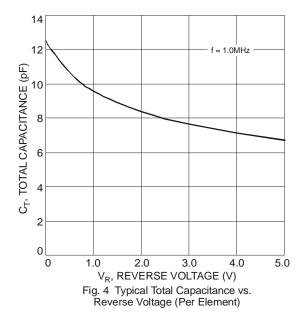
- 4. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. Suggested Pad Layout Document AP02001, which can be found on our website at http://www.diodes.com.
- 5. Only 1 diode under power. For all 4 diodes under power,  $P_D$  will be 25% of the listed value. 6. Non-repetitive current pulse per Figure 3 and derate above  $T_A = 25^{\circ}\text{C}$  per Figure 1.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Per element, f = 1MHz,  $T_A = 25$ °C



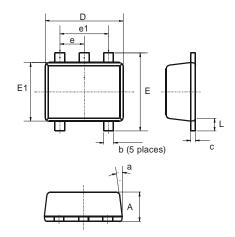






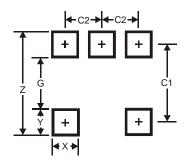


# Package Outline Dimensions



SOT553					
Dim	Min	Max Ty			
Α	0.55	0.60	0.60		
C	0.10	0.18	0.15		
D	1.50	1.70 1.6			
E	1.55	1.70	1.60		
E1	1.10	1.25	1.20		
L	0.10	0.30	0.20		
b	0.15	0.30	0.20		
е	0.50 Typ				
e1	1.00 Typ				
а	6°	8°	7°		
All Dimensions in mm					

# **Suggested Pad Layout**



Dimensions	Value (in mm)
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5



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