

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	80	V
Collector-Emitter Voltage	V _{CEO}	60	V
Emitter-Base Voltage	V _{EBO}	5	V
Collector Current - Continuous	I _C	1	А
Peak Pulse Collector Current	I _{CM}	2	A

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 4) @ $T_A = 25^{\circ}C$	PD	600	mW
Thermal Resistance, Junction to Ambient (Note 4) @ $T_A = 25^{\circ}C$	$R_{ ext{ heta}JA}$	209	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	O°

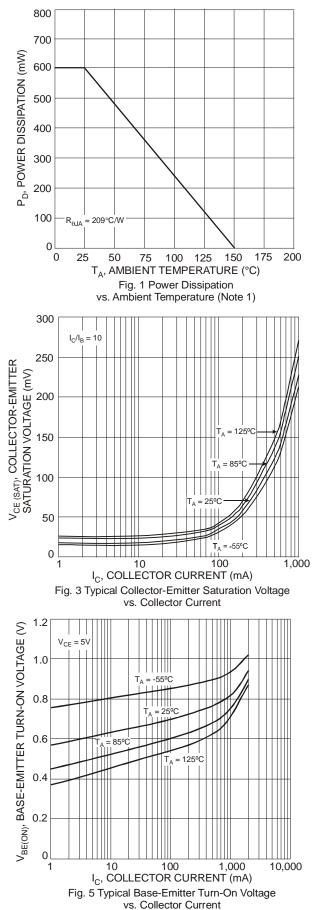
Electrical Characteristics @T_A = 25°C unless otherwise specified

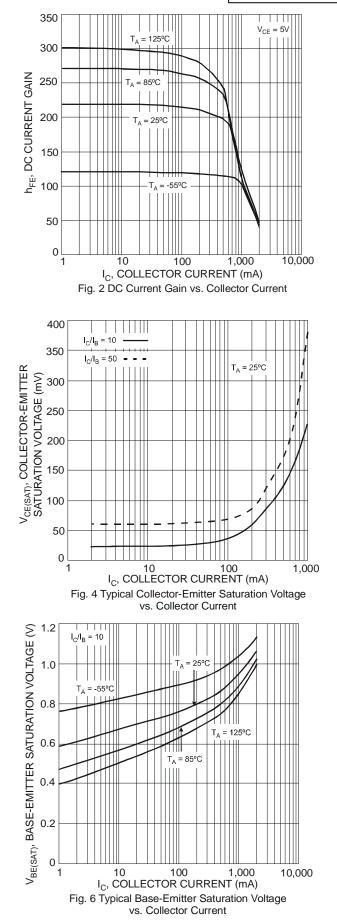
Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)					
Collector-Base Breakdown Voltage	V _{(BR)CBO}	80		V	$I_{C} = 100 \mu A, I_{E} = 0$
Collector-Emitter Breakdown Voltage	V _{(BR)CEO}	60		V	$I_{\rm C} = 10 {\rm mA}, \ I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	V _{(BR)EBO}	5	_	V	$I_E = 100 \mu A, I_C = 0$
Collector Cutoff Current	I _{CBO}	_	100	nA	$V_{CB} = 60V, I_E = 0$
Collector Cutoff Current	I _{CES}	_	100	nA	$V_{CE} = 60V$
Emitter Cutoff Current	I _{EBO}	_	100	nA	$V_{EB} = 4V, I_{C} = 0$
ON CHARACTERISTICS (Note 5)					
DC Current Gain	hFE	100 100 80 30	 300 	_	$\begin{split} I_{C} &= 1 \text{mA}, \ V_{CE} = 5 \text{V} \\ I_{C} &= 500 \text{mA}, \ V_{CE} = 5 \text{V} \\ I_{C} &= 1 \text{A}, \ V_{CE} = 5 \text{V} \\ I_{C} &= 2 \text{A}, \ V_{CE} = 5 \text{V} \end{split}$
Collector-Emitter Saturation Voltage	V _{CE(SAT)}	_	0.25 0.5	V	$I_{C} = 500$ mA, $I_{B} = 50$ mA $I_{C} = 1A$, $I_{B} = 100$ mA
Base-Emitter Saturation Voltage	V _{BE(SAT)}		1.1	V	$I_{\rm C} = 1$ A, $I_{\rm B} = 100$ mA
Base-Emitter Turn On Voltage	V _{BE(ON)}		1.0	V	$I_{C} = 1A, V_{CE} = 5V$
SMALL SIGNAL CHARACTERISTICS				•	
Output Capacitance	Cobo		10	pF	$V_{CB} = 10V, f = 1.0MHz$
Current Gain-Bandwidth Product	f _T	150	_	MHz	$V_{CE} = 10V, I_C = 50mA, f = 100MHz$

 Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch; pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.
Short duration pulse test used to minimize self-heating effect. Notes:



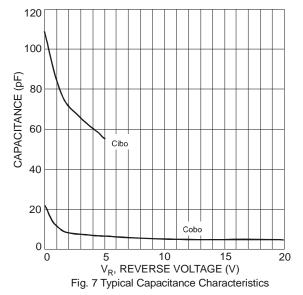




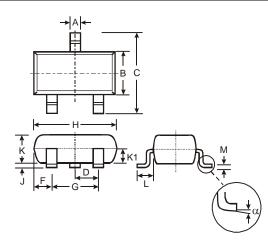


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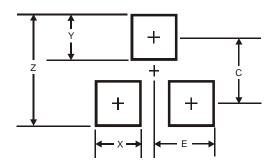


Package Outline Dimensions



SOT-23				
Dim	Min	Max	Тур	
Α	0.37	0.51	0.40	
в	1.20	1.40	1.30	
с	2.30	2.50	2.40	
D	0.89	1.03	0.915	
F	0.45	0.60	0.535	
G	1.78	2.05	1.83	
H	2.80	3.00	2.90	
J	0.013	0.10	0.05	
ĸ	0.903	1.10	1.00	
K1	-	-	0.400	
L	0.45	0.61	0.55	
М	0.085	0.18	0.11	
α	0°	8°	-	
All Dimensions in mm				

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.9
Х	0.8
Y	0.9
С	2.0
E	1.35



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