

Maximum Ratings $@T_A = 25^{\circ}C$ unless otherwise specified

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±12	V
Continuous Drain Current (Note 4) V _{GS} = -10V	Steady State	T _A = 25°C T _A = 70°C	ID	3.1 2.5	A
Continuous Drain Current (Note 4) V _{GS} = -4.5V	Steady State	T _A = 25°C T _A = 70°C	ID	2.7 2.2	A
Continuous Drain Current (Note 5) V _{GS} = -10V	Steady State	T _A = 25°C T _A = 70°C	ID	3.9 3.1	A
Continuous Drain Current (Note 5) V _{GS} = -4.5V	Steady State	T _A = 25°C T _A = 70°C	ID	3.3 2.7	A
Maximum Continuous Body Diode Forward Current			ls	2.2	A
Pulsed Drain Current (10us pulse, duty cycle=1%)			I _{DM}	20	А

Thermal Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 4)	PD	1.15	W
Thermal Resistance, Junction to Ambient (Note 4)	$R_{ extsf{ heta}JA}$	108	°C/W
Total Power Dissipation (Note 5)	PD	1.75	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{ extsf{ heta}JA}$	72	°C/W
Thermal Resistance, Junction to Case (Note 5)	R _θ Jc	23.4	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250 \mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-100	nA	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V _{GS(th)}	-0.5	-0.9	-1.5	V	$V_{DS} = V_{GS}, I_D = -250 \mu A$	
Static Drain-Source On-Resistance		_	65	75	mΩ	$V_{GS} = -10V, I_D = -4.2A$	
	R _{DS (ON)}	_	75	98		$V_{GS} = -4.5V, I_D = -4.0A$	
		_	98	150		$V_{GS} = -2.5V, I_D = -3.0A$	
Forward Transfer Admittance	Y _{fs}	_	5	_	S	$V_{DS} = -15V, I_D = -4.0A$	
Diode Forward Voltage	V _{SD}	_	-0.7	-1.0	V	$V_{GS} = 0V, I_{S} = -1A$	
DYNAMIC CHARACTERISTICS (Note 7)			÷				
Input Capacitance	C _{iss}	_	839	_		V _{DS} = -15V, V _{GS} = 0V f = 1.0MHz	
Output Capacitance	C _{oss}	_	47	_	pF		
Reverse Transfer Capacitance	C _{rss}	_	43	_			
Gate Resistance	R _G	_	12.3	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = -4.5V)	Qg	_	9.0	_			
Total Gate Charge (V _{GS} = -10.0V)	Qg	_	19.8	_	nC	V _{DS} = -15V, I _D = -4.0A	
Gate-Source Charge	Q _{gs}	_	1.6	_	nc		
Gate-Drain Charge	Q _{gd}		1.1	_			
Turn-On Delay Time	t _{D(on)}		9.7	_			
Turn-On Rise Time	tr		17.7	_	n 0	$V_{GS} = -10V, V_{DD} = -15V, R_G = 6\Omega,$	
Turn-Off Delay Time	t _{D(off)}	_	269	_	ns	I _D = -1A	
Turn-Off Fall Time	t _f	_	64	_			

Notes: 4. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.

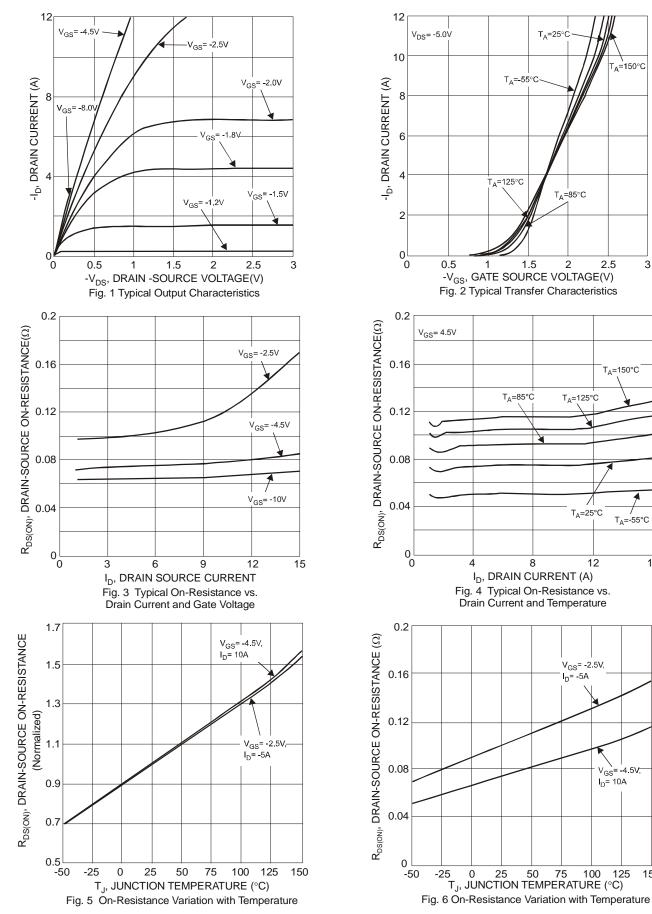
6. Short duration pulse test used to minimize self-heating effect.
7. Guaranteed by design. Not subject to production testing.



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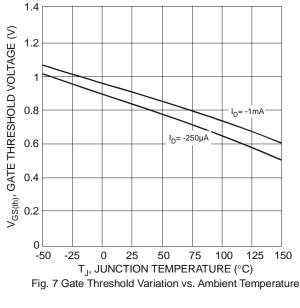


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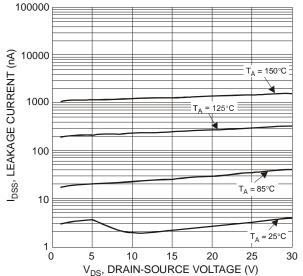
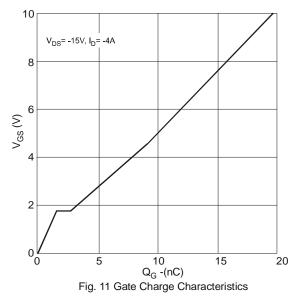


Fig. 9 Typical Drain-Source Leakage Current vs. Voltage



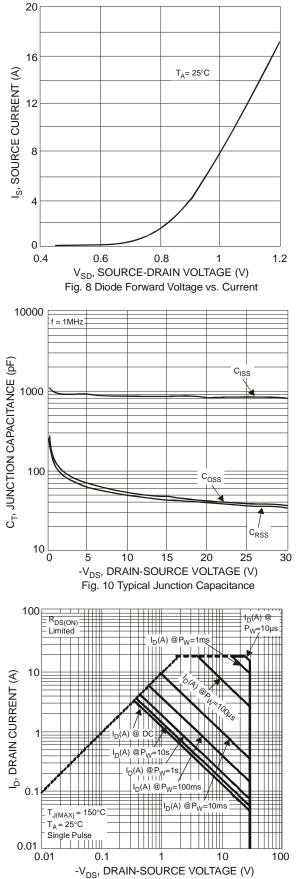
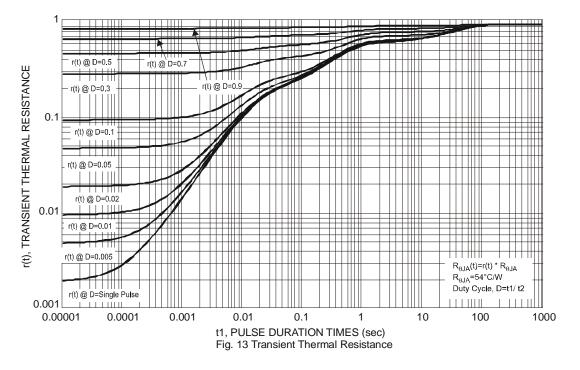


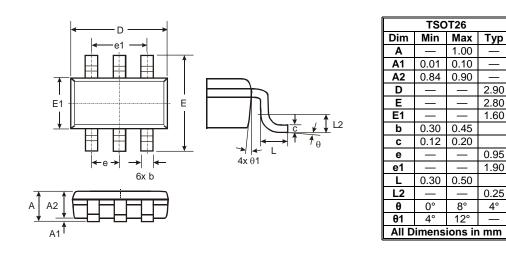
Fig. 12 SOA, Safe Operation Area

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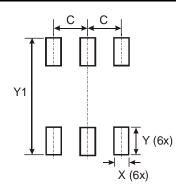




Package Outline Dimensions



Suggested Pad Layout



Dimensions	Value (in mm)
С	0.950
Х	0.700
Y	1.000
Y1	3.199



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