

# **Maximum Ratings** $(@T_A = +25^{\circ}C, \text{ unless otherwise specified.})$

Characteristic			Symbol	Value	Units
Drain-Source Voltage		$V_{DSS}$	-30	V	
Gate-Source Voltage (Note 4)		$V_{GSS}$	±25	V	
Continuous Drain Current (Note 5) V <sub>GS</sub> = -10V	Steady State	$T_A = 25$ °C $T_A = 70$ °C	I <sub>D</sub>	-4.8 -3.8	А
	t<10s	$T_A = 25$ °C $T_A = 70$ °C	I <sub>D</sub>	-6.3 -4.9	А
Maximum Continuous Body Diode Forward Current (Note 6)			I <sub>S</sub>	-3.0	Α
Pulsed Drain Current (10μs pulse, duty cycle = 1%)		I <sub>DM</sub>	-30	Α	

# Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units	
Total Power Dissipation (Note 5)	$T_A = 25$ °C	C	1.7	W	
Total Power Dissipation (Note 5)	T <sub>A</sub> = 70°C	$P_{D}$	1.1	] VV	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	6	73	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	$R_{\theta JA}$	37		
Operating and Storage Temperature Range		$T_J,T_STG$	-55 to 150	°C	

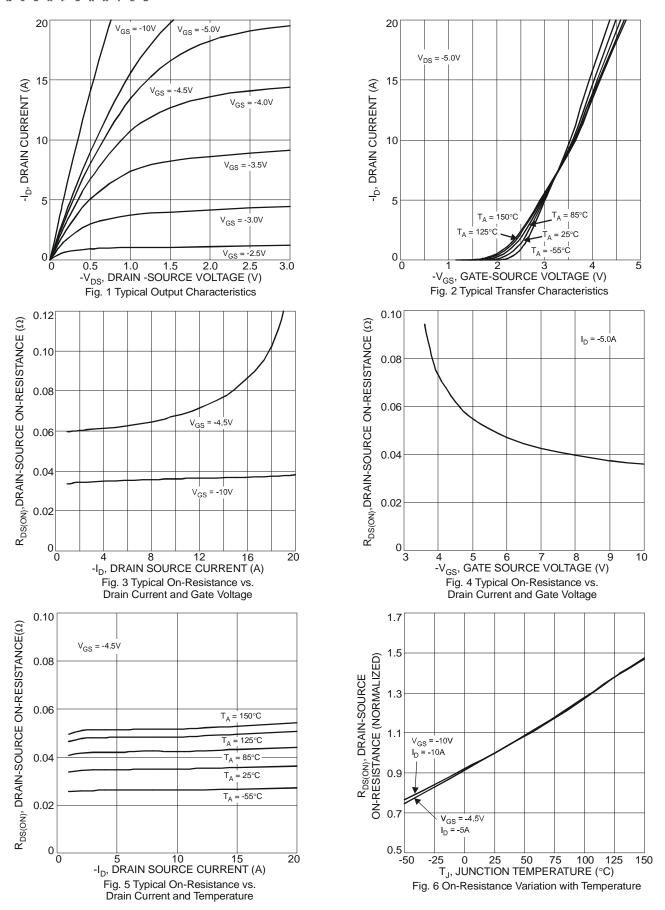
### **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 6)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	ī	-	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	-	-	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	Igss	-	-	±100	nA	$V_{GS} = \pm 25V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 6)							
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1.0	Ī	-2.0	V	$V_{DS} = V_{GS}$ , $I_D = -250\mu A$	
Static Drain-Source On-Resistance		-	36	45	$\mathbf{m}\Omega$	$V_{GS} = -10V, I_D = -6A$	
Static Dialit-Source Off-Resistance	R <sub>DS</sub> (ON)		61	80		$V_{GS} = -4.5V, I_{D} = -5A$	
Forward Transfer Admittance	Y <sub>fs</sub>		4.8	-	S	$V_{DS} = -10V, I_{D} = -5.3A$	
Diode Forward Voltage	V <sub>SD</sub>	-	-0.7	-1.0	V	$V_{GS} = 0V, I_{S} = -1.7A$	
DYNAMIC CHARACTERISTICS (Note 7)							
Input Capacitance	C <sub>iss</sub>		620	-	рF	151/1/ 01/	
Output Capacitance	Coss	-	83	-	pF	$V_{DS} = -15V, V_{GS} = 0V,$ -f = 1.0MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>		62	-	рF	T = 1.0WHZ	
Gate resistance	Rg	-	10.8	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	-	5.1	-	nC	V <sub>DS</sub> = -15V, I <sub>D</sub> = -6A	
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg	-	10.5	-	nC		
Gate-Source Charge	Q <sub>gs</sub>	-	1.8	-	nC		
Gate-Drain Charge	$Q_{gd}$	-	1.9	-	nC		
Turn-On Delay Time	t <sub>D(on)</sub>	-	6.8	-	ns		
Turn-On Rise Time	t <sub>r</sub>	-	4.9	-	ns	$V_{DD} = -15V, V_{GS} = -10V,$	
Turn-Off Delay Time	t <sub>D(off)</sub>	-	28.4	-	ns	$R_G = 6\Omega$ , $I_D = -1A$	
Turn-Off Fall Time	t <sub>f</sub>	-	12.4	-	ns	1	
Reverse Recovery Time	t <sub>rr</sub>	-	14	-	ns	I <sub>F</sub> = 12A, di/dt = 500A/μs	
Reverse Recovery Charge	Q <sub>rr</sub>	-	11	-	nC		

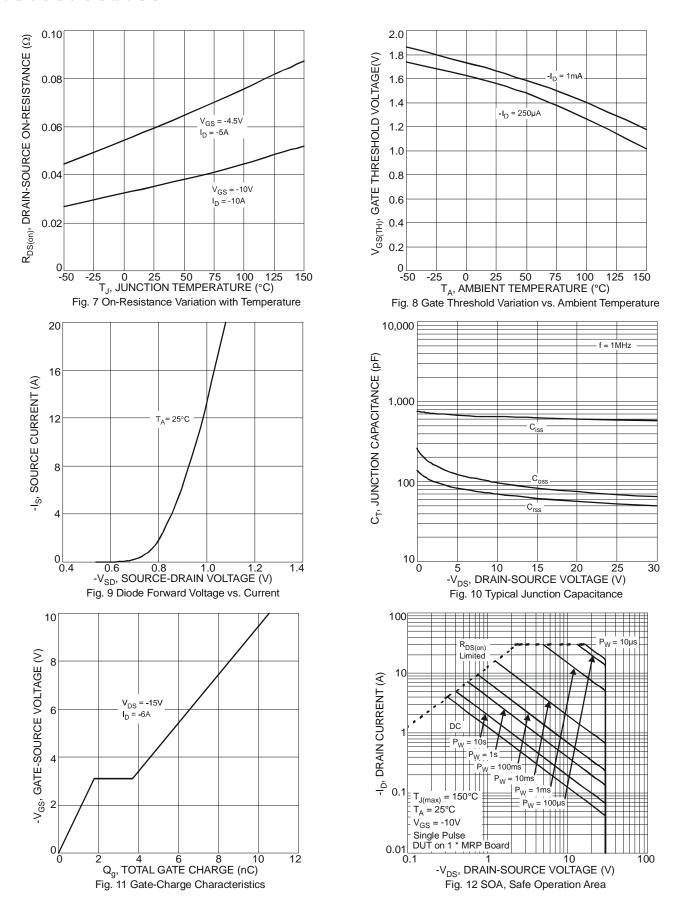
Notes: 4. AEC-Q101 V<sub>GS</sub> maximum is ±20V

- 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
- 6. Short duration pulse test used to minimize self-heating effect.
- 7. Guaranteed by design. Not subject to product testing.

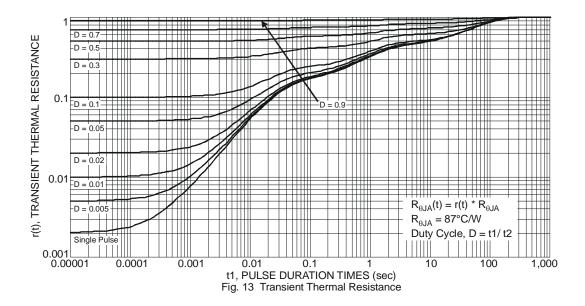






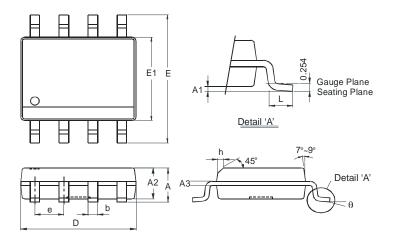






### **Package Outline Dimensions**

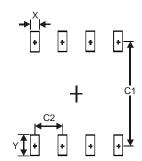
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



SO-8					
Dim	Min	Max			
Α	-	1.75			
A1	0.10	0.20			
A2	1.30	1.50			
A3	0.15	0.25			
b	0.3	0.5			
D	4.85	4.95			
Е	5.90	6.10			
E1	3.85	3.95			
е	1.27 Typ				
h	ı	0.35			
L	0.62	0.82			
θ	0°	8°			
All Dimensions in mm					

## Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Х	0.60
Y	1.55
C1	5.4
C2	1.27



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