

Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	60	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	350 290	mA
Maximum Continuous Body Diode Forward Current (Note 6)			Is	0.4	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) (Note 6)			I _{DM}	1.2	Α

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_{D}	320	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	397	°C/W
Total Power Dissipation (Note 6)		P_{D}	410	mW
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	306	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +150	°C

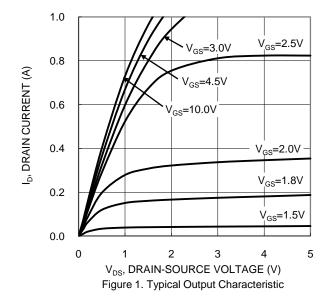
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

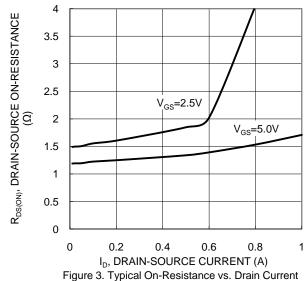
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	60	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current	I _{DSS}	l		1.0	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	1.0	V	$V_{DS} = 10V, I_D = 250\mu A$
			1.2	2.0	Ω	$V_{GS} = 5.0V, I_D = 0.05A$
Static Drain-Source On-Resistance	R _{DS(ON)}	_	1.6 2.5	2.5 3.5		$V_{GS} = 2.5V, I_D = 0.05A$
						$V_{GS} = 1.8V, I_D = 0.05A$
Forward Transconductance	Y _{fs}	200	_	_	mS	$V_{DS} = 10V, I_D = 0.2A$
Diode Forward Voltage	V _{SD}	_	0.75	1.4	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	l	28.5		pF	.,
Output Capacitance	Coss		3.9	_	pF	V _{DS} = 30V, V _{GS} = 0V f = 1.0MHz
Reverse Transfer Capacitance	C _{rss}		2.5	_	pF	1 - 1.0001112
Gate Resistance	R_g	_	65	_	Ω	$f = 1MHz$, $V_{GS} = 0V$, $V_{DS} = 0V$
Total Gate Charge	Qg	_	0.4	_	nC	451/1/ 401/
Gate-Source Charge	Q _{qs}	_	0.1	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V,$
Gate-Drain Charge	Q _{gd}	_	0.1	_	nC	I _D = 250mA
Turn-On Delay Time	t _{D(ON)}	_	2.1	-	ns	
Turn-On Rise Time	t _R	_	1.8	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$
Turn-Off Delay Time	t _{D(OFF)}	_	14.4	-	ns	$R_G = 25\Omega$, $I_D = 200 \text{mA}$
Turn-Off Fall Time	t _F	_	8.4	_	ns]

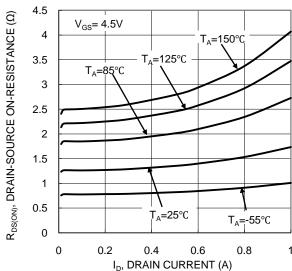
Notes:

- 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.6. Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
- 7. Short duration pulse test used to minimize self-heating effect.
- 8. Guaranteed by design. Not subject to product testing.



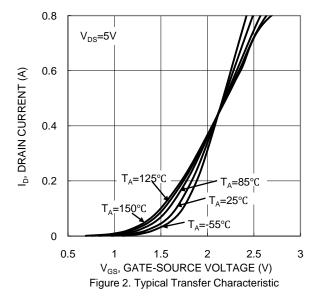


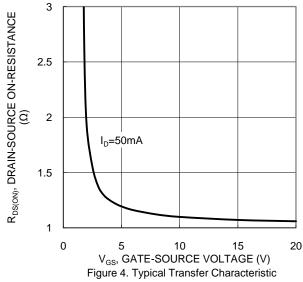


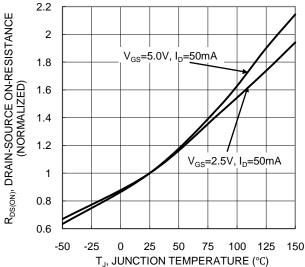


and Gate Voltage

Figure 5. Typical On-Resistance vs. Drain Current and Temperature







T_J, JUNCTION TEMPERATURE (°C)
Figure 6. On-Resistance Variation with Junction
Temperature



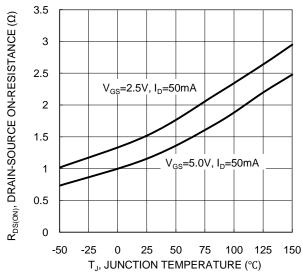
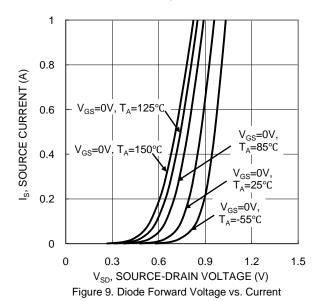
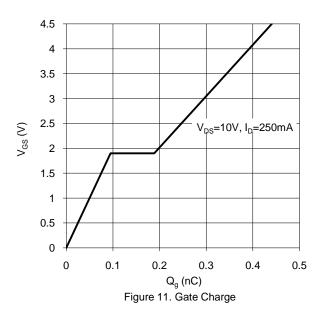


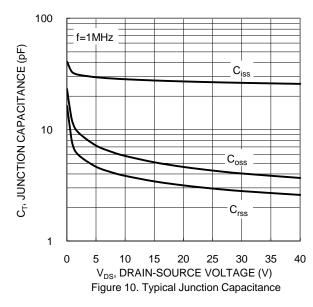
Figure 7. On-Resistance Variation with Junction Temperature

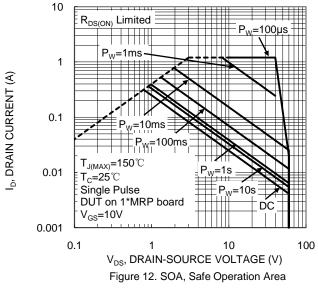




1.2 $V_{\text{GS(TH)}},$ GATE THRESHOLD VOLTAGE (V) 1 $I_D=1mA$ 0.8 $I_{D} = 250 \mu A$ 0.6 0.4 -50 -25 0 25 50 75 100 125 150 T_{.i}, JUNCTION TEMPERATURE (°C)

Figure 8. Gate Threshold Variation vs. Junction Temperature







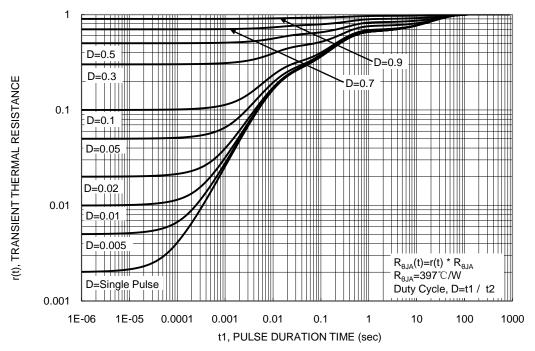
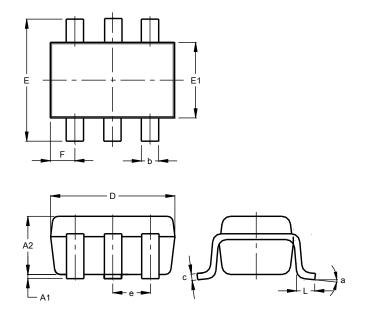


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

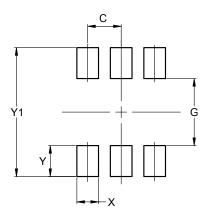


SOT363						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	1.00			
b	0.10	0.30	0.25			
С	0.10	0.22	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	0.650 BSC					
F	0.40	0.45	0.425			
L	0.25	0.40	0.30			
а	8°					
All Dimensions in mm						



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)			
С	0.650			
G	1.300			
Х	0.420			
Υ	0.600			
Y1	2.500			

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