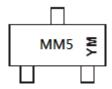


Marking Information



MM5 = Product Type Marking Code YM = Date Code Marking Y or \overline{Y} = Year (ex: I = 2021) M or \overline{M} = Month (ex: 9 = September)

Date Code Key

Year	2013		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	Α			J	K	L	М	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	V_{DSS}	50	V		
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	ΙD	360 250	mA		
Continuous Drain Current (Note 6) V _{GS} = 5V	ΙD	250 200	mA		
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	700	mA		

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Power Dissipation	(Note 5)	D-	320	- mW	
Total Fower Dissipation	(Note 6)	P _D	420		
Thermal Desistance, lunction to Ambient	(Note 5)		395	°C/W	
Thermal Resistance, Junction to Ambient	(Note 6)	$R_{\theta JA}$	301	*C/VV	
Operating and Storage Temperature Range	_	TJ, TSTG	-55 to +150	°C	

Notes:

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.



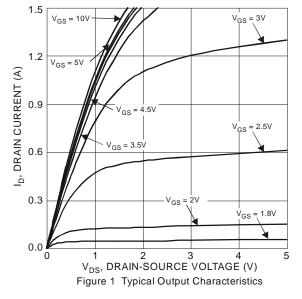
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

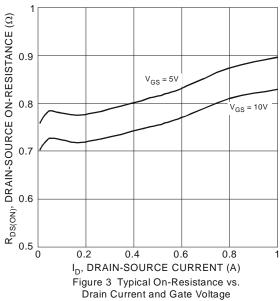
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	50	_		V	V _G S = 0V, I _D = 250µA	
Zero Gate Voltage Drain Current	IDSS		_	1.0	μA	V _{DS} = 50V, V _{GS} = 0V	
Gate-Body Leakage	Igss	_	_	±10	μΑ	$V_{GS} = \pm 12V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	0.8	_	1.5	V	$V_{DS} = V_{GS}$, $I_D = 100\mu A$	
Gate Threshold Voltage Temperature Coefficient (Note 8)	$\frac{\Delta V_{\text{GS(TH)}}}{\Delta T_{\text{J}}}$	_	-3.4		mV/°C	_	
Static Drain-Source On-Resistance	6	_	0.73	2.0	Ω	V _{GS} = 10V, I _D = 270mA	
Static Drain-Source On-Resistance	R _{DS(ON)}	_	0.77	3.0	Ω	V _G S = 5V, I _D = 200mA	
Forward Transconductance	grs	80	_	_	mS	V _{DS} = 10V, I _D = 200mA	
Diode Forward Voltage	VsD	_	0.75	1.2	V	V _G S = 0V, I _S = 115mA	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	C _{iss}		45.8	_			
Output Capacitance	Coss		5.3	_	pF	$V_{DS} = 25V, V_{GS} = 0V$ f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	3.9	_		1.500112	
Total Gate Charge V _{GS} = 10V	Qg	_	1.2	_			
Total Gate Charge V _{GS} = 4.5V	Qg	_	0.6	_	nC	$V_{GS} = 10V, V_{DS} = 10V,$ $I_{D} = 250mA$	
Gate-Source Charge	Qgs	_	0.2	_	nc nc		
Gate-Drain Charge	Qgd	_	0.1				
Turn-On Delay Time	td(on)	_	2.7	_			
Turn-On Rise Time	t _R	_	2.5	_]	V _{DD} = 30V, V _{GS} = 10V,	
Turn-Off Delay Time	tD(OFF)	_	18.9	_	ns	$R_G = 25\Omega$, $I_D = 200mA$	
Turn-Off Fall Time	tF	_	11.0				

Notes:

^{7.} Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to production testing.







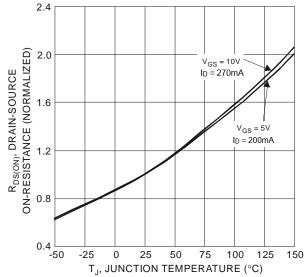
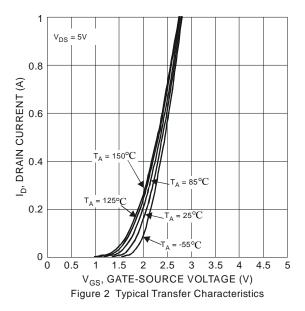


Figure 5 On-Resistance Variation with Temperature



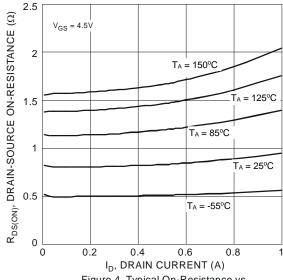


Figure 4 Typical On-Resistance vs. Drain Current and Temperature

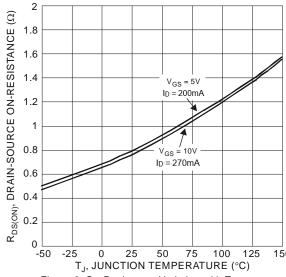


Figure 6 On-Resistance Variation with Temperature



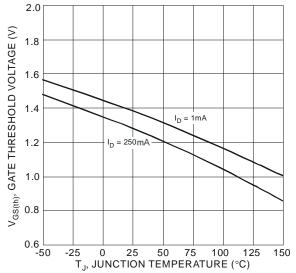
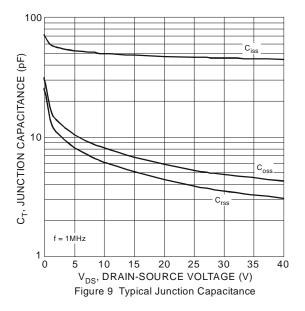
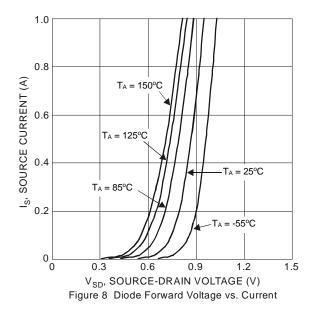
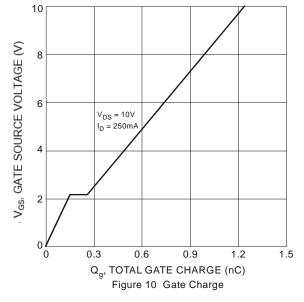


Figure 7 Gate Threshold Variation vs. Junction Temperature



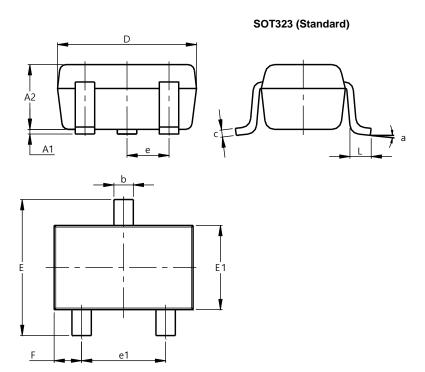






Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

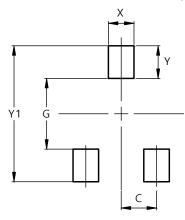


SOT323 (Standard)							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.80	1.00	0.90				
b	0.20	0.40	0.30				
С	0.08	0.18	0.13				
D	1.80	2.20	2.00				
Е	2.00	2.45	2.225				
E1	1.15	1.35	1.25				
е			0.65				
e1	1.20	1.40	1.30				
F	0.25	0.475	0.3625				
L	0.25	0.46	0.355				
а	0°	8°					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT323 (Standard)



Dimensions	Value (in mm)			
Dilliensions				
С	0.650			
G	1.300			
Х	0.470			
Υ	0.600			
V1	2 500			



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