

Maximum Ratings

Characteristic			Symbol	Value	Unit
Source-Source Voltage			V _{SSS}	24	V
Gate-Source Voltage		V _{GSS}	±12	V	
Continuous Source Current @ T _A = +25°C (Note 5)	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	Is	1.6 1.3	А
Pulsed Source Current @ T _A = +25°C (Notes 5 & 6)		I _{SM}	30	Α	

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation, @T _A = +25°C (Note 5)	P_{D}	1.45	W
Thermal Resistance, Junction to Ambient @T _A = +25°C (Note 5)	$R_{\theta JA}$	86.68	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

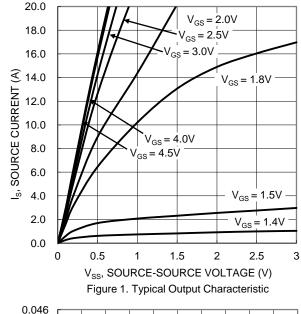
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

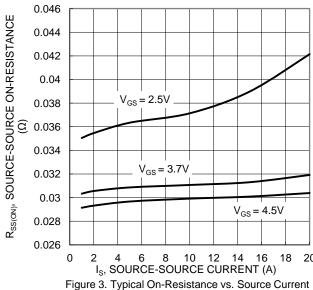
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Source to Source Breakdown Voltage T _J = +25°C	V _{(BR)SS}	24	_	_	V	$I_S = 1mA$, $V_{GS} = 0V$
Zero Gate Voltage Source Current T _J = +25°C	I _{SSS}	_	_	1.0	μΑ	$V_{SS} = 20V, V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 8V$, $V_{SS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.5	_	1.3	V	$V_{SS} = 10V, I_S = 1.0mA$
Static Source-Source On-Resistance	R _{SS(ON)}	20 20.5 21 22 23	29 30 31 33 36	45 48 50 57 72	mΩ	$V_{GS} = 4.5V$, $I_S = 3.0A$ $V_{GS} = 4.0V$, $I_S = 3.0A$ $V_{GS} = 3.7V$, $I_S = 3.0A$ $V_{GS} = 3.1V$, $I_S = 3.0A$ $V_{GS} = 2.5V$, $I_S = 3.0A$
Forward Transfer Admittance	Y _{fs}	_	9.4	_	S	$V_{SS} = 10V, I_S = 3.0A$
Body Diode Forward Voltage	V _{F(S-S)}	_	0.8	1.2	V	$I_F = 3.0A, V_{GS} = 0V$
DYNAMIC CHARACTERISTICS (Note 8)						
Total Gate Charge	Q_g	1	12.6	_	nC	$V_{GS} = 4.5V$, $V_{SS} = 10V$, $I_{S} = 6A$
Turn-On Delay Time	t _{D(ON)}	_	183	_	ns	
Turn-On Rise Time	t _R	_	278	_	ns	$V_{DD} = 10V$,
Turn-Off Delay Time	t _{D(OFF)}	_	738	_	ns	$R_L = 3.33\Omega$, $I_S = 3.0A$
Turn-Off Fall Time	t _F	_	572	_	ns	

Notes:

- Device mounted on FR-4 material with 1-inch² (6.45-cm²), 2-oz. (0.071-mm thick) Cu.
 Repetitive rating, pulse width limited by junction temperature.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to production testing.







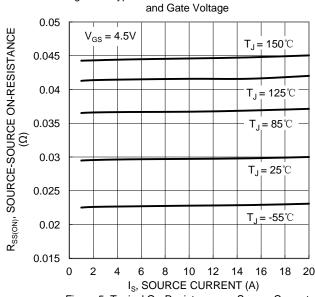


Figure 5. Typical On-Resistance vs. Source Current and Junction Temperature

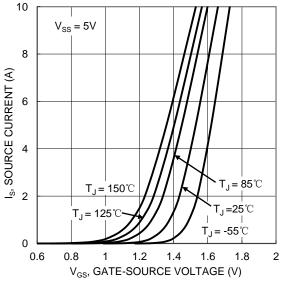
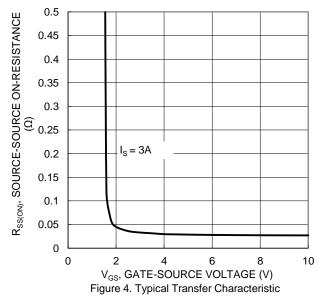


Figure 2. Typical Transfer Characteristic



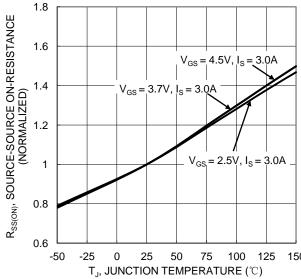


Figure 6. On-Resistance Variation with Junction Temperature

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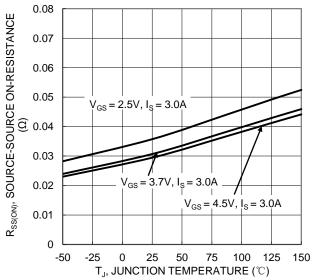


Figure 7. On-Resistance Variation with Junction Temperature

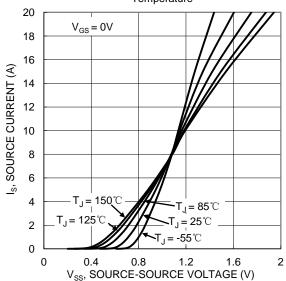


Figure 9. Diode Forward Voltage vs. Current

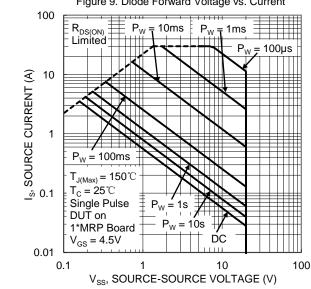


Figure 11. SOA, Safe Operation Area

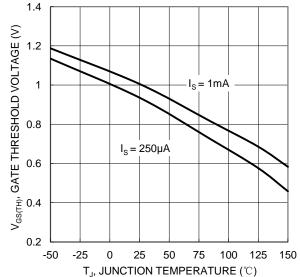
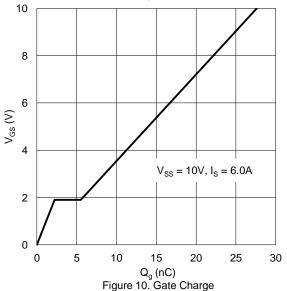


Figure 8. Gate Threshold Variation vs. Junction Temperature





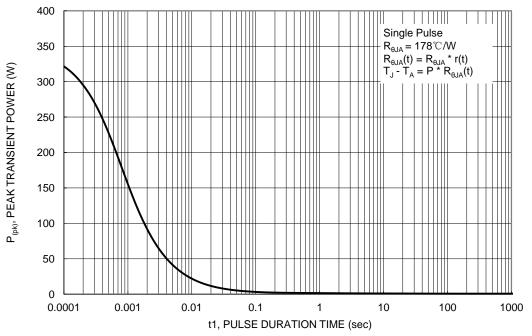
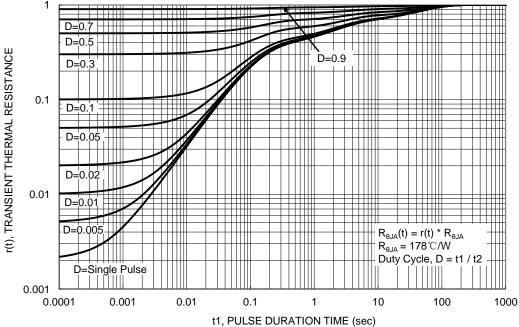


Figure 12. Single Pulse Maximum Power Dissipation



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Figure 13. Transient Thermal Resistance

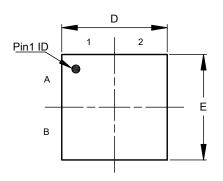
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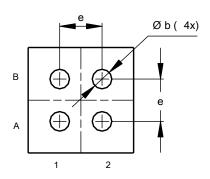


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-WLB1616-4





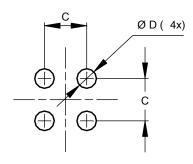
		A2
A1		
		Y Y
Y		Δ
		<u>T ::</u>
A		1
	Seating Plane	'

X2-WLB1616-4					
Dim	Min	Max	Тур		
Α		0.40	0.37		
A 1			0.15		
A2			0.22		
b	0.25	0.35	0.30		
D	1.58	1.66	1.62		
Е	1.58	1.66	1.62		
е	-	-	0.65		
All Dimensions in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

X2-WLB1616-4



Dimensions	Value (in mm)	
С	0.65	
D	0.30	



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