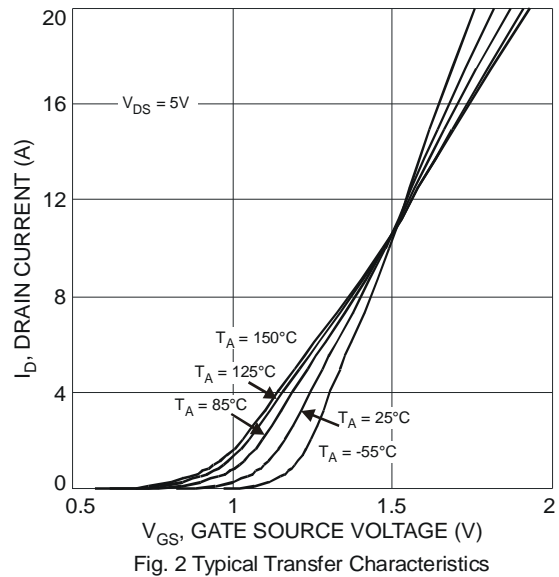
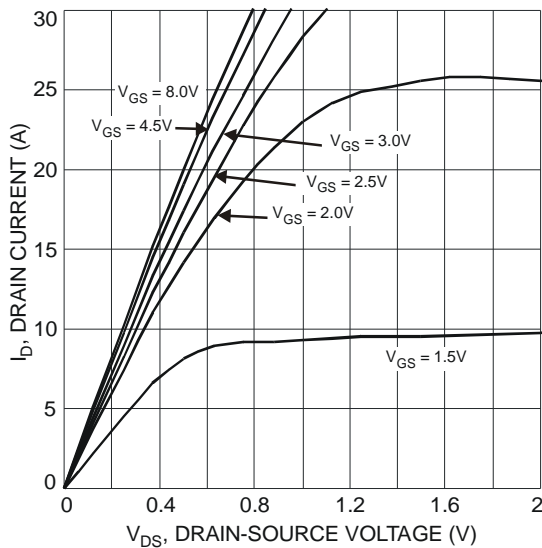


**Electrical Characteristics** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
STATIC CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	20	—	—	V	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V
Gate-Body Leakage Current	I <sub>GSS</sub>	—	—	±100	nA	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±8V
Gate Threshold Voltage	V <sub>GS(th)</sub>	0.5	—	0.9	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance (Note 5)	R <sub>DS (ON)</sub>	—	22	28	mΩ	V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 8.2A
			25	32		V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 3.3A
			31	40		V <sub>GS</sub> = 1.8V, I <sub>D</sub> = 2.0A
Forward Transfer Admittance	Y <sub>FS</sub>	—	7	—	S	V <sub>DS</sub> = 10V, I <sub>D</sub> = 4A
Diode Forward Voltage (Note 5)	V <sub>SD</sub>	—	0.7	0.9	V	I <sub>S</sub> = 2.25A, V <sub>GS</sub> = 0V
DYNAMIC CHARACTERISTICS (Note 6)						
Input Capacitance	C <sub>iss</sub>	—	856	—	pF	V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V f = 1.0MHz
Output Capacitance	C <sub>oss</sub>	—	83	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	78	—	pF	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz
Gate Resisitance	R <sub>G</sub>	—	1.32	—	Ω	
SWITCHING CHARACTERISTICS						
Total Gate Charge	Q <sub>g</sub>	—	8.3	—	nC	V <sub>GS</sub> = 4.5V, V <sub>DS</sub> = 10V, I <sub>D</sub> = 8.2A
Gate-Source Charge	Q <sub>gs</sub>	—	1.3	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	3.1	—	nC	
Turn-On Delay Time	t <sub>D(on)</sub>	—	8.4	—	ns	V <sub>DD</sub> = 10V, V <sub>GS</sub> = 4.5V, R <sub>L</sub> = 10Ω, R <sub>G</sub> = 6Ω
Turn-On Rise Time	t <sub>r</sub>	—	8.2	—	ns	
Turn-Off Delay Time	t <sub>D(off)</sub>	—	40.4	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	8.9	—	ns	

Notes: 5. Test pulse width t = 300ms.  
6. Guaranteed by design. Not subject to production testing.



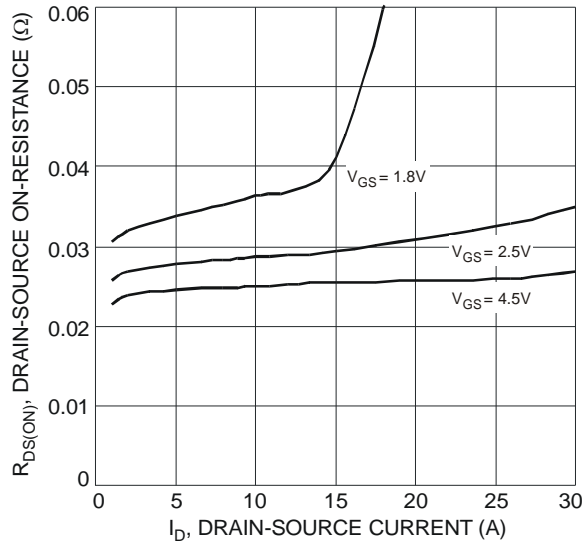


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

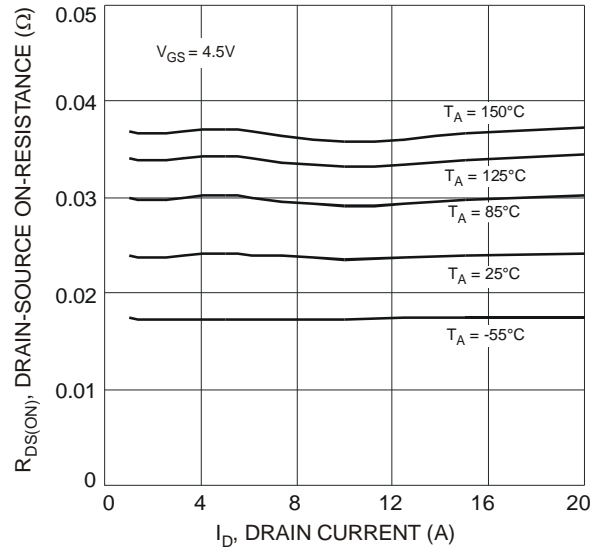


Fig. 4 Typical Drain-Source On-Resistance vs. Drain Current and Temperature

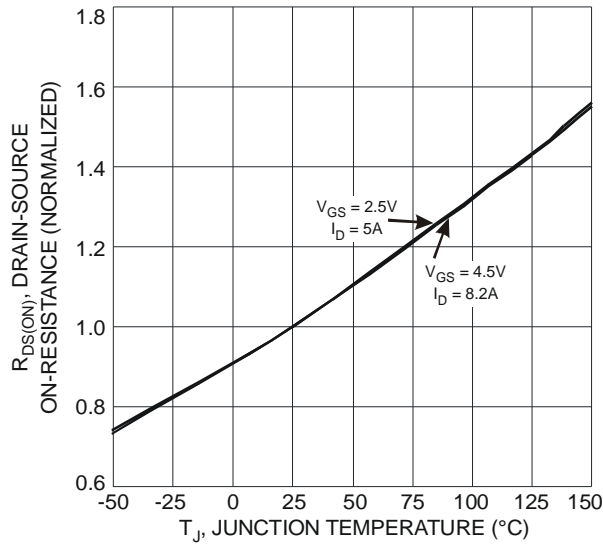


Fig. 5 On-Resistance Variation with Temperature

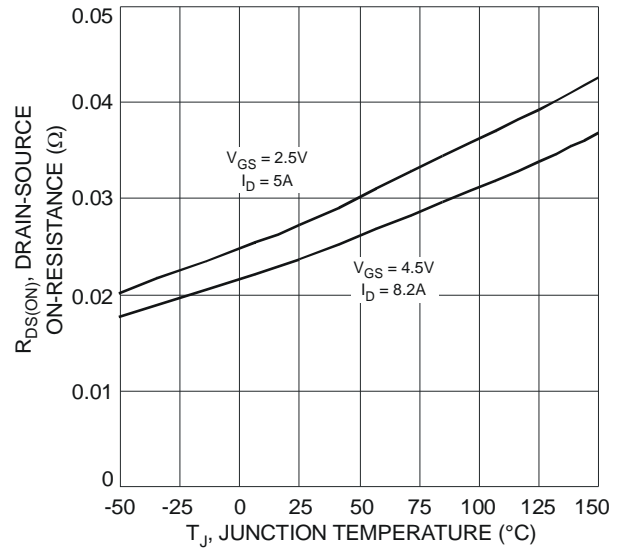


Fig. 6 On-Resistance Variation with Temperature

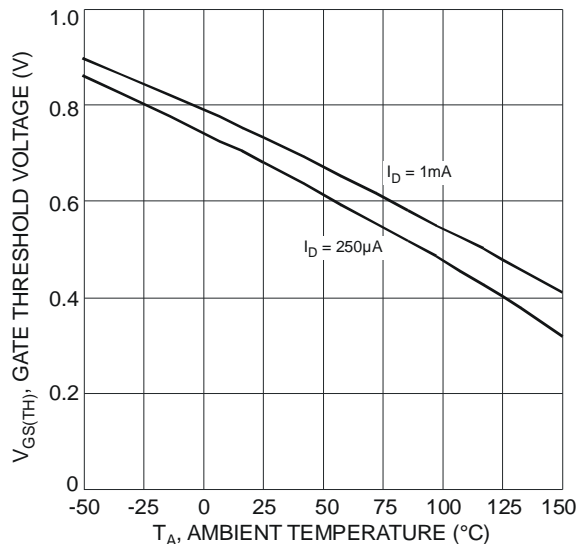


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

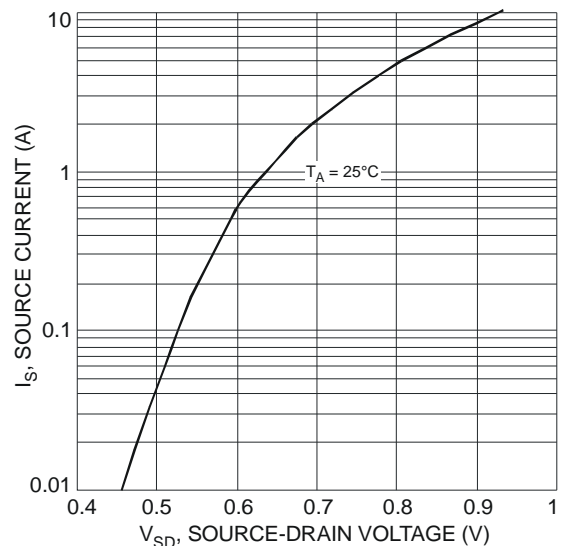
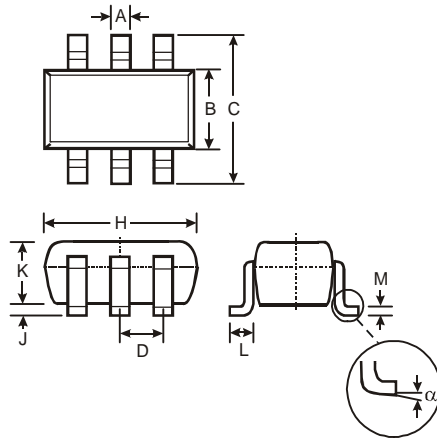


Fig. 8 Diode Forward Voltage vs. Current

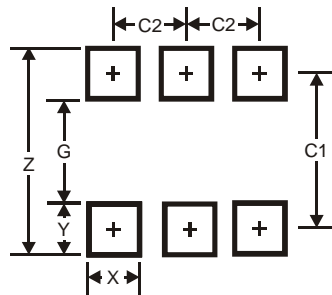


**Package Outline Dimensions**



SOT-26			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	—	—	0.95
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
α	0°	8°	—
All Dimensions in mm			

**Suggested Pad Layout**



Dimensions	Value (in mm)
Z	3.20
G	1.60
X	0.55
Y	0.80
C1	2.40
C2	0.95

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