September 1986

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# DM74ALS05A Hex Inverter with Open Collector Outputs

## **General Description**

FAIRCHILD

SEMICONDUCTOR

This device contains six independent gates, each of which performs the logic INVERT function. The open-collector outputs require external pull-up resistors for proper logical operation.

Pull-Up Resistor Equations

$$\mathsf{R}_{MAX} = \frac{\mathsf{V}_{CC}\left(\mathsf{Min}\right) - \mathsf{V}_{OH}}{\mathsf{N}_{1}\left(\mathsf{I}_{OH}\right) + \mathsf{N}_{2}\left(\mathsf{I}_{IH}\right)}$$

$$\mathsf{R}_{\mathsf{MIN}} = \frac{\mathsf{V}_{\mathsf{CC}}\left(\mathsf{Max}\right) - \mathsf{V}_{\mathsf{OL}}}{\mathsf{I}_{\mathsf{OL}} - \mathsf{N}_{\mathsf{3}}\left(\mathsf{I}_{\mathsf{IL}}\right)}$$

Where:

re:  $N_1 (I_{OH}) = total maximum output HIGH current$ for all outputs tied to pull-up resistor $<math>N_2 (I_{|H}) = total maximum input HIGH current$ for all inputs tied to pull-up resistor $<math>N_3 (I_{|L}) = total maximum input LOW current for$ all inputs tied to pull-up resistor

# **Ordering Code:**

Order Number	Package Number	Package Description
DM74ALS05AM	M14A	14-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74ALS05AN	N14A	14-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

### **Connection Diagram**





**Features** 

process

Switching specifications at 50 pF

ture and  $V_{\mbox{CC}}$  range

Schottky counterparts

Switching specifications guaranteed over full tempera-

Advanced oxide-isolated, ion-implanted Schottky TTL

■ Functionally and pin for pin compatible with Schottky

■ Improved AC performance over Schottky and low power

and low power Schottky TTL counterpart

$\mathbf{Y} = \overline{\mathbf{A}}$					
Input	Output				
Α	Y				
L	Н				
Н	L				

H = HIGH Logic Level L = LOW Logic Level

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## Absolute Maximum Ratings(Note 1)

Supply Voltage	7V
Input Voltage	7V
HIGH Level Output Voltage	7V
Operating Free Air Temperature Range	$0^{\circ}C$ to $+70^{\circ}C$
Storage Temperature Range	$-65^\circ C$ to $+150^\circ C$
Typical θ <sub>JA</sub>	
N Package	88.0°C/W
M Package	118.5°C/W

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

### **Recommended Operating Conditions**

Symbol	Parameter	Min	Nom	Max	Units
V <sub>CC</sub>	Supply Voltage	4.5	5	5.5	V
V <sub>IH</sub>	HIGH Level Input Voltage	2			V
VIL	LOW Level Input Voltage			0.8	V
V <sub>OH</sub>	HIGH Level Output Voltage			5.5	V
I <sub>OL</sub>	LOW Level Output Current			8	mA
T <sub>A</sub>	Free Air Operating Temperature	0		70	°C

#### **Electrical Characteristics**

over recommended operating free air temperature range. All typical values are measured at V<sub>CC</sub> = 5V, T<sub>A</sub> = 25°C.

Symbol	Parameter	Conditions		Min	Тур	Max	Units
V <sub>IK</sub>	Input Clamp Voltage	$V_{CC} = 4.5V, I_I = -18 \text{ mA}$				-1.5	V
I <sub>OH</sub>	HIGH Level Output Current	$V_{CC} = 4.5V, V_{OH} = 5.5V$				100	μA
V <sub>OL</sub>	LOW Level	$V_{CC} = 4.5V$	I <sub>OL</sub> =4 mA		0.25	0.4	V
	Output Voltage		I <sub>OL</sub> = 8 mA		0.35	0.5	V
l <sub>l</sub>	Input Current @ Max	Vec = 5 5V V = 7V				0.1	mΔ
	Input Voltage	v <sub>CC</sub> = 5.5v, v <sub>IH</sub> = 7v				0.1	ШA
I <sub>IH</sub>	HIGH Level Input Current	$V_{CC} = 5.5V, V_{IH} = 2.7V$				20	μA
IIL	LOW Level Input Current	$V_{CC} = 5.5V, \ V_{IL} = 0.4V$				-0.1	mA
I <sub>CC</sub>	Supply Current	$V_{CC} = 5.5V$	Outputs HIGH		0.65	1.1	mA
			Outputs LOW		2.4	4.2	mA

#### **Switching Characteristics**

over recommended operating free air temperature range

Symbol	Parameter	Conditions	Min	Max	Units
t <sub>PLH</sub>	Propagation Delay Time	V <sub>CC</sub> = 4.5V to 5.5V	23	54	ns
	LOW-to-HIGH Level Output	$R_L = 2 k\Omega, C_L = 50 pF$			
t <sub>PHL</sub>	Propagation Delay Time		4	14	
	HIGH-to-LOW Level Output		4	14	115

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