

Electrical Characteristics, Q1, MMBT4401 NPN Transistor Element @ $T_A = 25^\circ\text{C}$ unless otherwise specified

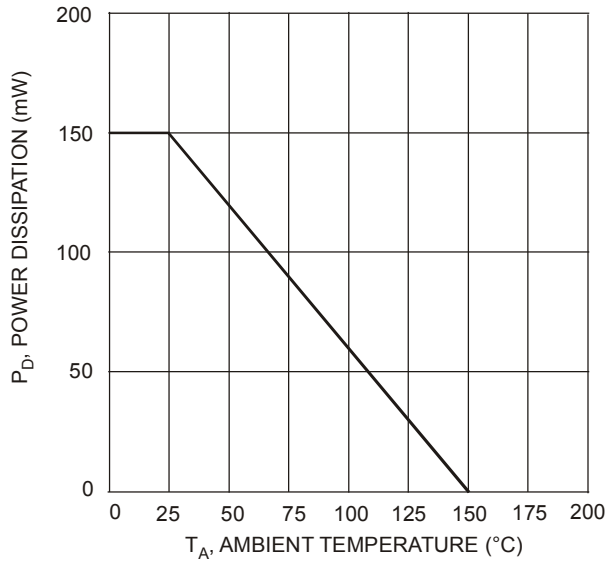
Characteristic	Symbol	Min	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 5)						
Collector-Base Breakdown Voltage	$V_{(BR)CBO}$	60	—	V	$I_C = 100\mu\text{A}, I_E = 0$	
Collector-Emitter Breakdown Voltage	$V_{(BR)CEO}$	40	—	V	$I_C = 1.0\text{mA}, I_B = 0$	
Emitter-Base Breakdown Voltage	$V_{(BR)EBO}$	6.0	—	V	$I_E = 100\mu\text{A}, I_C = 0$	
Collector Cutoff Current	I_{CEX}	—	100	nA	$V_{CE} = 35\text{V}, V_{EB(OFF)} = 0.4\text{V}$	
Base Cutoff Current	I_{BL}	—	100	nA	$V_{CE} = 35\text{V}, V_{EB(OFF)} = 0.4\text{V}$	
ON CHARACTERISTICS (Note 5)						
DC Current Gain	h_{FE}	20	—	—	$I_C = 100\mu\text{A}, V_{CE} = 1.0\text{V}$	
		40	—			$I_C = 1.0\text{mA}, V_{CE} = 1.0\text{V}$
		80	—			$I_C = 10\text{mA}, V_{CE} = 1.0\text{V}$
		100	300			$I_C = 150\text{mA}, V_{CE} = 1.0\text{V}$
		40	—			$I_C = 500\text{mA}, V_{CE} = 2.0\text{V}$
Collector-Emitter Saturation Voltage	$V_{CE(SAT)}$	—	0.40 0.75	V	$I_C = 150\text{mA}, I_B = 15\text{mA}$ $I_C = 500\text{mA}, I_B = 50\text{mA}$	
Base-Emitter Saturation Voltage	$V_{BE(SAT)}$	0.75	0.95 1.2	V	$I_C = 150\text{mA}, I_B = 15\text{mA}$ $I_C = 500\text{mA}, I_B = 50\text{mA}$	
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C_{cb}	—	6.5	pF	$V_{CB} = 5.0\text{V}, f = 1.0\text{MHz}, I_E = 0$	
Input Capacitance	C_{eb}	—	30	pF	$V_{EB} = 0.5\text{V}, f = 1.0\text{MHz}, I_C = 0$	
Input Impedance	h_{ie}	1.0	15	k Ω	$V_{CE} = 10\text{V}, I_C = 1.0\text{mA},$ $f = 1.0\text{kHz}$	
Voltage Feedback Ratio	h_{re}	0.1	8.0	$\times 10^{-4}$		
Small Signal Current Gain	h_{fe}	40	500	—		
Output Admittance	h_{oe}	1.0	30	μS		
Current Gain-Bandwidth Product	f_T	250	—	MHz		$V_{CE} = 10\text{V}, I_C = 20\text{mA},$ $f = 100\text{MHz}$
SWITCHING CHARACTERISTICS						
Delay Time	t_d	—	15	ns	$V_{CC} = 30\text{V}, I_C = 150\text{mA},$	
Rise Time	t_r	—	20	ns	$V_{BE(off)} = 2.0\text{V}, I_{B1} = 15\text{mA}$	
Storage Time	t_s	—	225	ns	$V_{CC} = 30\text{V}, I_C = 150\text{mA},$	
Fall Time	t_f	—	30	ns		$I_{B1} = I_{B2} = 15\text{mA}$

Electrical Characteristics, Q2, BSS84 P-Channel MOSFET Element @ $T_A = 25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)						
Drain-Source Breakdown Voltage	BV_{DSS}	-50	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-15	μA	$V_{DS} = -50\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$
		—	—	-60	μA	$V_{DS} = -50\text{V}, V_{GS} = 0\text{V}, T_J = 125^\circ\text{C}$
		—	—	-100	nA	$V_{DS} = -25\text{V}, V_{GS} = 0\text{V}, T_J = 25^\circ\text{C}$
Gate-Body Leakage	I_{GSS}	—	—	± 10	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 5)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.8	—	-2.0	V	$V_{DS} = V_{GS}, I_D = -1\text{mA}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	—	10	Ω	$V_{GS} = -5\text{V}, I_D = 0.100\text{A}$
Forward Transconductance	g_{FS}	.05	—	—	S	$V_{DS} = -25\text{V}, I_D = 0.1\text{A}$
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{iss}	—	—	45	pF	$V_{DS} = -25\text{V}, V_{GS} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	—	25	pF	
Reverse Transfer Capacitance	C_{rss}	—	—	12	pF	
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{D(ON)}$	—	10	—	ns	$V_{DD} = -30\text{V}, I_D = -0.27\text{A},$
Turn-Off Delay Time	$t_{D(OFF)}$	—	18	—	ns	$R_{GEN} = 50\Omega, V_{GS} = -10\text{V}$

Notes: 5. Short duration pulse test used to minimize self-heating effect.

MMBT4401 Section



T_A , AMBIENT TEMPERATURE (°C)
Fig. 1 Max Power Dissipation vs. Ambient Temperature (Total Device)

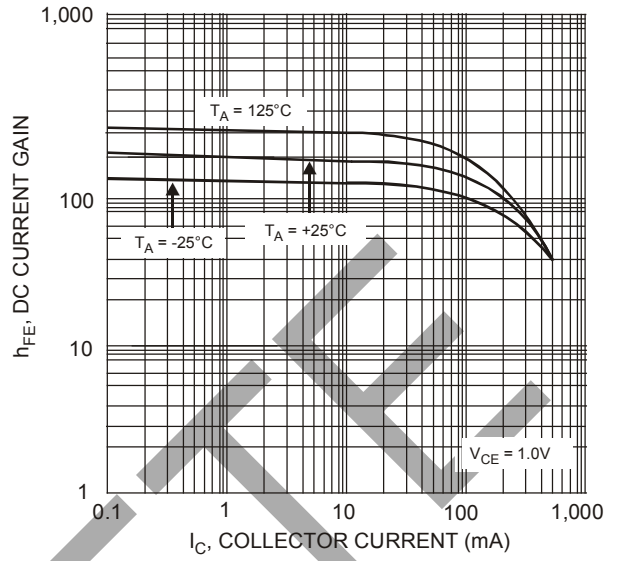


Fig. 2 Typical DC Current Gain vs. Collector Current

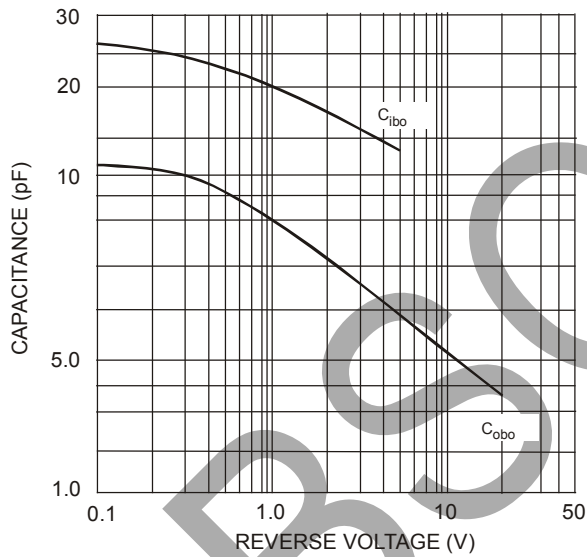


Fig. 3 Typical Capacitance

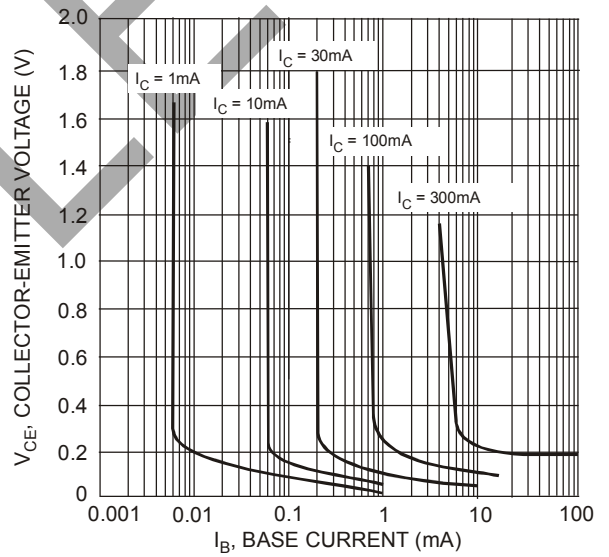


Fig. 4 Typical Collector Saturation Region

MMBT4401 Section

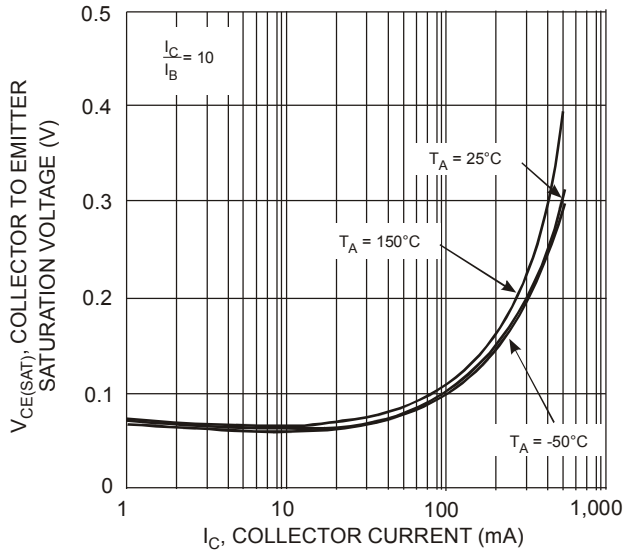


Fig. 5 Collector Emitter Saturation Voltage vs. Collector Current

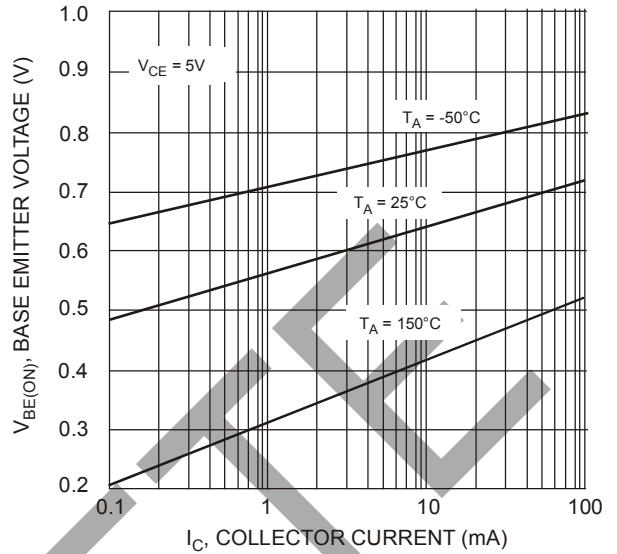


Fig. 6 Base Emitter Voltage vs. Collector Current

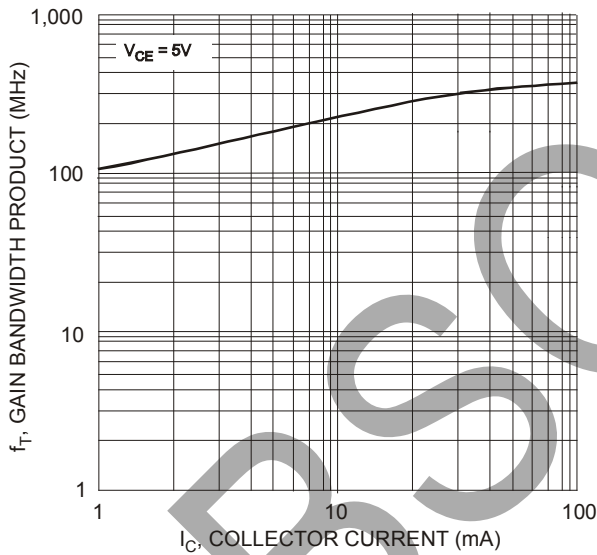


Fig. 7 Gain Bandwidth Product vs. Collector Current

BSS84 Section

OBSOLETE - PART DISCONTINUED

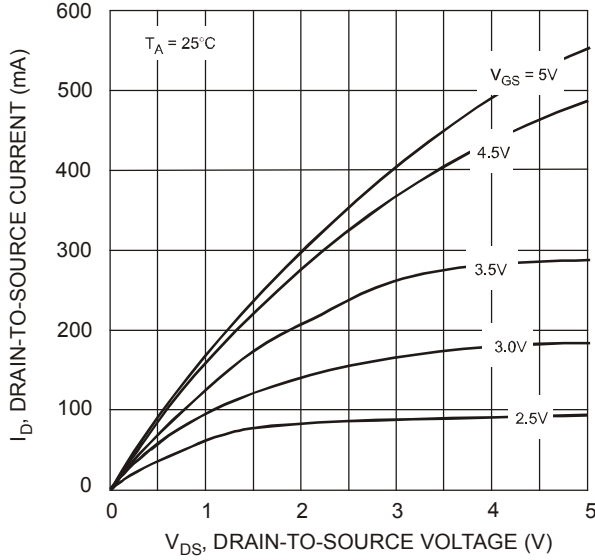


Fig. 8 Drain-Source Current vs. Drain-Source Voltage

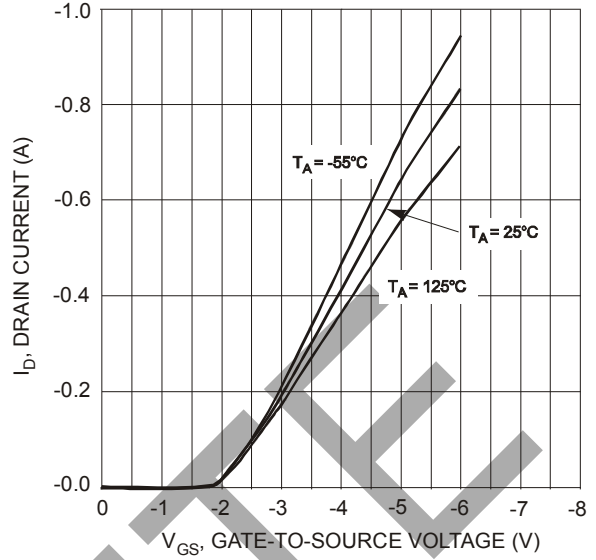


Fig. 9 Drain Current vs. Gate Source Voltage

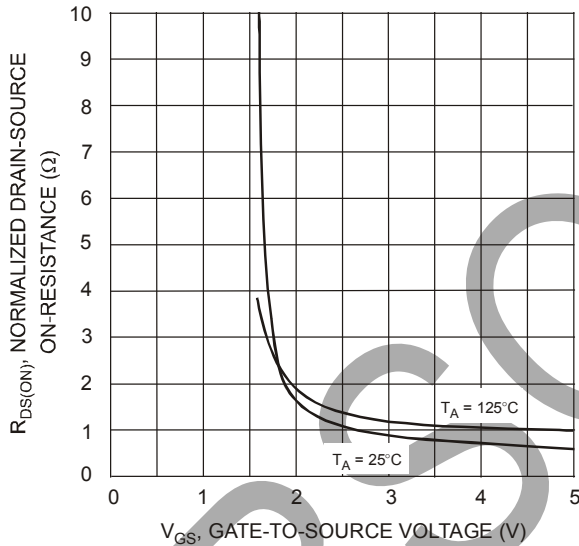


Fig. 10 On-Resistance vs. Gate-Source Voltage

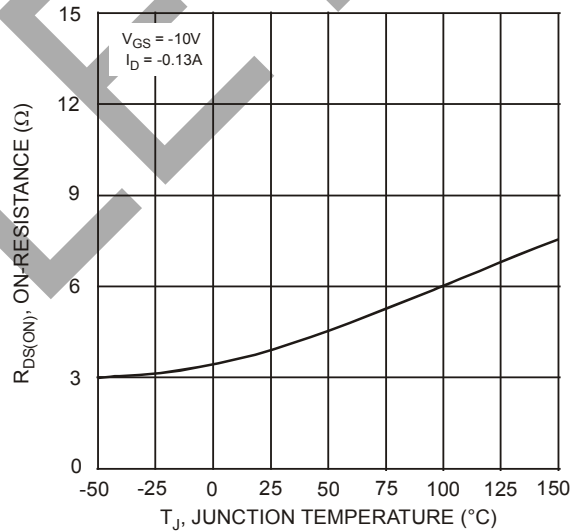


Fig. 11 On-Resistance vs. Junction Temperature

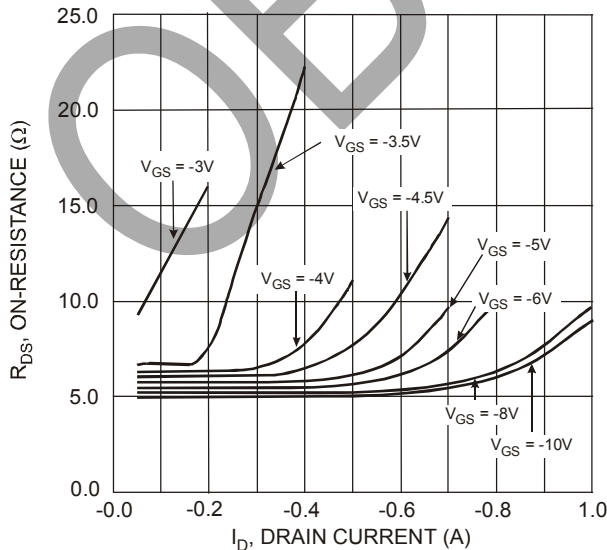


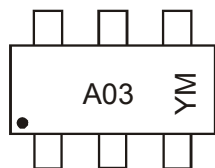
Fig. 12, On-Resistance vs. Drain Current

Ordering Information (Note 6)

Device	Packaging	Shipping
CTA2N1P-7-F	SOT-363	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information

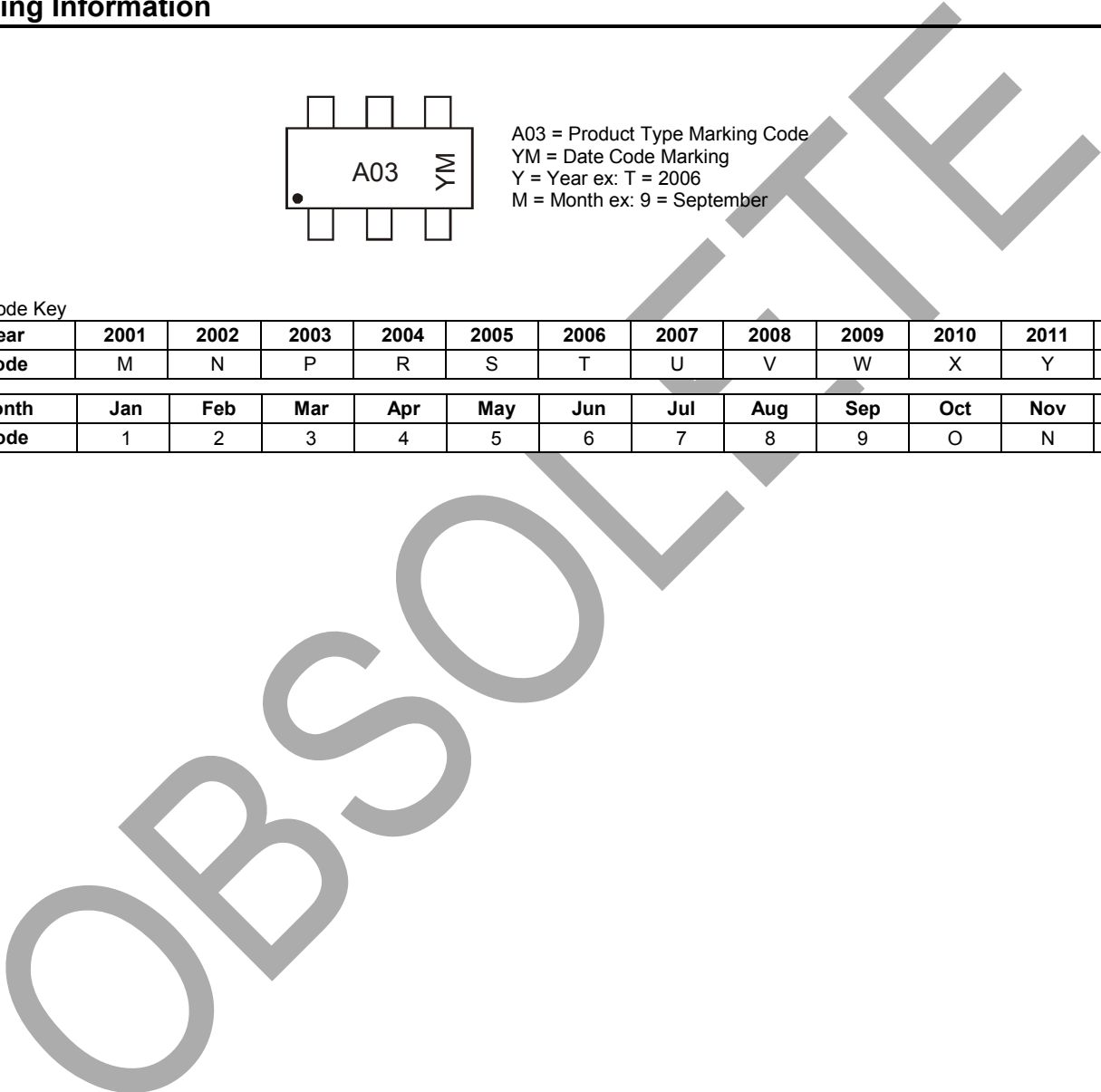


A03 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year ex: T = 2006
 M = Month ex: 9 = September

Date Code Key

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	M	N	P	R	S	T	U	V	W	X	Y	Z

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D



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