

Absolute Maximum Ratings @ 25°C

Parameter	Ratings	Units
Blocking Voltage	100	V _P
Reverse Input Voltage	5	V
Input Control Current	50	mA
Peak (10ms)	1	A
Input Power Dissipation ¹	70	mW
Total PowerDissipation ²	400	mW
Isolation voltage, Input to Output	1500	V _{rms}
Operational Temperature, Ambient	-40 to +85	°C
Storage Temperature	-40 to +125	°C

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

Typical values are characteristic of the device at +25°C, and are the result of engineering evaluations. They are provided for information purposes only, and are not part of the manufacturing testing requirements.

¹ Derate linearly 1.33 mW / °C

 $^2\,$ Derate output power linearly 3.33 mW / ^{o}C

Electrical Characteristics @ 25°C (Unless Otherwise Noted)

Parameter	Conditions	Symbol	Min	Тур	Max	Units
Output Characteristics				1		
Blocking Voltage	I _L =1μA	V _{DRM}	100	-	-	V
Load Current						
Continuous ¹	-	I,	-	-	150	mA _{rms} / mA _D
Peak	t=10ms	I _{LPK}	-	-	±350	mA _P
On-Resistance ²	I _L =150mA	R _{ON}	-	5	8	Ω
Off-State Leakage Current	V _L =100V _P , T=115°C	ILEAK	-	-	20	nA
Switching Speeds						
Turn-On	1 - 5mA = 10 / (-10)	t _{on}	-	-	2	
Turn-Off	I _F =5mA, V _L =10V	t _{off}	-	-	0.5	ms
Output Capacitance	I _F =0mA, V _L =50V, f=1MHz	C _{OUT}	-	6	-	pF
Input Characteristics						
Input Control Current to Activate	I _L =150mA	I _F	-	0.87	2	mA
Input Control Current to Deactivate	-	I _F	0.3	0.86	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.36	1.5	V
Reverse Input Current	V _R =5V	I _B	-	-	10	μA
Input/Output Characteristics						•
Capacitance, Input to Output	V _{IO} =0V, f=1MHz	CIO	-	1	-	pF

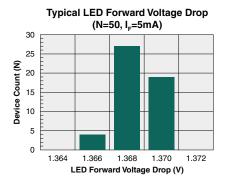
Load current derates linearly from 150mA @ 25°C to120mA @85°C. 2

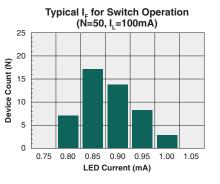
Measurement taken within 1 second of on-time.

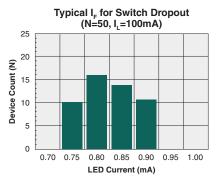


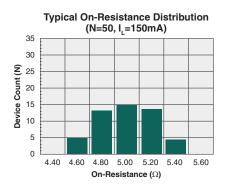
CPC1009N

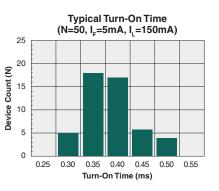
PERFORMANCE DATA*

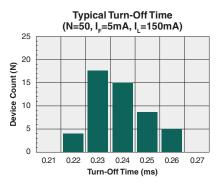


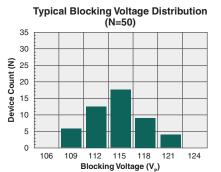


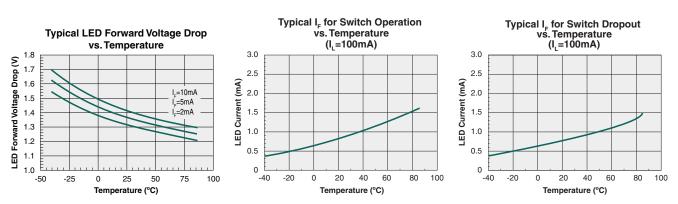












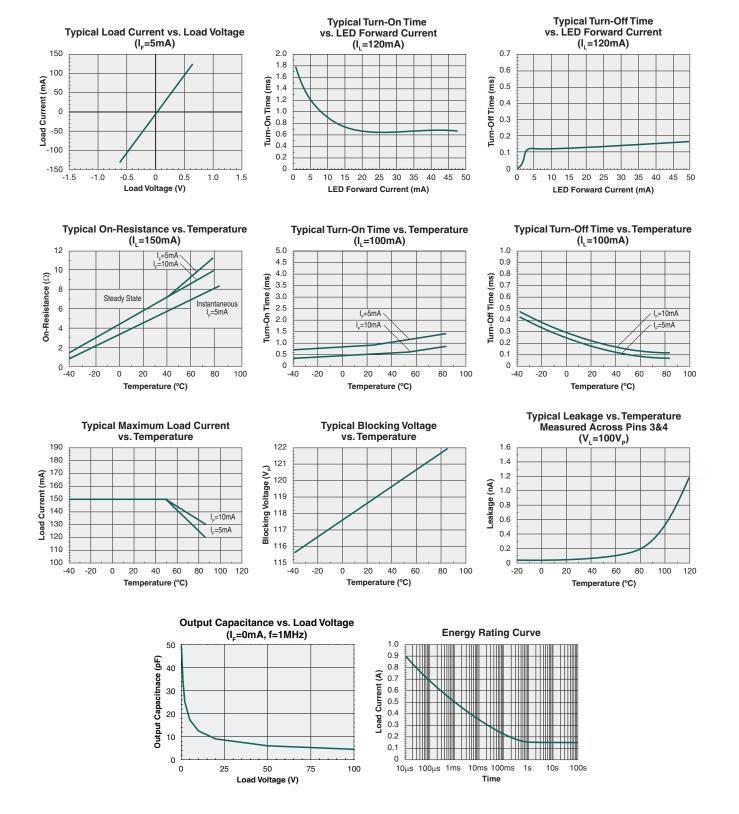
*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

Downloaded from Arrow.com.



CPC1009N

PERFORMANCE DATA*



*Unless otherwise noted, data presented in these graphs is typical of device operation at 25°C.

4



Manufacturing Information

Moisture Sensitivity

All plastic encapsulated semiconductor packages are susceptible to moisture ingression. IXYS Integrated Circuits classifies its plastic encapsulated devices for moisture sensitivity according to the latest version of the joint industry standard, IPC/JEDEC J-STD-020, in force at the time of product evaluation. We test all of our products to the maximum conditions set forth in the standard, and guarantee proper operation of our devices when handled according to the limitations and information in that standard as well as to any limitations set forth in the information or standards referenced below.

Failure to adhere to the warnings or limitations as established by the listed specifications could result in reduced product performance, reduction of operable life, and/or reduction of overall reliability.

This product carries a **Moisture Sensitivity Level (MSL)** classification as shown below, and should be handled according to the requirements of the latest version of the joint industry standard **IPC/JEDEC J-STD-033**.

Device	Moisture Sensitivity Level (MSL) Classification
CPC1009N	MSL 3

ESD Sensitivity



This product is ESD Sensitive, and should be handled according to the industry standard JESD-625.

Soldering Profile

Provided in the table below is the **IPC/JEDEC J-STD-020** Classification Temperature (T_C) and the maximum dwell time the body temperature of these surface mount devices may be ($T_C - 5$)°C or greater. The Classification Temperature sets the Maximum Body Temperature allowed for these devices during reflow soldering processes.

Device	Classification Temperature (T _c)	Dwell Time (t _p)	Max Reflow Cycles
CPC1009N	260°C	30 seconds	3

Board Wash

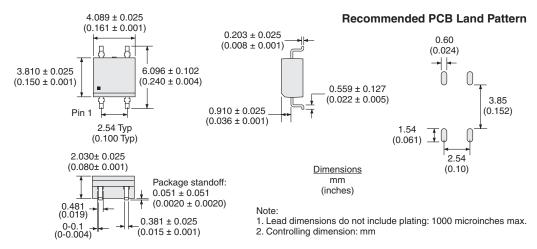
IXYS Integrated Circuits recommends the use of no-clean flux formulations. Board washing to reduce or remove flux residue following the solder reflow process is acceptable provided proper precautions are taken to prevent damage to the device. These precautions include but are not limited to: using a low pressure wash and providing a follow up bake cycle sufficient to remove any moisture trapped within the device due to the washing process. Due to the variability of the wash parameters used to clean the board, determination of the bake temperature and duration necessary to remove the moisture trapped within the package is the responsibility of the user (assembler). Cleaning or drying methods that employ ultrasonic energy may damage the device and should not be used. Additionally, the device must not be exposed to halide flux or solvents.



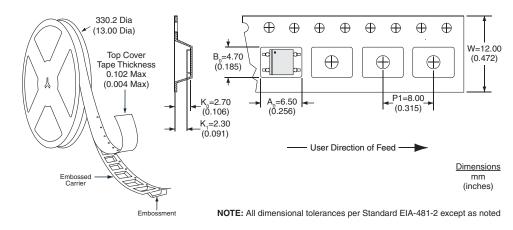


MECHANICAL DIMENSIONS

CPC1009N



CPC1009NTR Tape & Reel



For additional information please visit our website at: https://www.ixysic.com



Disclaimer Notice - Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at https://www.littelfuse.com/disclaimer-electronics.

Specification: DS-CPC1009N-R08 @Copyright 2021, Littelfuse, Inc. OptoMOS® is a registered trademark of IXYS Integrated Circuits All rights reserved. Printed in USA. 8/5/2021