

Maximum Ratings

Parameter	Symbol	Value	Unit
Drain source voltage	V _{DS}	55	V
Drain-gate voltage, R_{GS} = 20 k Ω	V DGR	55	
Gate source voltage	V _{GS}	±20	
Nominal load current (ISO 10483)	I _{D(ISO)}		A
$V_{\rm GS}$ = 4.5 V, $V_{\rm DS}$ \leq 0.5 V, $T_{\rm C}$ = 85 °C		19	
$V_{ m GS}$ = 10 V, $V_{ m DS}$ \leq 0.5 V, $T_{ m C}$ = 85 °C		26	
Continuous drain current ¹⁾	I _D	35	
<i>T</i> _C = 100 °C, <i>V</i> _{GS} = 4.5V			
Pulsed drain current	I _{D puls}	188	
Avalanche energy, single pulse	E _{AS}	1.65	J
$I_{\rm D}$ = 19 A, $R_{\rm GS}$ = 25 Ω			
Power dissipation	P _{tot}	170	W
<i>T</i> _C = 25 °C			
Operating temperature ²)	Ti	-40+175	°C
Peak temperature (single event)	<i>T</i> _{ipeak}	200	
Storage temperature	T _{stg}	-55 +150	
DIN humidity category, DIN 40 040		E	
IEC climatic category; DIN IEC 68-1		40/150/56	

¹current limited by bond wire

 $^2 \text{Note:}$ Thermal trip temperature of temperature sensor is below 175°C

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Thermal Characteristics

Parameter	Symbol	Values		Unit	
		min.	typ.	max.	
Characteristics					
junction - case:	R _{thJC}	-	-	0.88	K/W
Thermal resistance @ min. footprint	R _{th(JA)}	-	-	62	
Thermal resistance @ 6 cm ² cooling area ¹⁾	R _{th(JA)}	-	33	40	

Electrical Characteristics

Parameter	Symbol	Values			Unit
at T_{j} = 25°C, unless otherwise specified		min.	typ.	max.	
Static Characteristics					
Drain-source breakdown voltage	V _{(BR)DSS}	55	-	-	V
<i>V</i> _{GS} = 0 V, <i>I</i> _D = 0.25 mA					
Gate threshold voltage, $V_{GS} = V_{DS}$	V _{GS(th)}				
<i>I</i> _D = 130 μA		1.2	1.6	2	
<i>I</i> _D = 250 μA		-	1.65	-	
Zero gate voltage drain current	I _{DSS}				μA
$V_{\rm DS}$ = 50 V, $V_{\rm GS}$ = 0 V, $T_{\rm j}$ = -40 °C		-	-	0.1	
$V_{\rm DS}$ = 50 V, $V_{\rm GS}$ = 0 V, $T_{\rm j}$ = 25 °C		-	0.1	1	
$V_{\rm DS}$ = 50 V, $V_{\rm GS}$ = 0 V, $T_{\rm j}$ = 150 °C		-	-	100	
Gate-source leakage current	I _{GSS}				nA
V_{GS} = 20 V, V_{DS} = 0 V, T_{j} = 25 °C		-	10	100	
$V_{\rm GS}$ = 20 V, $V_{\rm DS}$ = 0 V, $T_{\rm j}$ = 150 °C		-	20	100	
Drain-Source on-state resistance	R _{DS(on)}				mΩ
<i>V</i> _{GS} = 4.5 V, <i>I</i> _D = 19 A		-	16	18	
<i>V</i> _{GS} = 10 V, <i>I</i> _D = 19 A		-	11.5	13	

¹ Device on 50mm*50mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70µm thick) copper area for drain connection. PCB mounted vertical without blown air.



Electrical Characteristics

Parameter	Symbol		Unit		
at $T_j = 25^{\circ}$ C, unless otherwise specified		min.	typ.	max.	
Dynamic Characteristics				•	
Forward transconductance	<i>9</i> fs	25	-	-	S
$V_{\rm DS}$ > 2* $I_{\rm D}$ * $R_{\rm DS(on)max}$, $I_{\rm D}$ = 35 A					
Input capacitance	C _{iss}	-	2130	2660	pF
$V_{\rm GS}$ = 0 V, $V_{\rm DS}$ = 25 V, f = 1 MHz					
Output capacitance	C _{oss}	-	600	750	
<i>V</i> _{GS} = 0 V, <i>V</i> _{DS} = 25 V, <i>f</i> = 1 MHz					
Reverse transfer capacitance	C _{rss}	-	320	400	1
<i>V</i> _{GS} = 0 V, <i>V</i> _{DS} = 25 V, <i>f</i> = 1 MHz					
Turn-on delay time	t _{d(on)}	-	15	25	ns
$V_{\rm DD}$ = 30 V, $V_{\rm GS}$ = 4.5 V, $I_{\rm D}$ = 47 A,					
$R_{\rm G}$ = 2.2 Ω					
Rise time	t _r	-	70	105	1
$V_{\rm DD}$ = 30 V, $V_{\rm GS}$ = 4.5 V, $I_{\rm D}$ = 47 A,					
$R_{\rm G}$ = 2.2 Ω					
Turn-off delay time	t _{d(off)}	-	40	60	1
$V_{\rm DD}$ = 30 V, $V_{\rm GS}$ = 4.5 V, $I_{\rm D}$ = 47 A,					
$R_{\rm G}$ = 2.2 Ω					
Fall time	t _f	-	25	40	1
$V_{\rm DD}$ = 30 V, $V_{\rm GS}$ = 4.5 V, $I_{\rm D}$ = 47 A,					
$R_{\rm G}$ = 2.2 Ω					
Gate Charge Characteristics				•	
Gate charge at threshold	Q _{g(th)}	-	2.5	3.8	nC
V_{DD} = 40 V, I_{D} = 0.1 A, V_{GS} = 0 to 1 V					
Gate charge at 5.0 V	Q _{g(5)}	-	50	75	1
$V_{\rm DD}$ = 40 V, $I_{\rm D}$ = 47 A, $V_{\rm GS}$ = 0 to 5 V					
Gate charge total	Q _{g(total)}	-	85	130	1
V_{DD} = 40 V, I_{D} = 47 A, V_{GS} = 0 to 10 V	9(10101)				
Gate plateau voltage	V _(plateau)	-	4.5	-	V

 V_{DD} = 40 V, I_{D} = 47 A

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Electrical Characteristics

Parameter	Symbol	Values			Unit
at T_j = 25°C, unless otherwise specified		min.	typ.	max.]
Reverse Diode		•	•	•	•
Inverse diode continuous forward current	I _S	35	-	-	A
<i>T</i> _C = 25 °C					
Inverse diode direct current, pulsed	/ _{FM}	188	-	-	
<i>T</i> _C = 25 °C					
Inverse diode forward voltage	V _{SD}	-	1.25	1.8	V
<i>V</i> _{GS} = 0 V, <i>I</i> _F = 94 A					
Reverse recovery time	<i>t</i> _{rr}	-	110	165	ns
$V_{\rm R}$ = 30 V, $I_{\rm F}$ = $I_{\rm S}$, d $i_{\rm F}$ /d t = 100 A/µs					
Reverse recovery charge	Q _{rr}	-	0.23	0.35	μC
$V_{\rm R}$ = 30 V, $I_{\rm F}$ = $I_{\rm S}$, d $i_{\rm F}$ /d t = 100 A/µs					

Sensor Characteristics

For temperature sensing, i.e. temperature protection, please consider application note "Temperature sense concept - Speed TEMPFET".

For short circuit protection please consider application note "Short circuit behaviour of the Speed TEMPFET family".

All application notes are available at http://www.infineon.com/tempfet/

Forward voltage	V _{AK(on)}				V
/ _{AK(on)} = 5 mA, <i>T</i> _j = -40+150 °C		-	1.3	1.4	
/ _{AK(on)} = 1.5 mA, <i>T</i> _j = 150 °C		-	-	0.9	
Sensor override		-	-	10	
<i>t</i> _P = 100 μs, <i>T</i> _j = -40+150 °C					
Forward current	I _{AK(on)}	-	-	5	mA
<i>T</i> _j = −40+150 °C					
Sensor override		-	-	600	
<i>t</i> _P = 100 μs, <i>T</i> _j = -40+150 °C					



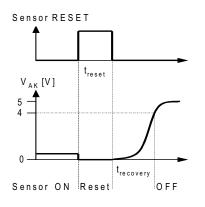
Electrical Characteristics

Parameter	Symbol	Values		Unit	
at T_{i} = 25°C, unless otherwise specified		min.	typ.	max.	
Sensor Characteristics					
Temperature sensor leakage current	I _{AK(off)}	-	-	4	μA
<i>T</i> _j = 150 °C					
Min. reset pulse duration ¹⁾	<i>t</i> _{reset}	100	-	-	μs
<i>T</i> _j = -40+150 °C, <i>I</i> _{AK(on)} = 0.3 mA,					
V _{AK(Reset)} ^{<0.5V}					
V _{AK} Recovery time ¹⁾²⁾	<i>t</i> _{recovery}	-	-	150]
<i>T</i> _j = -40+150 °C, <i>I</i> _{AK(on)} = 0.3 mA					

Characteristics

Holding current, V _{AK(off)} = 5V	I _{AK(hold)}				mA
<i>T</i> _j = 25 °C		0.05	-	0.5	
<i>T</i> _j = 150 °C		0.05	-	0.3	
Thermal trip temperature	T _{TS(on)}	150	160	170	°C
V _{TS} = 5V					
Turn-off time (Pin G+A and K+S connected)	t _{off}	0.5	-	2.5	μs
V _{TS} = 5V, <i>I</i> _{TS(on)} = 2 mA					
Reset voltage	V _{AK(reset)}	0.5	-	-	V
<i>T</i> _j = -40+150°C					

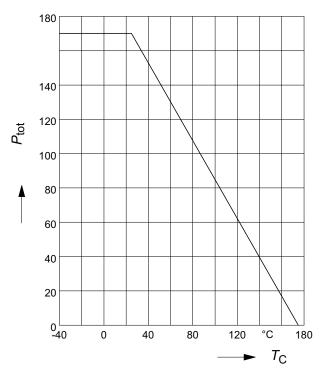
Sensor recovery behaviour:



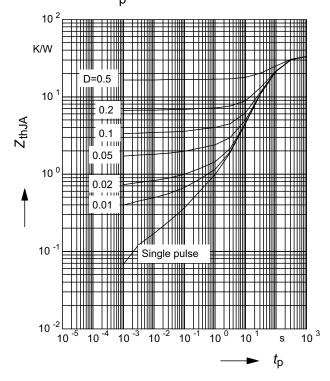
 $^1 See$ diagram Sensor recovery behaviour $^2 T ime$ after reset pulse until V_{AK} reaches 4V again



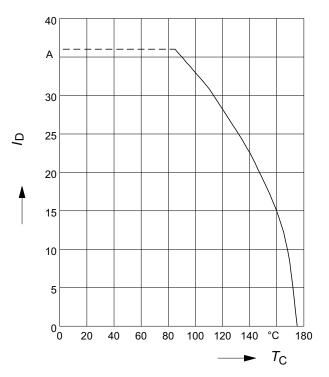
1 Maximum allowable power dissipation P_{tot} = f(T_C)



3 Typ. transient thermal impedance $Z_{\text{thJA}}=f(t_p) @ 6 \text{ cm}^2$ cooling area Parameter: $D=t_p/T$

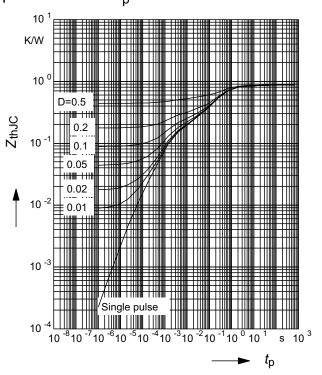


2 Drain current $I_{\rm D}$ = f($T_{\rm C}$); $V_{\rm GS} \ge 4.5 V$



4 Transient thermal impedance

 $Z_{\text{thJC}} = f(t_p)$ parameter : $D = t_p/T$

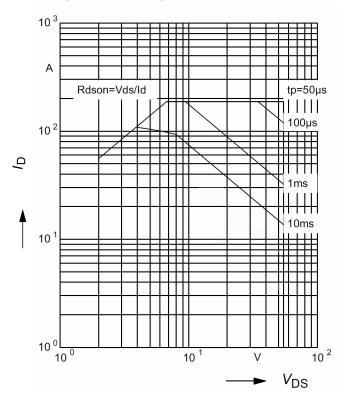


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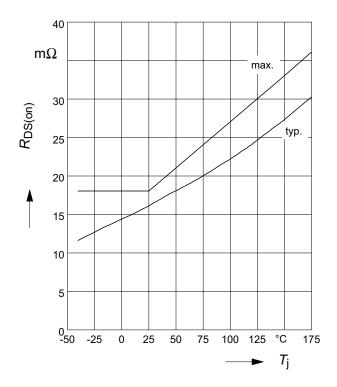


5 Safe operating area

*I*_D=f(V_{DS}); *D*=0.01; *T*_C=25°C

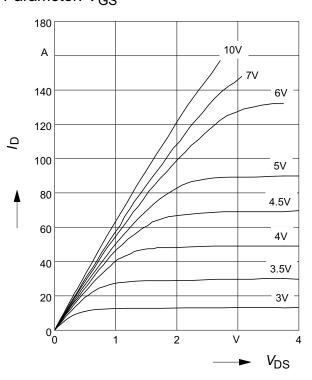


7 On-state resistance *R*_{ON} = f(T_i); *I*_D=19A; *V*_{GS} = 4.5V



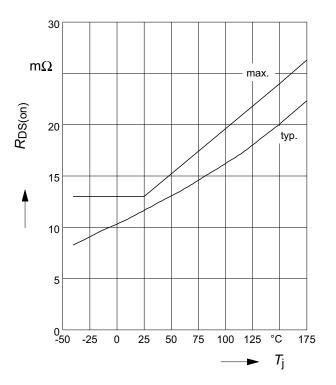
6 Typ. output characteristic

I_D = f(V_{DS}); T_j=25°C Parameter: V_{GS}



8 On-state resistance

R_{ON} = f(T_j); *I*_D=19A; *V*_{GS} = 10V

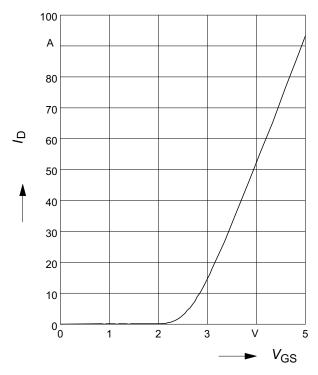


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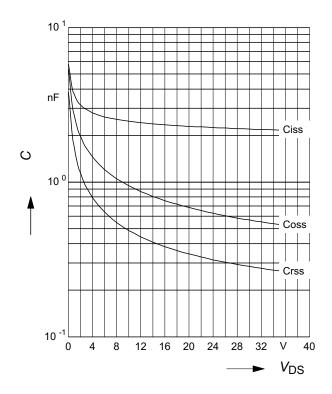


9 Typ. transfer characteristics

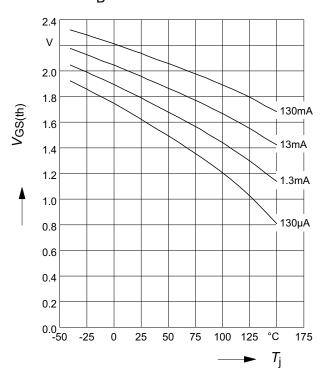
 $I_{D} = f(V_{GS}); V_{DS} = 12V; T_{j} = 25^{\circ}C$



11 Typ. capacitances *C* = f(*V*_{DS}); *V*_{GS}=0 V, *f*=1 MHz

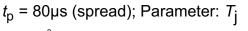


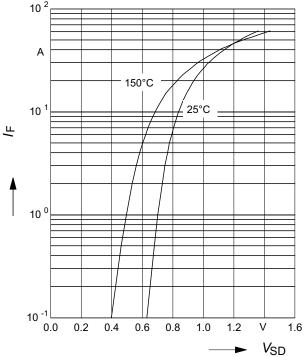
10 Typ. input threshold voltage V_{GS(th)} = f(Tj); V_{DS}=V_{GS} Parameter: I_D



12 Typ. forward charcteristics of

reverse diode *I*_F = f(*V*_{SD})

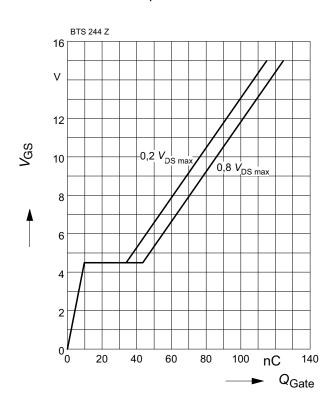




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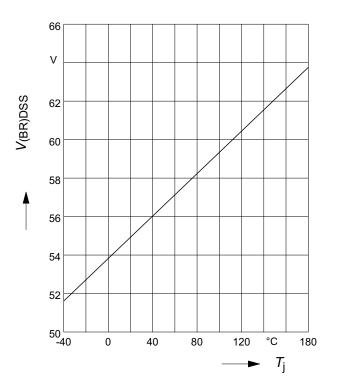


13 Typ. gate charge V_{GS} = f(Q_{Gate}); *I*_{D puls} = 47A



14 Drain-source break down voltage

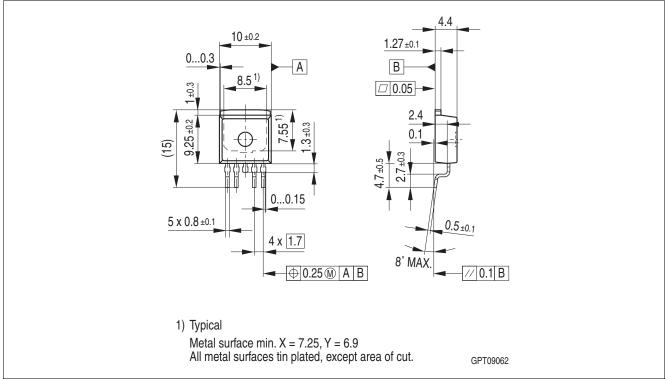
 $V_{(BR)DSS} = f(T_j)$





Package Outlines

1 Package Outlines





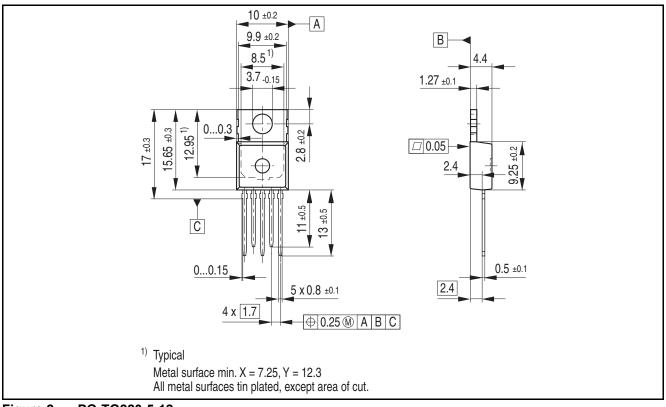


Figure 2 PG-TO220-5-12

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Package Outlines

Green Product (RoHS compliant)

To meet the world-wide customer requirements for environmentally friendly products and to be compliant with government regulations the device is available as a green product. Green products are RoHS-Compliant (i.e Pb-free finish on leads and suitable for Pb-free soldering according to IPC/JEDEC J-STD-020).

For further information on alternative packages, please visit our website: http://www.infineon.com/packages.



Revision History

2 Revision History

Revision	Date	Changes
1.4	2013-07-26	page 1, 11: updated package name and package drawing: PG-TO220-5-62 to PG-TO263-5-2 (SMD) PG-TO220-5-43 to PG-TO220-5-12 (THD, straight leads); page 1, 11/12: removed package: PG-TO220-5-3 (THD, staggered leads) page 1: added sales names for the different packages;
		page 8: updated description figure 5
1.3	2009-12-04	updated package drawing of PG-TO220-5-62
1.2	2009-07-31	removed 100ms and DC line in SOA diagram
1.1	2008-11-10	all pages: added new Infineon logo Initial version of RoHS-compliant derivate of the BTS244Z Page 1 and 12: added RoHS compliance statement and Green product feature Page 1, 11 and 12: Package changed to RoHS compliant version page 13: added Revision history page 14: update of disclaimer

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