2. Pinning information

Table 3. **Pinning** Simplified outline Pin Description **Graphic symbol SOT223** 1 base 2, 4 2 collector 3 emitter 4 collector sym016 **SOT89** 1 emitter 2 collector 3 base SOT1061 1 base] 3 | emitter 3 collector sym021 Transparent top view

3. Ordering information

Table 4. Ordering information

Type number[1]	Package					
	Name	Description	Version			
BCP55	SC-73	plastic surface-mounted package with increased heatsink; 4 leads	SOT223			
BCX55	SC-62	plastic surface-mounted package; exposed die pad for good heat transfer; 3 leads	SOT89			
BC55PA	HUSON3	plastic thermal enhanced ultra thin small outline package; no leads; 3 terminals; body 2 \times 2 \times 0.65 mm	SOT1061			

^[1] Valid for all available selection groups.

BCP55_BCX55_BC55PA

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4. Marking

Table 5. Marking codes

3	
Type number	Marking code
BCP55	BCP55
BCP55-10	BCP55/10
BCP55-16	BCP55/16
BCX55	BE
BCX55-10	BG
BCX55-16	BM
BC55PA	AW
BC55-10PA	ВН
BC55-16PA	ВЈ

5. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter	-	60	V
V_{CEO}	collector-emitter voltage	open base	-	60	V
V _{EBO}	emitter-base voltage	open collector	-	5	V
I _C	collector current		-	1	А
I _{CM}	peak collector current	single pulse; $t_p \le 1 \text{ ms}$	-	2	Α
I _B	base current		-	0.3	А
I _{BM}	peak base current	single pulse; $t_p \le 1 \text{ ms}$	-	0.3	Α
P _{tot}	total power dissipation	$T_{amb} \le 25 ^{\circ}C$			
	BCP55		<u>[1]</u> _	0.65	W
			[2] _	1.00	W
			[3]	1.35	W
	BCX55		<u>[1]</u> _	0.50	W
			[2]	0.95	W
			[3]	1.35	W
	BC55PA		<u>[1]</u> _	0.42	W
			[2]	0.83	W
			[3]	1.10	W
			[4]	0.81	W
			<u>[5]</u> _	1.65	W
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-55	+150	°C
T _{stg}	storage temperature		-65	+150	°C

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

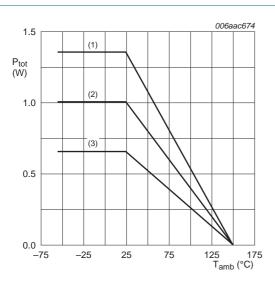
BCP55_BCX55_BC55PA

^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

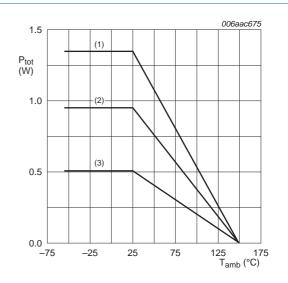
^[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

^[5] Device mounted on an FR4 PCB, 4-layer copper, tin-plated, mounting pad for collector 1 cm².



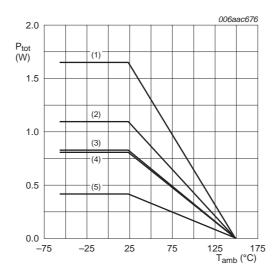
- (1) FR4 PCB, mounting pad for collector 6 cm²
- (2) FR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

Fig 1. Power derating curves SOT223



- (1) FR4 PCB, mounting pad for collector 6 cm²
- (2) FR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

Fig 2. Power derating curves SOT89



- (1) FR4 PCB, 4-layer copper, mounting pad for collector 1 cm²
- (2) FR4 PCB, single-sided copper, mounting pad for collector 6 cm²
- (3) FR4 PCB, single-sided copper, mounting pad for collector 1 cm²
- (4) FR4 PCB, 4-layer copper, standard footprint
- (5) FR4 PCB, single-sided copper, standard footprint

Fig 3. Power derating curves SOT1061

6. Thermal characteristics

Table 7. Thermal characteristics

Table 7.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air				
	BCP55		<u>[1]</u> -	-	192	K/W
			[2] _	-	125	K/W
			[3] _	-	93	K/W
	BCX55		<u>[1]</u> -	-	250	K/W
			[2] _	-	132	K/W
			[3] _	-	93	K/W
	BC55PA		<u>[1]</u> _	-	298	K/W
			[2] _	-	151	K/W
			[3] _	-	114	K/W
			[4] _	-	154	K/W
			[5] _	-	76	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point					
	BCP55		-	-	16	K/W
	BCX55		-	-	16	K/W
	BC55PA		-	-	20	K/W

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

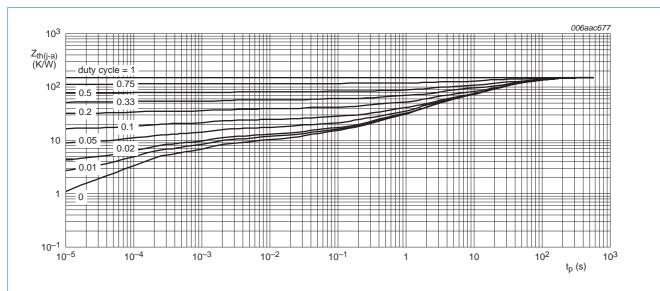
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^[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

^[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm².

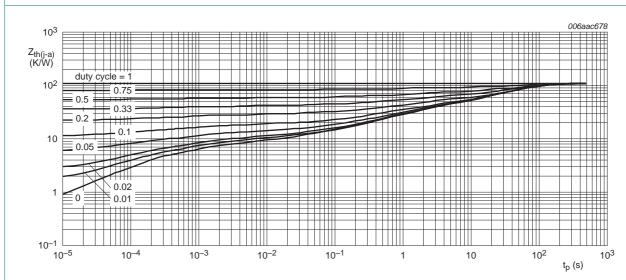
^[4] Device mounted on an FR4 PCB, 4-layer copper, tin-plated and standard footprint.

^{5]} Device mounted on an FR4 PCB, 4-layer copper, tin-plated, mounting pad for collector 1 cm².



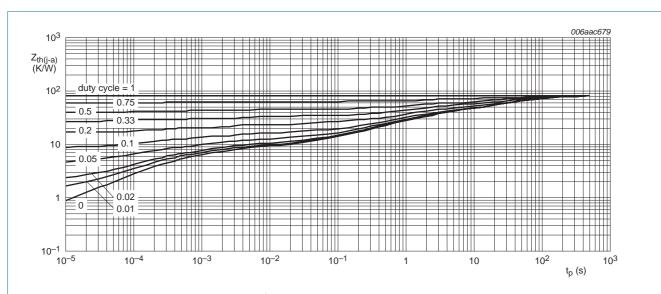
FR4 PCB, standard footprint

Fig 4. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT223; typical values



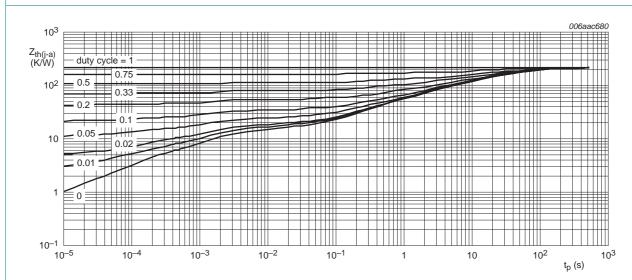
FR4 PCB, mounting pad for collector 1 cm²

Fig 5. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT223; typical values



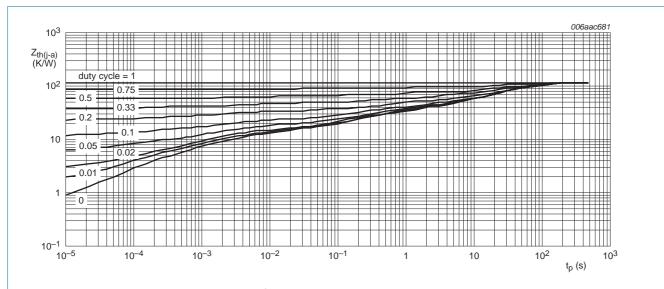
FR4 PCB, mounting pad for collector 6 cm²

Fig 6. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT223; typical values



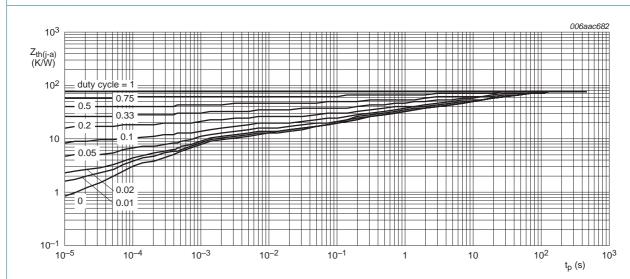
FR4 PCB, standard footprint

Fig 7. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT89; typical values



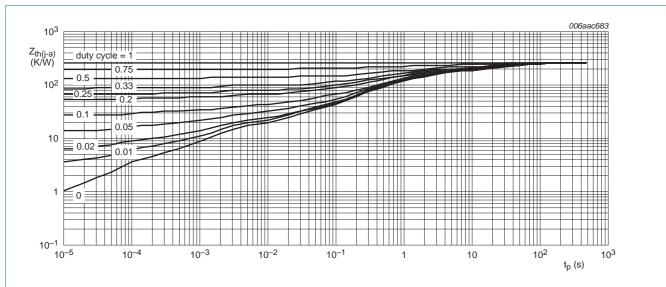
FR4 PCB, mounting pad for collector 1 cm²

Fig 8. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT89; typical values



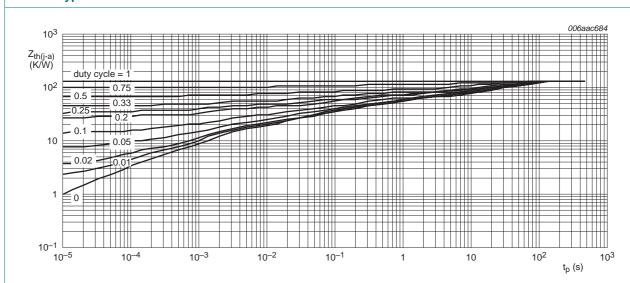
FR4 PCB, mounting pad for collector 6 cm²

Fig 9. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT89; typical values



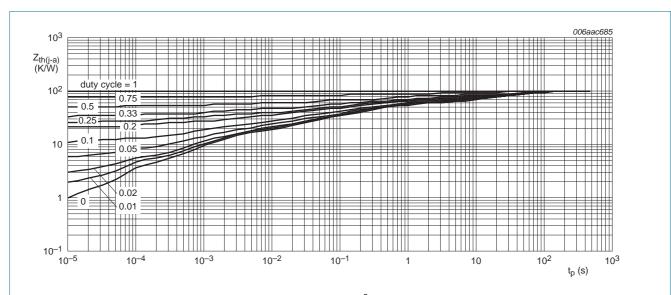
FR4 PCB, single-sided copper, standard footprint

Fig 10. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT1061; typical values



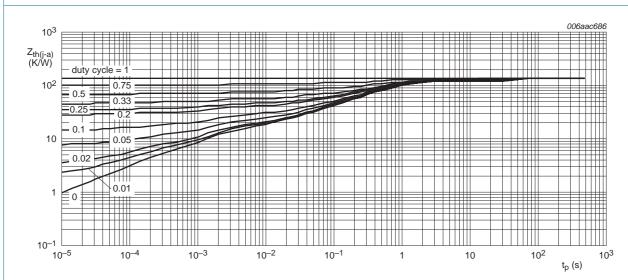
FR4 PCB, single-sided copper, mounting pad for collector 1 cm²

Fig 11. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT1061; typical values



FR4 PCB, single-sided copper, mounting pad for collector 6 cm²

Fig 12. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT1061; typical values



FR4 PCB, 4-layer copper, standard footprint

Fig 13. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT1061; typical values

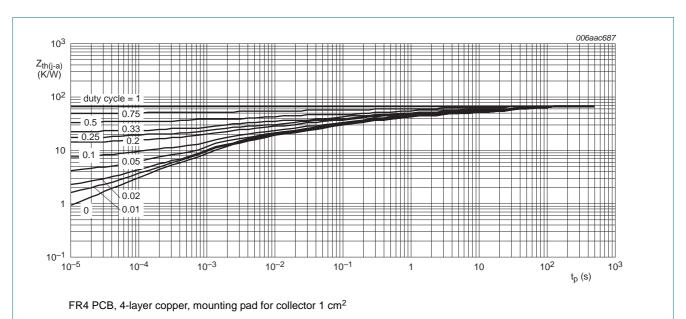


Fig 14. Transient thermal impedance from junction to ambient as a function of pulse duration for SOT1061; typical values

7. Characteristics

Table 8. Characteristics

 $T_{amb} = 25$ °C unless otherwise specified.

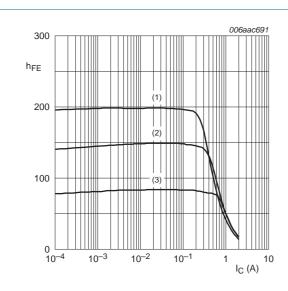
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I_{CBO}	collector-base cut-off	$V_{CB} = 30 \text{ V}; I_{E} = 0 \text{ A}$	-	-	100	nA
	current	$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A};$ $T_j = 150 ^{\circ}\text{C}$	-	-	10	μΑ
I _{EBO}	emitter-base cut-off current	$V_{EB} = 5 \text{ V}; I_{C} = 0 \text{ A}$	-	-	100	nA
h _{FE}	DC current gain	V _{CE} = 2 V				
		$I_C = 5 \text{ mA}$	<u>[1]</u> 63	-	-	
		I _C = 150 mA	<u>[1]</u> 63	-	250	
		$I_C = 500 \text{ mA}$	<u>[1]</u> 40	-	-	
	DC current gain	V _{CE} = 2 V				
	h _{FE} selection -10	$I_C = 150 \text{ mA}$	<u>[1]</u> 63	-	160	
	h _{FE} selection -16	$I_C = 150 \text{ mA}$	<u>11</u> 100	-	250	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}$	[1] -	-	0.5	V
V_{BE}	base-emitter voltage	$V_{CE} = 2 \text{ V}; I_{C} = 500 \text{ mA}$	<u>[1]</u> _	-	1	V
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz	-	6	-	pF
f _T	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 50 \text{ mA};$ f = 100 MHz	100	180	-	MHz

^[1] Pulse test: $t_p \le 300 \ \mu s$; $\delta = 0.02$.

BCP55_BCX55_BC55PA

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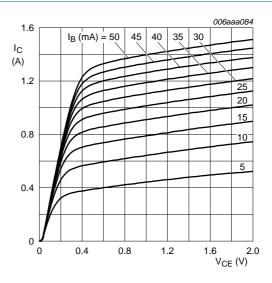
V_{CE} = 2 V

(1) $T_{amb} = 100 \, ^{\circ}C$

(2) $T_{amb} = 25 \, ^{\circ}C$

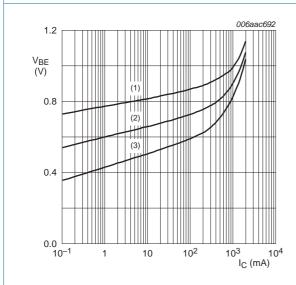
(3) $T_{amb} = -55 \, ^{\circ}C$

Fig 15. DC current gain as a function of collector current; typical values



T_{amb} = 25 °C

Fig 16. Collector current as a function of collector-emitter voltage; typical values



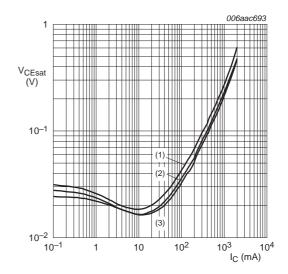
 $V_{CE} = 2 V$

(1) $T_{amb} = -55 \, ^{\circ}C$

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = 100 \, ^{\circ}C$

Fig 17. Base-emitter voltage as a function of collector current; typical values



 $I_{\rm C}/I_{\rm B} = 10$

(1) $T_{amb} = 100 \, ^{\circ}C$

(2) $T_{amb} = 25 \, ^{\circ}C$

(3) $T_{amb} = -55 \, ^{\circ}C$

Fig 18. Collector-emitter saturation voltage as a function of collector current; typical values

BCP55_BCX55_BC55PA

8. Test information

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline

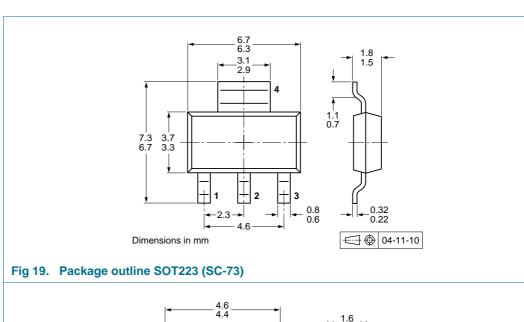
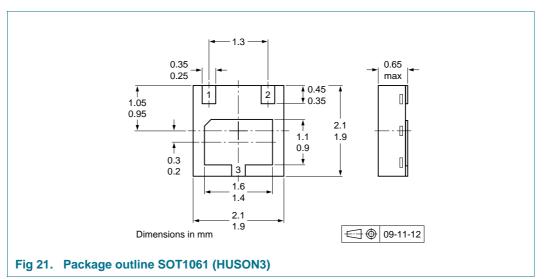


Fig 20. Package outline SOT89 (SC-62/TO-243)

Dimensions in mm

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⊕ 06-08-29



10. Packing information

Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Туре	Package	•		Packing quantity		
number[2]				1000	3000	4000
BCP55	SOT223	8 mm pitch, 12 mm tape and reel		-115	-	-135
BCX55 SOT89		8 mm pitch, 12 mm tape and reel; T1	[3]	-115	-	-135
		8 mm pitch, 12 mm tape and reel; T3	[4]	-146	-	-
BC55PA	SOT1061	4 mm pitch, 8 mm tape and reel		-	-115	-

^[1] For further information and the availability of packing methods, see Section 14.

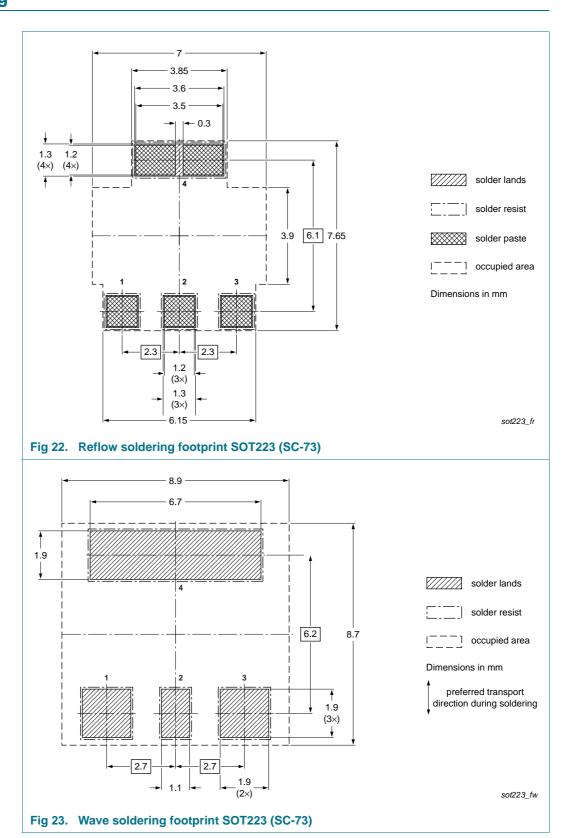
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^[2] Valid for all available selection groups.

^[3] T1: normal taping

^[4] T3: 90° rotated taping

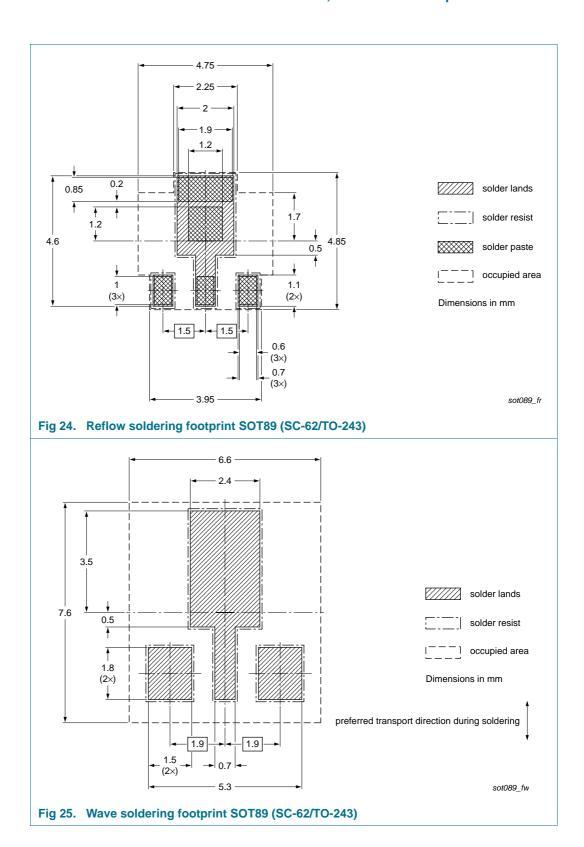
11. Soldering

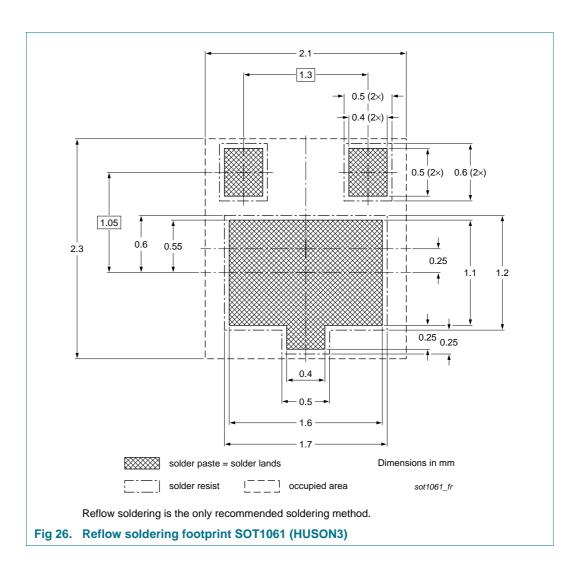


BCP55_BCX55_BC55PA

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12. Revision history

Table 10. Revision history

,				
Document ID	Release date	Data sheet status	Change notice	Supersedes
BCP55_BCX55_BC55PA v.8	20111024	Product data sheet	-	BC637_BCP55_BCX55 v.7
Modifications:	 Type numb 	er removed: BC637		
	 Type numb 	er added: BC55PA, BC55-	10PA and BC55-1	6PA
	Section 1 "I	Product profile": updated		
	 Section 2 "I 	Pinning information": upda	ted	
	• <u>Table 6</u> and	d <u>7</u> : updated according to la	atest measuremen	ts
	 Figure 1, 2, 	4, 5, 7 to 9, 15, 17 and 18	: updated	
	 Figure 3, 6, 	10 to 14: added		
	 Section 8 " 	Test information": added		
	• Section 10	"Packing information": upo	lated	
	 Section 11 	"Soldering": added		
	• Section 13	"Legal information": update	ed	
BC637_BCP55_BCX55 v.7	20070625	Product data sheet	-	BC637_BCP55_BCX55 v.6
BC637_BCP55_BCX55 v.6	20050218	Product data sheet	CPCN2004050	BC635_637_639 v.4
			29	BCP54_55_56 v.5
				BCX54_55_56 v.4
BC635_637_639 v.4	20011010	Product specification	-	BC635_637_639 v.3
BCP54_55_56 v.5	20030206	Product specification	-	BCP54_55_56 v.4
BCX54_55_56 v.4	20011010	Product specification	-	BCX54 55 56 v.3

13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- [1] Please consult the most recently issued document before initiating or completing a design
- [2] The term 'short data sheet' is explained in section "Definitions"
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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BCP55_BCX55_BC55PA

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BCP55; BCX55; BC55PA

60 V, 1 A NPN medium power transistors

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

13.4 Trademarks

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14. Contact information

For more information, please visit: http://www.nexperia.com

For sales office addresses, please send an email to: salesaddresses@nexperia.com

BCP55; BCX55; BC55PA

Nexperia

60 V, 1 A NPN medium power transistors

15. Contents

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For more information, please visit: http://www.nexperia.com For sales office addresses, please send an email to: salesaddresses@nexperia.com Date of release: 24 October 2011

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