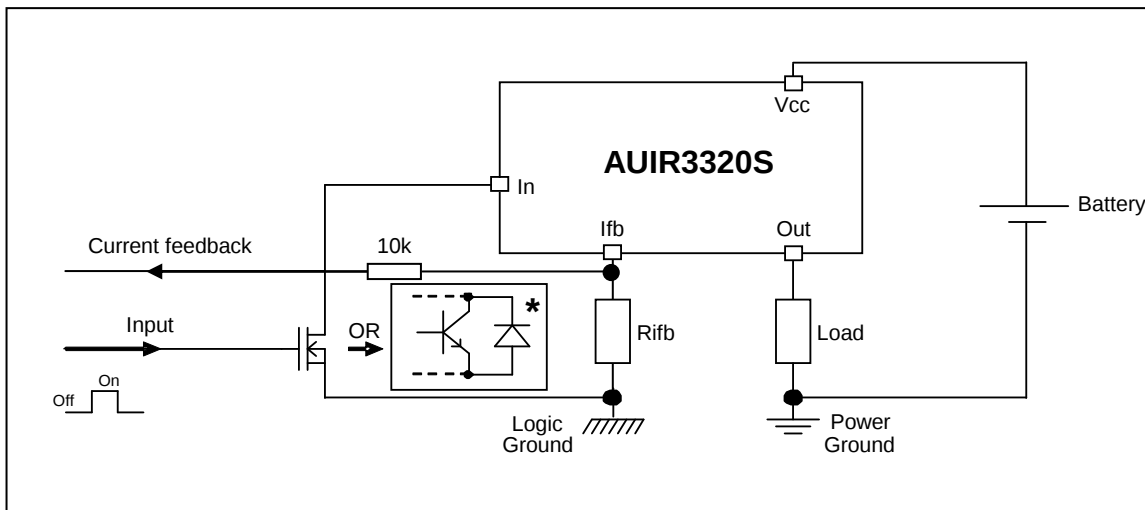


Typical Connection



*The diode between the collector and the emitter is necessary for the reverse battery protection

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Vcc lead. (Tj=-40°..150°C, Vcc=6.26V Tambient=25°C unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vcc-Vin	Maximum Vcc voltage	-16	37	V
Vcc-Vin cont.	Maximum continuous Vcc voltage	-16	26	
Vcc-Vfb	Maximum lfb voltage	-16	33	
Vcc-Vout	Maximum output voltage	-0.3	37	
Ids cont.	Maximum body diode continuous current Rth=60°C/W (1) Tambient=25°C	—	2.8	A
Ids pulsed	Maximum body diode pulsed current (1)	—	100	
Pd	Maximum power dissipation Rth=60°C/W Tambient=25°C	—	2	W
Tj max.	Maximum operating junction temperature	-40	150	°C
	Maximum storage temperature	-55	150	
Min Rfb	Minimum on the resistor on lfb pin	0.3	—	kΩ
lfb max.	Max. lfb current	-50	50	mA

(1) Limited by junction temperature. Pulsed is also limited by wiring

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
Rth1	Thermal resistance junction to ambient D ² -Pak Std footprint	60	—	°C/W
Rth2	Thermal resistance junction to ambient D ² -Pak 1" sqrt. footprint	40	—	
Rth3	Thermal resistance junction to case D ² -Pak	0.7	—	

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
Iout	Continuous output current	—	45 16	A
	Tambient=85°C, Rth=5°C/W, Tj=150°C			
	Tambient=85°C, Rth=40°C/W, Tj=150°C			
Rlfb	Recommended lfb resistor (2)(3)	0.3	3.5	kΩ
Pulse min.	Minimum turn-on pulse width	1	—	ms
Fmax.	Maximum operating frequency	—	200	Hz

(2) If Rlfb is too low, the device can be damaged.

(3) If Rlfb is too high, the device may not switch on.

Protection Characteristics

T_j=-40°..150°C, V_{cc}=6..26V, R_{ifb}=500 to 3.5kΩ. Typical value are given for V_{cc}=14V and T_j=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
V _{ifb-Vin@Isd}	Over-current shutdown threshold	3.8	4.7	5.9	V	
T _{sd}	Over temperature threshold	—	165	—	°C	See fig. 5
OV	Over voltage protection (not latched)	26	29	33	V	
Isdf	Fixed over current shutdown	55	75	105	A	V _{ifb} <V _{ifb-Vin@Isd}
Isd_560	Programmable over current shutdown	34	50	71		R _{ifb} =560Ω
T _{reset}	Time to reset protection	—	50	500	μs	See fig. 5
Min. pulse	Min. pulse width (no WAIT state)	—	900	2000		T _j =25°C
WAIT	WAIT function timer	0.4	1	2	ms	See fig. 4 and 5
R _{ds(on) rev.}	Reverse battery On state resistance, T _j =25°C	—	4	6	mΩ	V _{cc-Vin} =-14V, I _{out} =30A
	T _j =125°C	—	6	9		

Static Electrical Characteristics

T_j=-40°..150°C, V_{cc}=6..26V (unless otherwise specified). Typical value are given for V_{cc}=14V and T_j=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
V _{cc op.}	Operating Voltage range	6	—	26	V	
I _{cc off}	Supply leakage current	—	1.5	5	μA	V _{in} =V _{cc} , V _{cc-Vout} =14V, V _{cc-Vifb} =14V, T _j =25°C
I _{in, on}	On state I _N positive current	1.5	3	6	mA	V _{cc-Vin} =14V, T _j =25°C
V _{ih}	High level Input threshold voltage (4)	—	5.4	6.3	V	
V _{il}	Low level Input threshold voltage (4)	4	4.9	5.8		
V _{hyst}	Input hysteresis V _{ih} -V _{il}	0.2	0.4	1.5		
I _{out}	Drain to source leakage current	—	1.2	5	μA	V _{in} =V _{cc} , V _{cc-Vifb} =0V, V _{cc-Vout} =14V, T _j =25°C
R _{ds(on)}	On state resistance (5) T _j =25°C	—	3.3	4	mΩ	I _{out} =30A, V _{cc-Vin} =14V
	On state resistance (5) T _j =25°C	—	3.5	5.5		I _{out} =17A, V _{cc-Vin} =6V
	On state resistance (5)(6) T _j =150°C	—	5.5	6.5		I _{out} =30A, V _{cc-Vin} =14V
V _{clamp1}	V _{cc} to V _{out} clamp voltage 1	36	39	43	V	I _{out} =50mA
V _{clamp2}	V _{cc} to V _{out} clamp voltage 2	—	40	—		I _{out} =30A, T _j =25°C

(4) Input thresholds are measured directly between the input pin and the tab. Any parasitic resistance in common between the load current path and the input signal path can significantly affect the thresholds.

(5) R_{ds(on)} is measured between the tab and the Out pin, 5mm away from the package.

(6) Guaranteed by design

Switching Electrical Characteristics

V_{cc}=14V, Resistive load=0.5Ω, T_j=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Tdon	Turn on delay time to 10% Vcc	70	170	300	μs	See figure 2
tr1	Rise time to Vcc-Vout=5V	30	100	210		
tr2	Rise time to Vcc-Vout=0.1Vcc	30	125	250		
Eon	Turn on energy	—	15	—	mJ	
Tdoff	Turn off delay time	30	70	140	μs	
Tf	Fall time to Vout=10% of Vcc	20	100	250		
Eoff	Turn off energy	—	9	—	mJ	

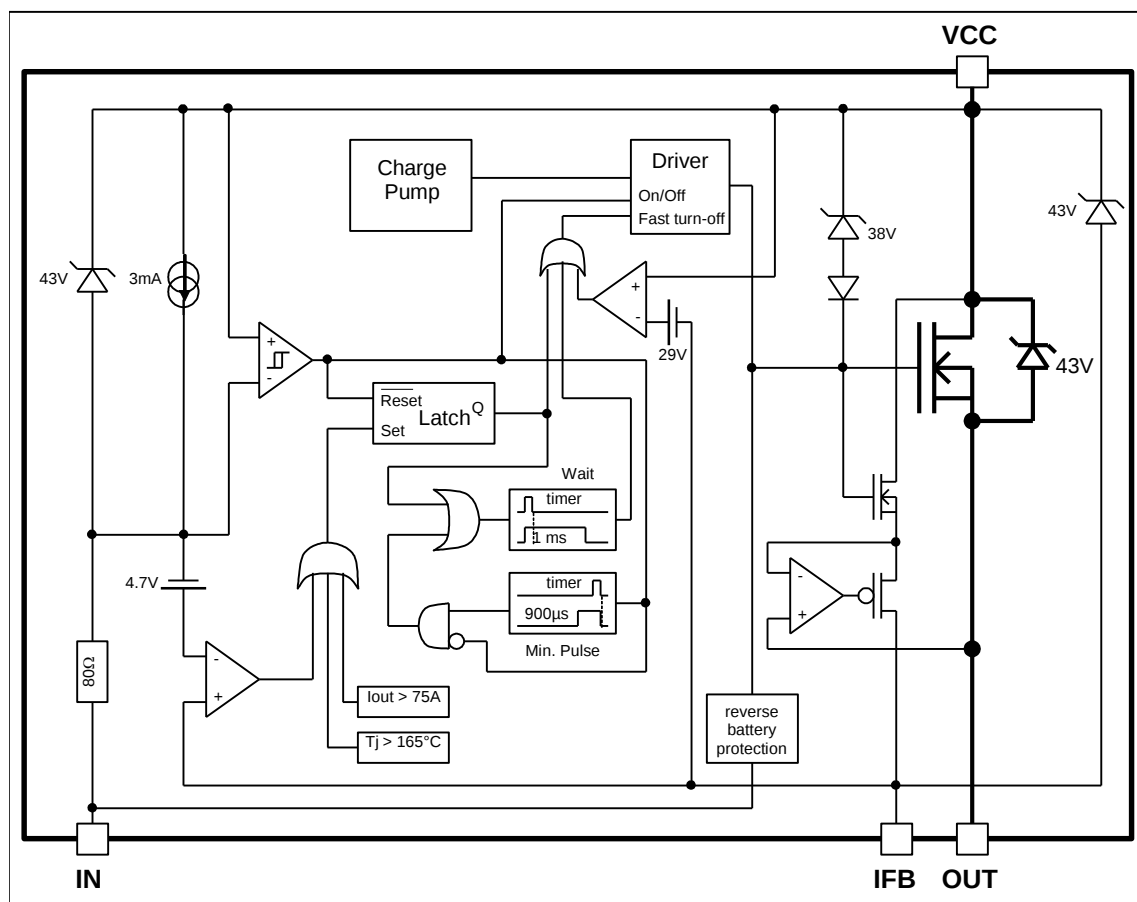
Current Sense Characteristics

Tj=-40°..150°C, Vcc=6..26V (unless otherwise specified). Typical value are given for Vcc=14V and Tj=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Ratio	I Load/I _{fb} current ratio	4900	6000	6600	—	R _{fb} =500Ω, I _{out} =30A
Ratio TC	I Load/I _{fb} variation over temperature (6)	-4	—	+4	%	T _j = -40°C to 150°C
Offset	Load current diagnostic offset	-0.4	0	+0.4	A	I _{out} =2A
Trst	I _{fb} response time (low signal)	—	1	—	μs	90% of the I _{out} step

Functional Block Diagram

All values are typical



Lead Assignments

- 1- In
- 2- I_{fb}
- 3- V_{cc} (tab)
- 4- Out
- 5- Out

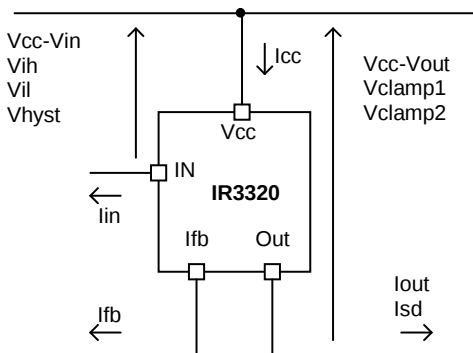
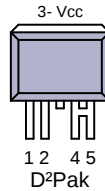


Figure 1 – Voltages and current definitions

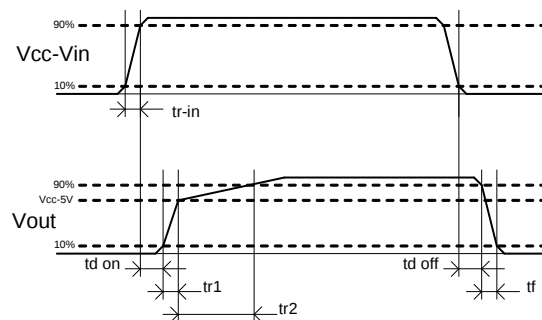


Figure 2 – Switching time definitions

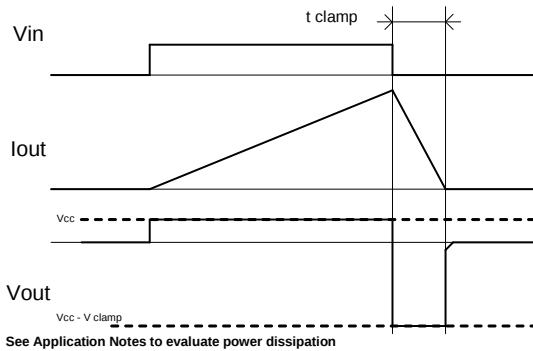


Figure 3 – Active clamp waveforms

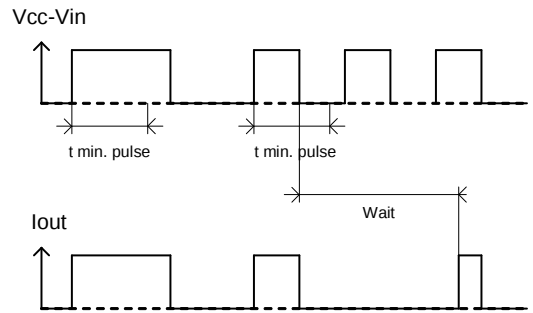


Figure 4 – Min. pulse and Wait function

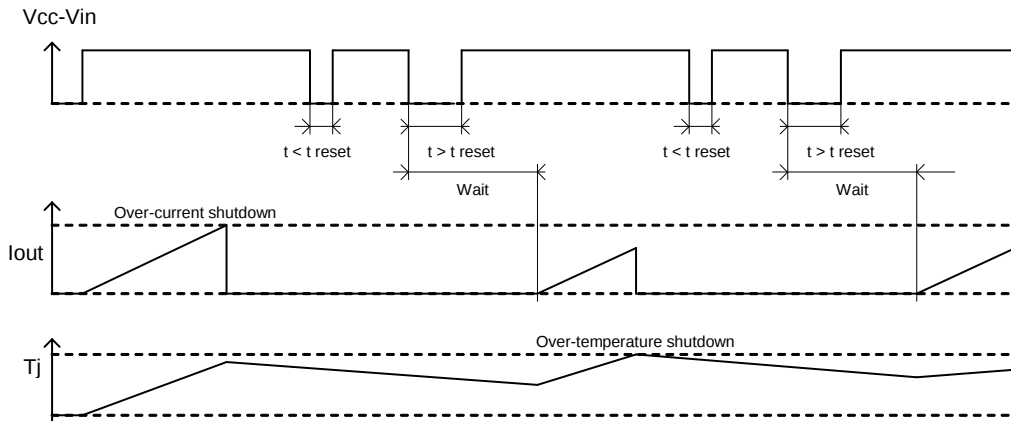
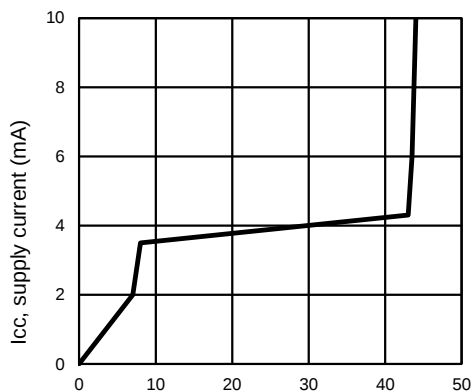


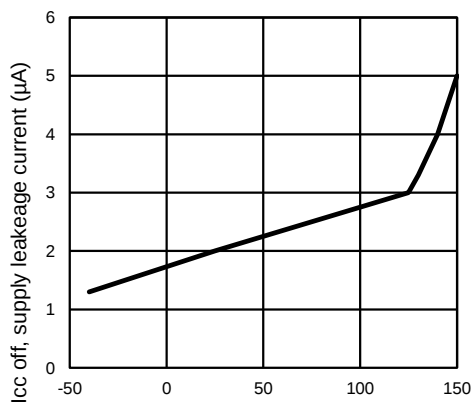
Figure 5 – Protection Timing Diagrams

All curves are typical characteristics. $T_j=25^{\circ}\text{C}$, $R_{\text{ifb}}=500\Omega$, $V_{\text{cc}}=14\text{V}$ (unless otherwise specified).



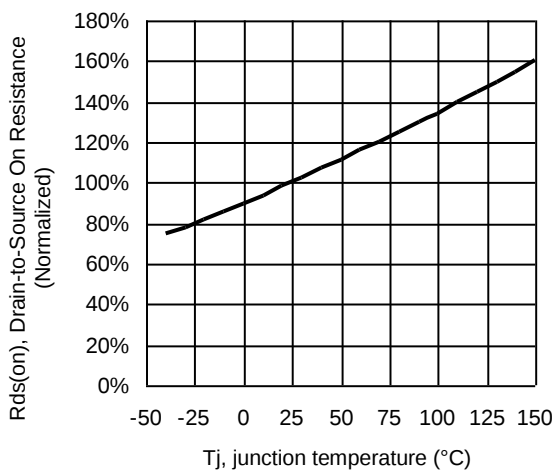
Vcc-Vin, supply voltage (V)

Figure 6 – Icc (mA) Vs Vcc-Vin (V)



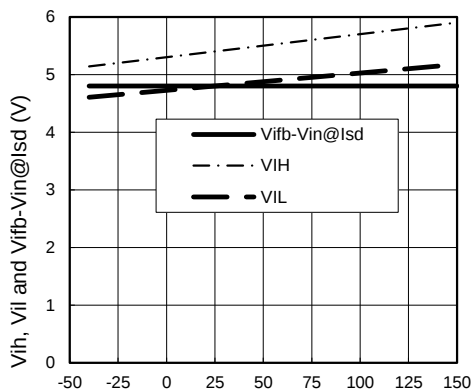
Tj, junction temperature ($^{\circ}\text{C}$)

Figure 7 – Icc off (µA) Vs Tj ($^{\circ}\text{C}$)



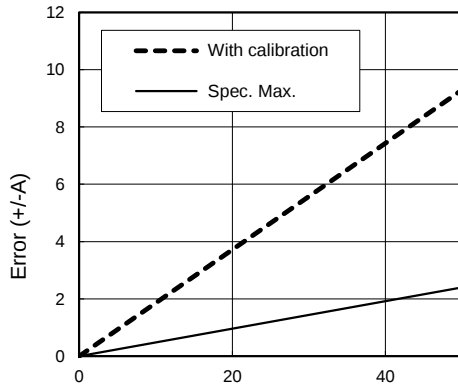
Tj, junction temperature ($^{\circ}\text{C}$)

Figure 8 - Normalized Rds(on) (%) Vs Tj ($^{\circ}\text{C}$)



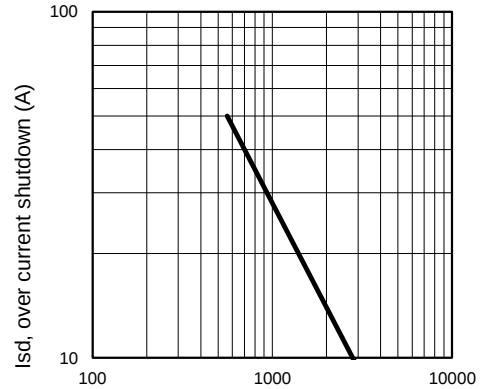
Tj, junction temperature ($^{\circ}\text{C}$)

Figure 9 – Vih, Vil and Vifb-Vin@Isd (V) Vs Tj ($^{\circ}\text{C}$)



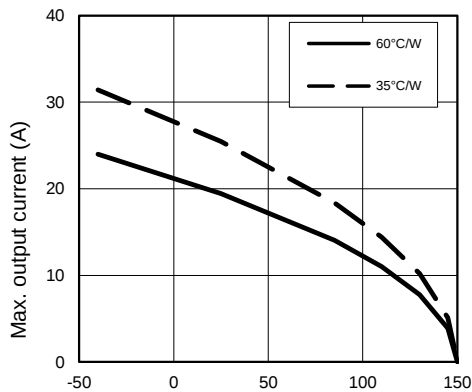
I load, load current (A)

Figure 10 – Error (+/- A) Vs I load (A)



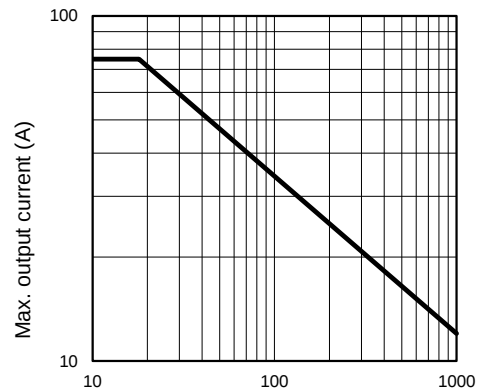
Rifb, feedback resistor (Ω)

Figure 11 – Ids (A) Vs Rifb (Ω)



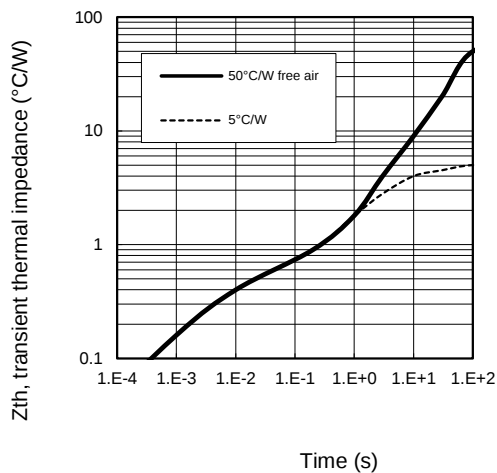
Tamb., ambient temperature (°C)

Figure 12 – Max. iout (A) Vs Tamb. (°C)



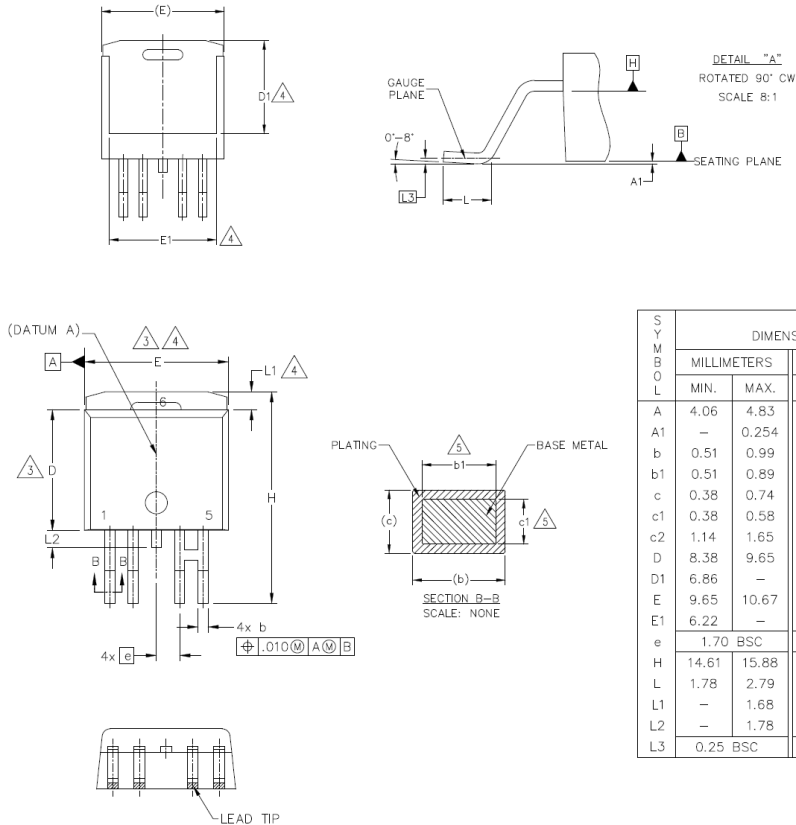
Inductance (μH)

Figure 13 – Max. iout (A) Vs inductance (μH)



**Figure 14 – Transient thermal impedance (°C/W)
Vs time (s)**

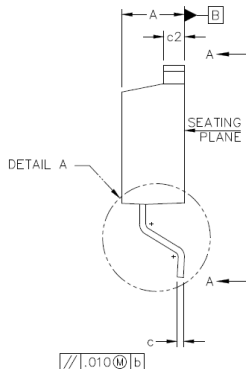
Case Outline - D2PAK - 5 Leads



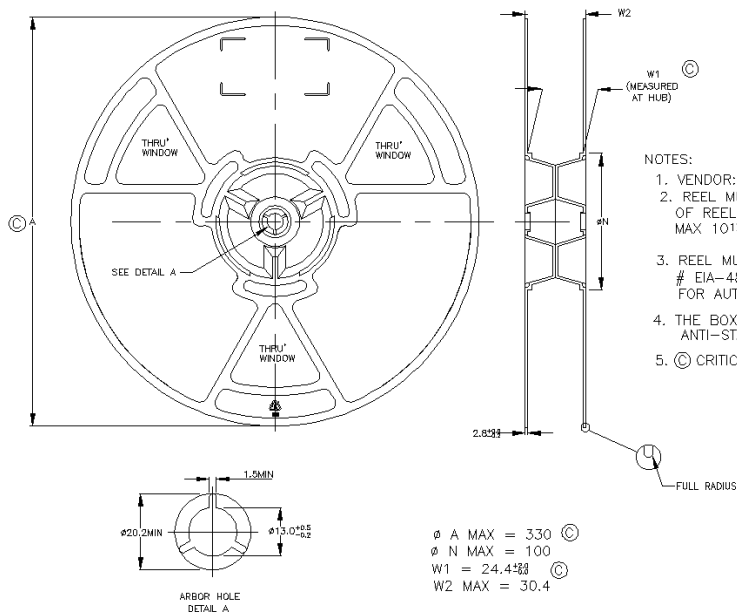
SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	4.06	4.83	.160	.190	4
A1	—	0.254	—	.010	
b	0.51	0.99	.020	.039	
b1	0.51	0.89	.020	.035	
c	0.38	0.74	.015	.029	4
c1	0.38	0.58	.015	.023	
c2	1.14	1.65	.045	.065	
D	8.38	9.65	.330	.380	
D1	6.86	—	.270	—	3
E	9.65	10.67	.380	.420	
E1	6.22	—	.245	—	
e	1.70 BSC		.067 BSC		
H	14.61	15.88	.575	.625	
L	1.78	2.79	.070	.110	
L1	—	1.68	—	.066	
L2	—	1.78	—	.070	
L3	0.25 BSC		.010 BSC		

NOTES:

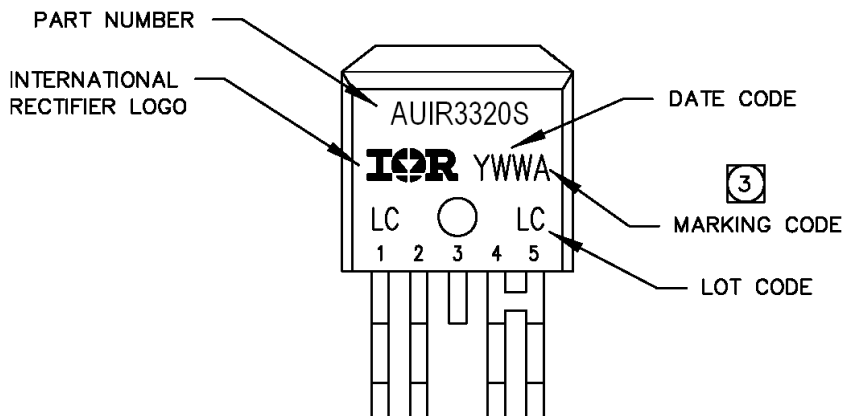
- DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M-1994
- DIMENSIONS ARE SHOWN IN MILLIMETERS [INCHES].
- DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.127 [0.005"] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY AT DATUM H.
- THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSION E, L1, D1 & E1.
- DIMENSION b1 AND c1 APPLY TO BASE METAL ONLY.
- DATUM A & B TO BE DETERMINED AT DATUM PLANE H.
- CONTROLLING DIMENSION: INCH.
- OUTLINE CONFORMS TO JEDEC OUTLINE TO-263BA.



Tape & Reel - D2PAK – 5 leads



Part Marking Information



Qualification Information[†]

Qualification Level		Automotive (per AEC-Q100)	
		Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
Moisture Sensitivity Level		D2PAK-5L	MSL1, 260°C (per IPC/JEDEC J-STD-020)
ESD	Machine Model	Class M3 (400V) (per AEC-Q100-003)	
	Human Body Model	Class H2 (4,000 V) (per AEC-Q100-002)	
	Charged Device Model	Class C4 (1000 V) (per AEC-Q100-011)	
IC Latch-Up Test		Class II, Level A (per AEC-Q100-004)	
RoHS Compliant		Yes	

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Revision History

Revision	Date	Notes/Changes
A7	June, 4 th 2012	Initial release
A8	August, 13rd 2012	-Update switching limits -Update Iratio max limit
A9	August, 30 th 2012	Update Tj max.
Rev1.0	July, 11 th 2017	- Page 'Case Outline - D2PAK - 5 Leads' updated - Page 'Ordering information' updated - Page 14 'Notice' updated