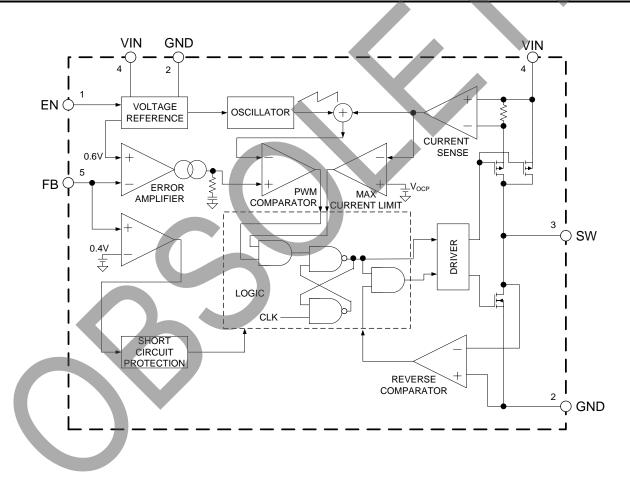


Pin Descriptions

Pin Number	Pin Name	Function		
1	EN	Control input pin. Forcing this pin above 1.5V enables the IC. Forcing this pin below 0.4V shuts down the IC. When the IC is in shutdown mode, all functions are disabled to decrease the supply current below $1\mu A$		
2	GND	Ground pin		
3	SW	Power switch output pin. Inductor connection to drain of the internal PFET and NFET switches		
4	VIN	Supply input pin. Bypass to GND with a 4.7µF or greater ceramic capacitor		
5	FB	This is the feedback pin of the device. Connect this pin directly to the output if the fixed output voltage version is used. For the adjustable version, an external resistor divider is connected to this pin		

Functional Block Diagram





Absolute Maximum Ratings (Note 1)

Symbol	Parameter	Rating	Unit
V _{IN}	Input Voltage	-0.3 to 6.0	V
V _{FB}	Feedback Voltage	-0.3 to V _{IN} +0.3	V
V _{EN}	EN Pin Voltage	-0.3 to V _{IN} +0.3	V
V _{SW}	SW Pin Voltage	-0.3 to V _{IN} +0.3	V
θ _{JA}	Thermal Resistance	265	°C/W
TJ	Operating Junction Temperature	+150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
T _{LEAD}	Lead Temperature (Soldering, 10sec)	+260	°C
-	ESD(Machine Model)	200	V
_	ESD(Human Body Model)	2000	V

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Мах	Unit	
V _{IN}	Input Voltage	2.5	5.5	V	
T _A	Operating Ambient Temperature	-40	+85	°C	

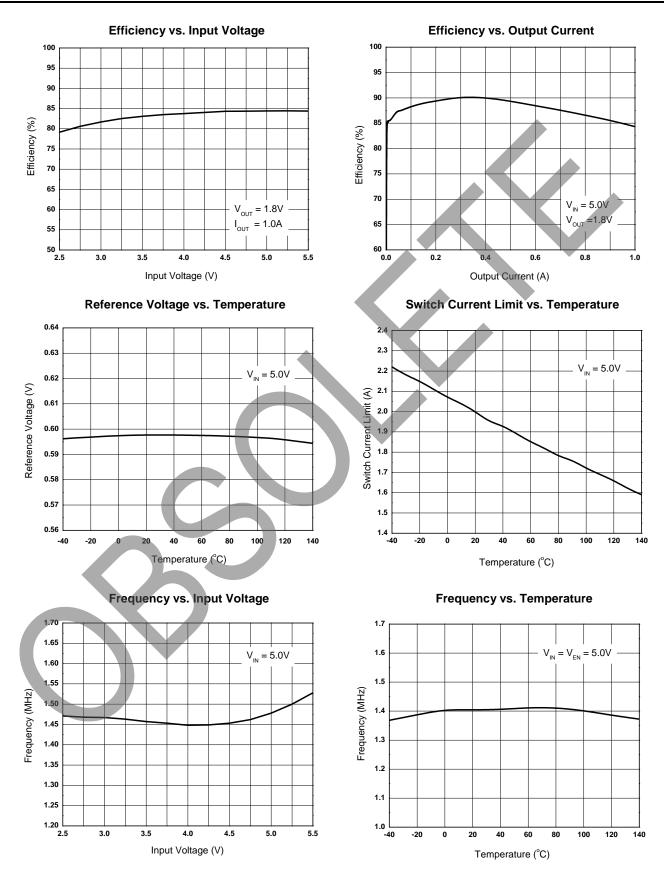


Electrical Characteristics (V_{IN} = 5V, T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
VIN	Input Voltage	-	2.5	_	5.5	V	
lq	Quiescent Current	V _{FB} = 0.65V	-	62	100	μA	
I _{STBY}	Shutdown Supply Current	V _{EN} = GND	_	0.1	1	μA	
V _{REF}	Reference Voltage	For Adjustable Output Voltage	0.588	0.6	0.612	V	
I _{FB_H}	Feedback Bias Current	V _{FB} = 1V	-0.1	-	0.1	μA	
I _{FB_L}		V _{FB} = 0V	-0.1	-	0.1		
Rds(on)_ p	PMOSFET R _{ON}	I _{SW} = 200mA	-	0.25	-	Ω	
R _{DS(ON)} _N	NMOSFET R _{ON}	I _{SW} = -200mA	-	0.2	-	Ω	
I _{LIM}	Switch Current Limit	V _{FB} = 0.55V	1.3	1.8	-	А	
V _H	EN Pin Threshold	-	1.5		_	V	
VL		-	-	_	0.4	V	
V _{UVLO}	UVLO Threshold	V _{IN} Rising	_	2.3	_	v	
V _{HYS}	UVLO Hysteresis	-	-	0.2	_	V	
fosc	Oscillator Frequency	-	1.12	1.40	1.68	MHz	
D _{MAX}	Max. Duty Cycle	-	100	_	_	0/	
D _{MIN}	Min. Duty Cycle	-	_	_	0	- %	
I _{SW_H}		V _{SW} = 0V	_	0.1	_		
I _{SW_L}	SW Leakage Current	V _{SW} = 5V	_	0.1	_	μA	
t _{SS}	Soft-Start Time	-	_	1	_	ms	
T _{OTSD}	Thermal Shutdown	-	_	+160	_	°C	
T _{HYS}	Thermal Shutdown Hysteresis	-	_	+20	_	٥C	



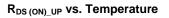
Performance Characteristics (VIN = 5V, TA = +25°C, unless otherwise specified.)



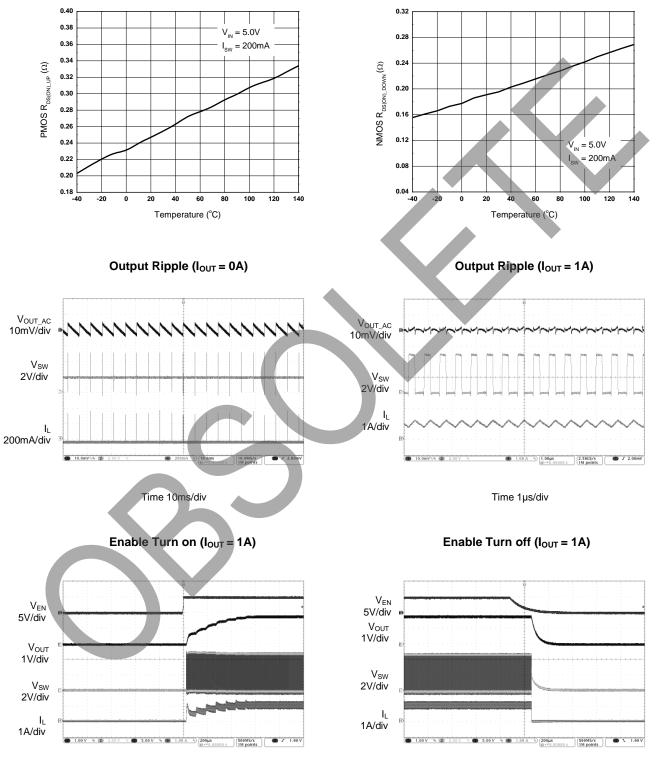
AP3417A Document number: DS41418 Rev. 3 - 4 Downloaded from Arrow.com.



Performance Characteristics (Cont. VIN = 5V, TA = +25°C, unless otherwise specified.)



R_{DS (ON)_DOWN} vs. Temperature



Time 200µs/div

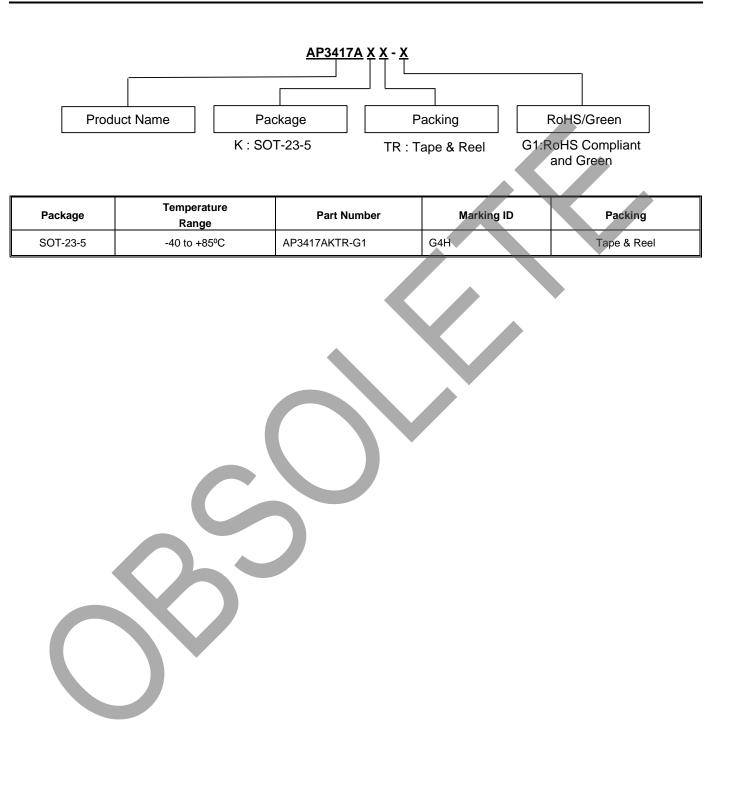


Performance Characteristics (Cont. VIN = 5V, TA = +25°C, unless otherwise specified.)





Ordering Information

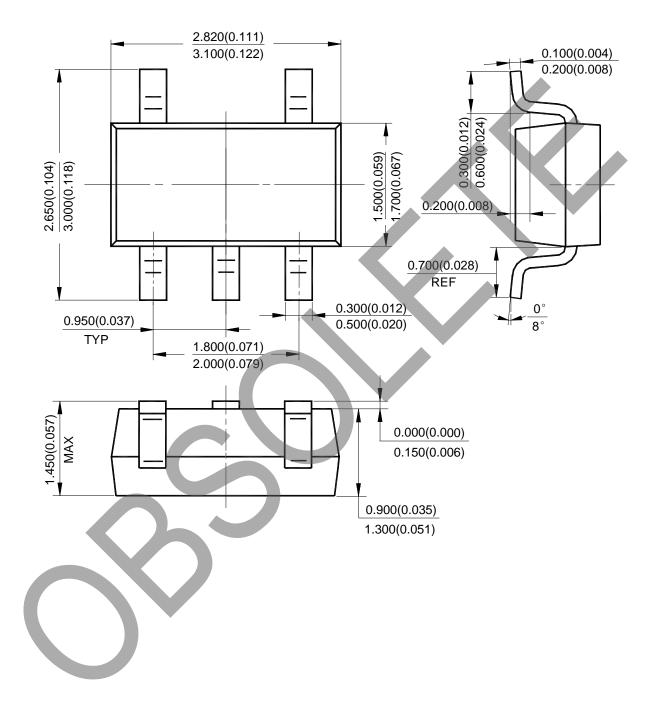




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Package Outline Dimensions (All dimensions in mm(inch).)

(1) Package Type: SOT-23-5

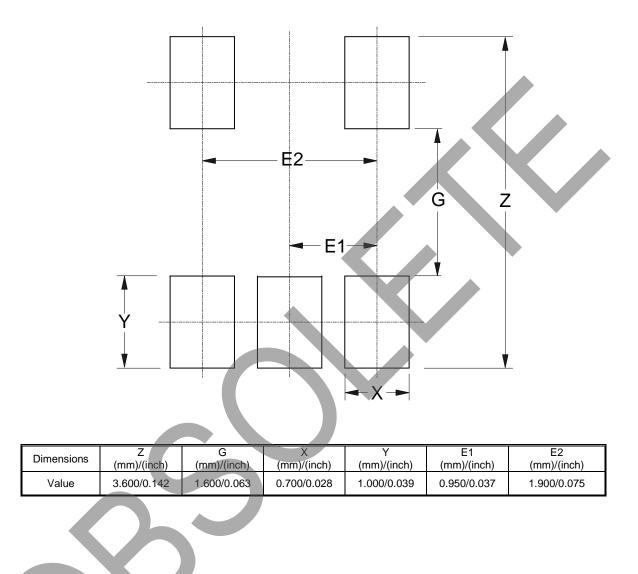




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Suggested Pad Layout

(1) Package Type: SOT-23-5





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