

TABLE OF CONTENTS

|                                |   |                                                    |    |
|--------------------------------|---|----------------------------------------------------|----|
| Features .....                 | 1 | Absolute Maximum Ratings .....                     | 6  |
| Applications.....              | 1 | ESD Caution.....                                   | 6  |
| Functional Block Diagram ..... | 1 | Truth Table for Switches .....                     | 6  |
| General Description .....      | 1 | Pin Configurations and Function Descriptions ..... | 7  |
| Product Highlights .....       | 1 | Terminology .....                                  | 8  |
| Revision History .....         | 2 | Typical Performance Characteristics .....          | 9  |
| Specifications.....            | 3 | Test Circuits.....                                 | 12 |
| Dual Supply .....              | 3 | Outline Dimensions .....                           | 14 |
| Single Supply .....            | 5 | Ordering Guide .....                               | 14 |

REVISION HISTORY

|                                        |    |
|----------------------------------------|----|
| 3/16—Rev. 0 to Rev. A                  |    |
| Changes to Figure 2 and Figure 3 ..... | 7  |
| Updated Outline Dimensions .....       | 14 |
| Changes to Ordering Guide .....        | 14 |

9/05—Revision 0: Initial Version

## SPECIFICATIONS

## DUAL SUPPLY

$V_{DD} = 15\text{ V} \pm 10\%$ ,  $V_{SS} = -15\text{ V} \pm 10\%$ , GND = 0 V, unless otherwise noted.

Table 1.

| Parameters                                               | Y Version <sup>1</sup> |                |                                    | Unit    | Test Conditions/Comments <sup>1</sup>                                                                                                                               |
|----------------------------------------------------------|------------------------|----------------|------------------------------------|---------|---------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|                                                          | 25°C                   | −40°C to +85°C | −40°C to +125°C                    |         |                                                                                                                                                                     |
| ANALOG SWITCH                                            |                        |                |                                    |         |                                                                                                                                                                     |
| Analog Signal Range                                      |                        |                | V <sub>DD</sub> to V <sub>SS</sub> | V       | V <sub>S</sub> = ±10 V, I <sub>S</sub> = −1 mA; Figure 20<br>V <sub>DD</sub> = +13.5 V, V <sub>SS</sub> = −13.5 V<br>V <sub>S</sub> = ±10 V, I <sub>S</sub> = −1 mA |
| On Resistance (R <sub>ON</sub> )                         | 120                    |                |                                    | Ω typ   |                                                                                                                                                                     |
|                                                          | 190                    | 230            | 260                                | Ω max   |                                                                                                                                                                     |
| On Resistance Match Between Channels (ΔR <sub>ON</sub> ) | 3.5                    |                |                                    | Ω typ   |                                                                                                                                                                     |
|                                                          | 6                      | 10             | 12                                 | Ω max   | V <sub>S</sub> = −5 V, 0 V, +5 V; I <sub>S</sub> = −1 mA                                                                                                            |
| On Resistance Flatness (R <sub>FLAT(ON)</sub> )          | 20                     |                |                                    | Ω typ   |                                                                                                                                                                     |
|                                                          | 57                     | 72             | 79                                 | Ω max   |                                                                                                                                                                     |
| LEAKAGE CURRENTS                                         |                        |                |                                    |         |                                                                                                                                                                     |
| Source Off Leakage, I <sub>S</sub> (Off)                 | ±0.02                  |                |                                    | nA typ  | V <sub>DD</sub> = +16.5 V, V <sub>SS</sub> = −16.5 V<br>V <sub>S</sub> = ±10 V, V <sub>S</sub> = ∓10 V; Figure 21                                                   |
|                                                          | ±0.1                   | ±0.6           | ±1                                 | nA max  | V <sub>S</sub> = ±10 V, V <sub>S</sub> = ∓10 V; Figure 21                                                                                                           |
| Drain Off Leakage, I <sub>D</sub> (Off)                  | ±0.02                  |                |                                    | nA typ  |                                                                                                                                                                     |
|                                                          | ±0.1                   | ±0.6           | ±1                                 | nA max  |                                                                                                                                                                     |
| Channel On Leakage, I <sub>D</sub> , I <sub>S</sub> (On) | ±0.02                  |                |                                    | nA typ  | V <sub>S</sub> = V <sub>D</sub> = ±10 V; Figure 22                                                                                                                  |
|                                                          | ±0.2                   | ±0.6           | ±1                                 | nA max  |                                                                                                                                                                     |
| DIGITAL INPUTS                                           |                        |                |                                    |         |                                                                                                                                                                     |
| Input High Voltage, V <sub>INH</sub>                     |                        |                | 2.0                                | V min   | V <sub>IN</sub> = V <sub>INL</sub> or V <sub>INH</sub>                                                                                                              |
| Input Low Voltage, V <sub>INL</sub>                      |                        |                | 0.8                                | V max   |                                                                                                                                                                     |
| Input Current, I <sub>INL</sub> or I <sub>INH</sub>      | 0.005                  |                |                                    | μA typ  |                                                                                                                                                                     |
|                                                          |                        |                | ±0.1                               | μA max  |                                                                                                                                                                     |
| Digital Input Capacitance, C <sub>IN</sub>               | 2                      |                |                                    | pF typ  |                                                                                                                                                                     |
| DYNAMIC CHARACTERISTICS <sup>2</sup>                     |                        |                |                                    |         |                                                                                                                                                                     |
| Transition Time, t <sub>TRANS AOFF BON</sub>             | 125                    |                |                                    | ns typ  | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF                                                                                                                      |
|                                                          | 150                    |                | 200                                | ns max  | V <sub>S</sub> = 10 V; Figure 23                                                                                                                                    |
| Transition Time, t <sub>TRANS BOFF AON</sub>             | 70                     |                |                                    | ns typ  | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF                                                                                                                      |
|                                                          | 90                     |                | 115                                | ns max  | V <sub>S</sub> = 10 V; Figure 23                                                                                                                                    |
| Break-Before-Make Time Delay, t <sub>D</sub>             | 25                     |                |                                    | ns typ  | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF                                                                                                                      |
|                                                          |                        |                | 10                                 | ns min  | V <sub>S1</sub> = V <sub>S2</sub> = 10 V; Figure 24                                                                                                                 |
| Charge Injection                                         | −1                     |                |                                    | pC typ  | V <sub>S</sub> = 0 V, R <sub>S</sub> = 0 Ω, C <sub>L</sub> = 1 nF; Figure 25                                                                                        |
| Off Isolation                                            | 80                     |                |                                    | dB typ  | R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 1 MHz; Figure 26                                                                                                  |
| Channel-to-Channel Crosstalk                             | 85                     |                |                                    | dB typ  | R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 1 MHz; Figure 27                                                                                                  |
| Total Harmonic Distortion + Noise                        | 0.15                   |                |                                    | % typ   | R <sub>L</sub> = 10 kΩ, 5 V rms, f = 20 Hz to 20 kHz                                                                                                                |
| −3 dB Bandwidth                                          | 1000                   |                |                                    | MHz typ | R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF; Figure 28                                                                                                             |
| C <sub>S</sub> (Off)                                     | 1.3                    |                |                                    | pF typ  | f = 1 MHz; V <sub>S</sub> = 0 V                                                                                                                                     |
|                                                          | 1.6                    |                |                                    | pF max  | f = 1 MHz; V <sub>S</sub> = 0 V                                                                                                                                     |
| C <sub>D</sub> , C <sub>S</sub> (On)                     | 3.5                    |                |                                    | pF typ  | f = 1 MHz; V <sub>S</sub> = 0 V                                                                                                                                     |
|                                                          | 4.3                    |                |                                    | pF max  | f = 1 MHz; V <sub>S</sub> = 0 V                                                                                                                                     |

| Parameters         | Y Version <sup>1</sup> |                |                 | Unit                                   | Test Conditions/Comments <sup>1</sup>                 |
|--------------------|------------------------|----------------|-----------------|----------------------------------------|-------------------------------------------------------|
|                    | 25°C                   | –40°C to +85°C | –40°C to +125°C |                                        |                                                       |
| POWER REQUIREMENTS |                        |                |                 |                                        | $V_{DD} = +16.5\text{ V}$ , $V_{SS} = -16.5\text{ V}$ |
| $I_{DD}$           | 0.001                  |                | 1.0             | $\mu\text{A typ}$<br>$\mu\text{A max}$ | Digital inputs = 0 V or $V_{DD}$                      |
| $I_{DD}$           | 170                    |                | 230             | $\mu\text{A typ}$<br>$\mu\text{A max}$ | Digital inputs = 5 V                                  |
| $I_{SS}$           | 0.001                  |                | 1.0             | $\mu\text{A typ}$<br>$\mu\text{A max}$ | Digital inputs = 0 V or $V_{DD}$                      |
| $I_{SS}$           | 0.001                  |                | 1.0             | $\mu\text{A typ}$<br>$\mu\text{A max}$ | Digital inputs = 5 V                                  |

<sup>1</sup> Temperature range for Y version is –40°C to +125°C.

<sup>2</sup> Guaranteed by design; not subject to production test.

**SINGLE SUPPLY**

$V_{DD} = 12\text{ V} \pm 10\%$ ,  $V_{SS} = 0\text{ V}$ ,  $GND = 0\text{ V}$ , unless otherwise noted.

**Table 2.**

| Parameters                                                         | Y Version <sup>1</sup> |                |                        | Unit    | Test Conditions/Comments                                                     |
|--------------------------------------------------------------------|------------------------|----------------|------------------------|---------|------------------------------------------------------------------------------|
|                                                                    | 25°C                   | –40°C to +85°C | –40°C to +125°C        |         |                                                                              |
| ANALOG SWITCH                                                      |                        |                |                        |         |                                                                              |
| Analog Signal Range                                                |                        |                | 0 V to V <sub>DD</sub> | V       |                                                                              |
| On Resistance (R <sub>ON</sub> )                                   | 300                    |                |                        | Ω typ   | V <sub>S</sub> = 0 V to 10 V, I <sub>S</sub> = –1 mA; Figure 20              |
|                                                                    | 475                    | 567            | 625                    | Ω max   | V <sub>DD</sub> = 10.8 V, V <sub>SS</sub> = 0 V                              |
| On Resistance Match Between Channels (ΔR <sub>ON</sub> )           | 4.5                    |                |                        | Ω typ   | V <sub>S</sub> = 0 V to 10 V, I <sub>S</sub> = –1 mA                         |
|                                                                    | 16                     | 26             | 27                     | Ω max   |                                                                              |
| On Resistance Flatness (R <sub>FLAT(ON)</sub> )                    | 60                     |                |                        | Ω typ   | V <sub>S</sub> = 3 V, 6 V, 9 V, I <sub>S</sub> = –1 mA                       |
| LEAKAGE CURRENTS                                                   |                        |                |                        |         |                                                                              |
| Source Off Leakage, I <sub>S</sub> (Off)                           | ±0.02                  |                |                        | nA typ  | V <sub>DD</sub> = 13.2 V                                                     |
|                                                                    | ±0.1                   | ±0.6           | ±1                     | nA max  | V <sub>S</sub> = 1 V/10 V, V <sub>D</sub> = 10 V/1 V; Figure 21              |
| Drain Off Leakage, I <sub>D</sub> (Off)                            | ±0.02                  |                |                        | nA typ  | V <sub>S</sub> = 1 V/10 V, V <sub>D</sub> = 10 V/1 V; Figure 21              |
|                                                                    | ±0.1                   | ±0.6           | ±1                     | nA max  |                                                                              |
| Channel On Leakage, I <sub>D</sub> , I <sub>S</sub> (On)           | ±0.02                  |                |                        | nA typ  | V <sub>S</sub> = V <sub>D</sub> = 1 V or 10 V, Figure 22                     |
|                                                                    | ±0.2                   | ±0.6           | ±1                     | nA max  |                                                                              |
| DIGITAL INPUTS                                                     |                        |                |                        |         |                                                                              |
| Input High Voltage, V <sub>INH</sub>                               |                        |                | 2.0                    | V min   |                                                                              |
| Input Low Voltage, V <sub>INL</sub>                                |                        |                | 0.8                    | V max   |                                                                              |
| Input Current, I <sub>INL</sub> or I <sub>INH</sub>                | 0.001                  |                |                        | μA typ  | V <sub>IN</sub> = V <sub>INL</sub> or V <sub>INH</sub>                       |
|                                                                    |                        |                | ±0.1                   | μA max  |                                                                              |
| Digital Input Capacitance, C <sub>IN</sub>                         | 3                      |                |                        | pF typ  |                                                                              |
| DYNAMIC CHARACTERISTICS <sup>2</sup>                               |                        |                |                        |         |                                                                              |
| Transition Time, t <sub>TRANS B<sub>OFF</sub> A<sub>ON</sub></sub> | 105                    |                |                        | ns typ  | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF                               |
|                                                                    | 140                    |                | 175                    | ns max  | V <sub>S</sub> = 8 V; Figure 23                                              |
| Transition Time, t <sub>TRANS A<sub>OFF</sub> B<sub>ON</sub></sub> | 155                    |                |                        | ns typ  | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF                               |
|                                                                    | 190                    |                | 255                    | ns max  | V <sub>S</sub> = 8 V; Figure 23                                              |
| Break-Before-Make Time Delay, t <sub>D</sub>                       | 50                     |                |                        | ns typ  | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF                               |
|                                                                    |                        |                | 10                     | ns min  | V <sub>S1</sub> = V <sub>S2</sub> = 8 V; Figure 24                           |
| Charge Injection                                                   | –0.8                   |                |                        | pC typ  | V <sub>S</sub> = 6 V, R <sub>S</sub> = 0 Ω, C <sub>L</sub> = 1 nF; Figure 25 |
| Off Isolation                                                      | 75                     |                |                        | dB typ  | R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 1 MHz; Figure 26;          |
| Channel-to-Channel Crosstalk                                       | 85                     |                |                        | dB typ  | R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 1 MHz; Figure 27           |
| –3 dB Bandwidth                                                    | 800                    |                |                        | MHz typ | R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF; Figure 28                      |
| C <sub>S</sub> (Off)                                               | 1.6                    |                |                        | pF typ  | f = 1 MHz; V <sub>S</sub> = 6 V                                              |
|                                                                    | 1.9                    |                |                        | pF max  | f = 1 MHz; V <sub>S</sub> = 6 V                                              |
| C <sub>D</sub> , C <sub>S</sub> (On)                               | 4                      |                |                        | pF typ  | f = 1 MHz; V <sub>S</sub> = 6 V                                              |
|                                                                    | 4.9                    |                |                        | pF max  | f = 1 MHz; V <sub>S</sub> = 6 V                                              |
| POWER REQUIREMENTS                                                 |                        |                |                        |         |                                                                              |
| I <sub>DD</sub>                                                    | 0.001                  |                |                        | μA typ  | V <sub>DD</sub> = 13.2 V                                                     |
|                                                                    |                        |                | 1.0                    | μA max  | Digital inputs = 0 V or V <sub>DD</sub>                                      |
| I <sub>DD</sub>                                                    | 170                    |                |                        | μA typ  | Digital inputs = 5 V                                                         |
|                                                                    |                        |                | 230                    | μA max  |                                                                              |

<sup>1</sup> Temperature range for Y version is –40°C to +125°C.

<sup>2</sup> Guaranteed by design; not subject to production test.

## ABSOLUTE MAXIMUM RATINGS

$T_A = 25^\circ\text{C}$ , unless otherwise noted.

Table 3.

| Parameter                                             | Rating                                                                                 |
|-------------------------------------------------------|----------------------------------------------------------------------------------------|
| $V_{DD}$ to $V_{SS}$                                  | 35 V                                                                                   |
| $V_{DD}$ to GND                                       | −0.3 V to +25 V                                                                        |
| $V_{SS}$ to GND                                       | +0.3 V to −25 V                                                                        |
| Analog Inputs <sup>1</sup>                            | $V_{SS} - 0.3\text{ V}$ to $V_{DD} + 0.3\text{ V}$ or<br>30 mA, whichever occurs first |
| Digital Inputs <sup>1</sup>                           | GND − 0.3 V to $V_{DD} + 0.3\text{ V}$ or<br>30 mA, whichever occurs first             |
| Peak Current, S or D                                  | 100 mA (pulsed at 1 ms,<br>10% duty cycle max)                                         |
| Continuous Current per<br>Channel, S or D             | 25 mA                                                                                  |
| Operating Temperature Range<br>Automotive (Y Version) | −40°C to +125°C                                                                        |
| Storage Temperature Range                             | −65°C to +150°C                                                                        |
| Junction Temperature                                  | 150°C                                                                                  |
| 16-Lead TSSOP, $\theta_{JA}$ Thermal<br>Impedance     | 112°C/W                                                                                |
| 12-Lead LFCSP, $\theta_{JA}$ Thermal<br>Impedance     | 80°C/W                                                                                 |
| Reflow Soldering Peak<br>Temperature, Pb Free         | 260°C                                                                                  |

<sup>1</sup> Over voltages at IN, S, or D are clamped by internal diodes. Current must be limited to the maximum ratings given.

Stresses at or above those listed under Absolute Maximum Ratings may cause permanent damage to the product. This is a stress rating only; functional operation of the product at these or any other conditions above those indicated in the operational section of this specification is not implied. Operation beyond the maximum operating conditions for extended periods may affect product reliability.

### ESD CAUTION



**ESD (electrostatic discharge) sensitive device.** Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

### TRUTH TABLE FOR SWITCHES

Table 4.

| IN | Switch A | Switch B |
|----|----------|----------|
| 0  | Off      | On       |
| 1  | On       | Off      |

## PIN CONFIGURATIONS AND FUNCTION DESCRIPTIONS

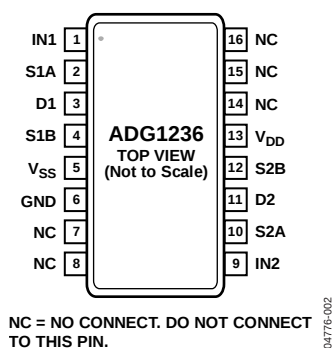


Figure 2. TSSOP Pin Configuration

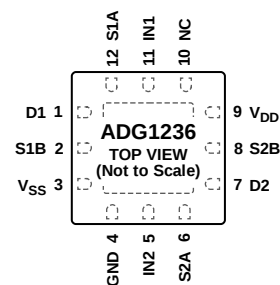


Figure 3. LFCSP Pin Configuration

Table 5. Pin Function Descriptions

| Pin No.        |       | Mnemonic | Description                                 |
|----------------|-------|----------|---------------------------------------------|
| TSSOP          | LFCSP |          |                                             |
| 1              | 11    | IN1      | Logic Control Input.                        |
| 2              | 12    | S1A      | Source Terminal. Can be an input or output. |
| 3              | 1     | D1       | Drain Terminal. Can be an input or output.  |
| 4              | 2     | S1B      | Source Terminal. Can be an input or output. |
| 5              | 3     | $V_{SS}$ | Most Negative Power Supply Potential.       |
| 6              | 4     | GND      | Ground (0 V) Reference.                     |
| 7, 8, 14 to 16 | 10    | NC       | No Connect.                                 |
| 9              | 5     | IN2      | Logic Control Input.                        |
| 10             | 6     | S2A      | Source Terminal. Can be an input or output. |
| 11             | 7     | D2       | Drain Terminal. Can be an input or output.  |
| 12             | 8     | S2B      | Source Terminal. Can be an input or output. |
| 13             | 9     | $V_{DD}$ | Most Positive Power Supply Potential.       |

## TERMINOLOGY

**I<sub>DD</sub>**

The positive supply current.

**I<sub>SS</sub>**

The negative supply current.

**V<sub>D</sub> (V<sub>S</sub>)**

The analog voltage on Terminals D and S.

**R<sub>ON</sub>**

The ohmic resistance between D and S.

**R<sub>FLAT(ON)</sub>**

Flatness is defined as the difference between the maximum and minimum value of on resistance as measured over the specified analog signal range.

**I<sub>S</sub> (Off)**

The source leakage current with the switch off.

**I<sub>D</sub> (Off)**

The drain leakage current with the switch off.

**I<sub>D</sub>, I<sub>S</sub> (On)**

The channel leakage current with the switch on.

**V<sub>INL</sub>**

The maximum input voltage for Logic 0.

**V<sub>INH</sub>**

The minimum input voltage for Logic 1.

**I<sub>INL</sub> (I<sub>INH</sub>)**

The input current of the digital input.

**C<sub>S</sub> (Off)**

The off switch source capacitance, measured with reference to ground.

**C<sub>D</sub> (Off)**

The off switch drain capacitance, measured with reference to ground.

**C<sub>D</sub>, C<sub>S</sub> (On)**

The on switch capacitance, measured with reference to ground.

**C<sub>IN</sub>**

The digital input capacitance.

**t<sub>TRANS</sub>**

The delay time between the 50% and 90% points of the digital input and switch on condition when switching from one address state to another.

**Charge Injection**

A measure of the glitch impulse transferred from the digital input to the analog output during switching.

**Off Isolation**

A measure of unwanted signal coupling through an off switch.

**Crosstalk**

A measure of unwanted signal that is coupled through from one channel to another as a result of parasitic capacitance.

**Bandwidth**

The frequency at which the output is attenuated by 3 dB.

**On Response**

The frequency response of the on switch.

**Insertion Loss**

The loss due to the on resistance of the switch.

**THD + N**

The ratio of the harmonic amplitude plus noise of the signal to the fundamental.

## TYPICAL PERFORMANCE CHARACTERISTICS

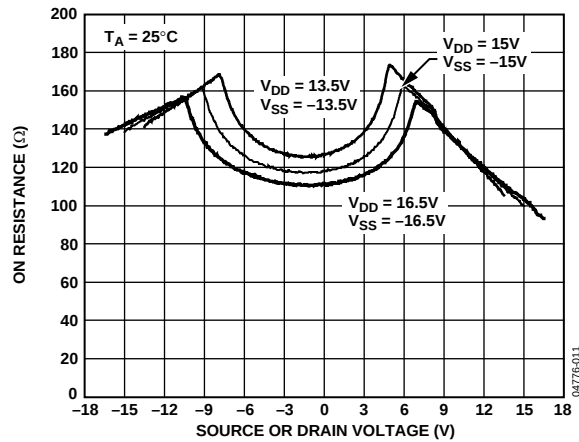
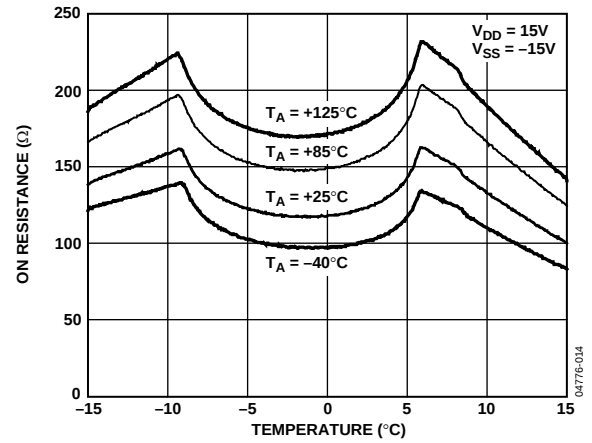
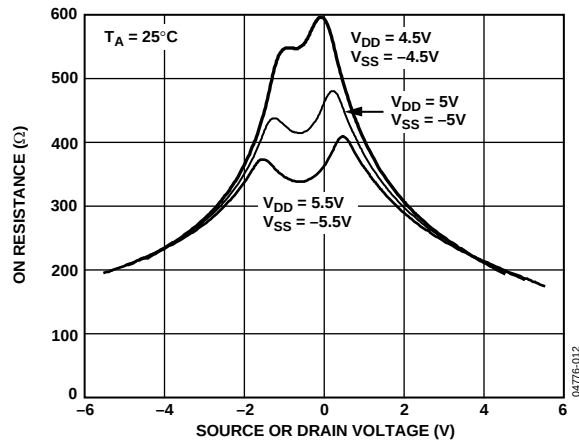
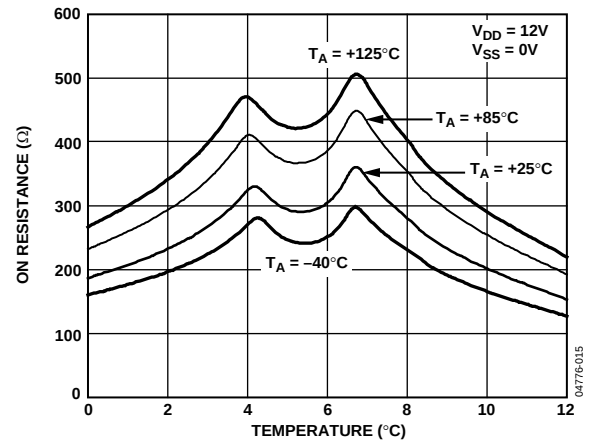
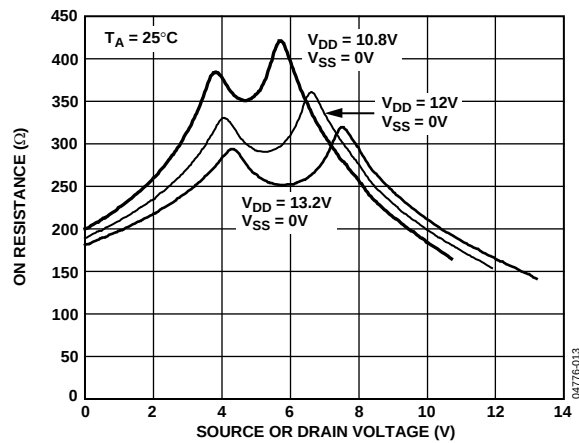
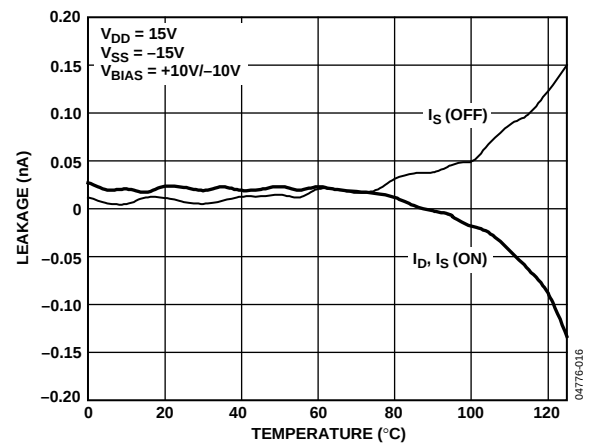
Figure 4. On Resistance as a Function of  $V_D$  ( $V_S$ ) for Dual SupplyFigure 7. On Resistance as a Function of  $V_D$  ( $V_S$ ) for Different Temperatures, Dual SupplyFigure 5. On Resistance as a Function of  $V_D$  ( $V_S$ ) for Dual SupplyFigure 8. On Resistance as a Function of  $V_D$  ( $V_S$ ) for Different Temperatures, Single SupplyFigure 6. On Resistance as a Function of  $V_D$  ( $V_S$ ) for Single Supply

Figure 9. Leakage Currents as a Function of Temperature, Dual Supply

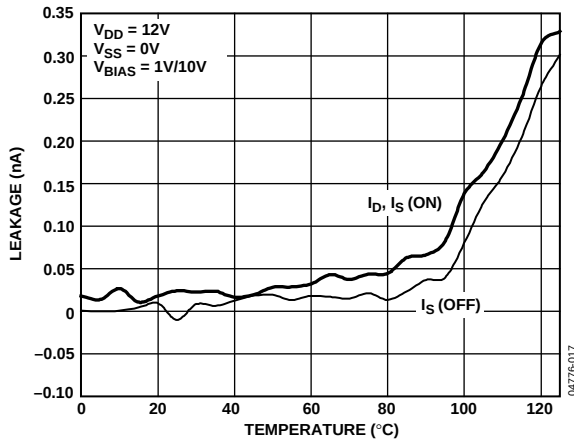


Figure 10. Leakage Currents as a Function of Temperature, Single Supply

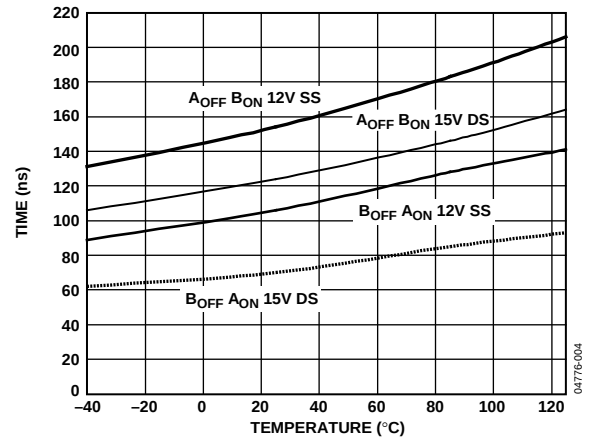
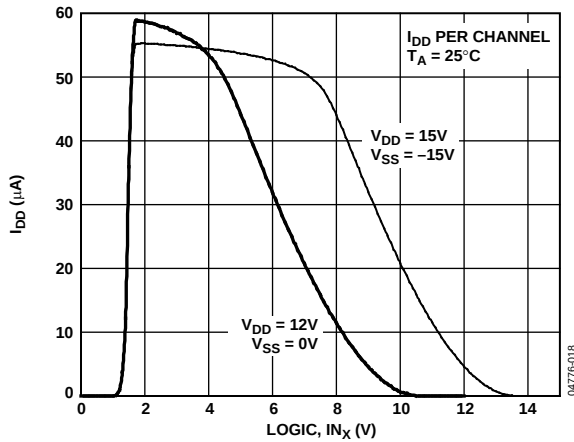
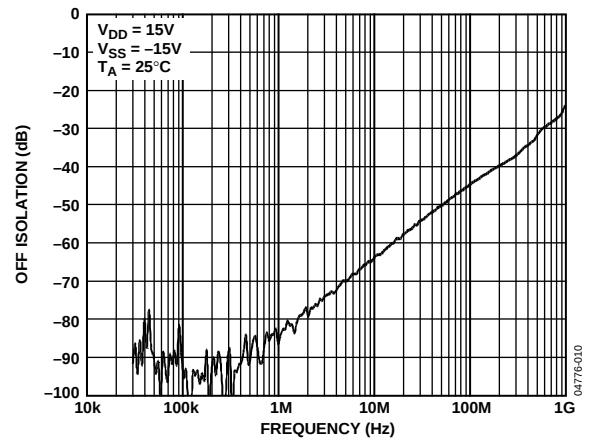
Figure 13.  $t_{\text{TRANSITION}}$  Times vs. TemperatureFigure 11.  $I_{\text{DD}}$  vs. Logic Level

Figure 14. Off Isolation vs. Frequency

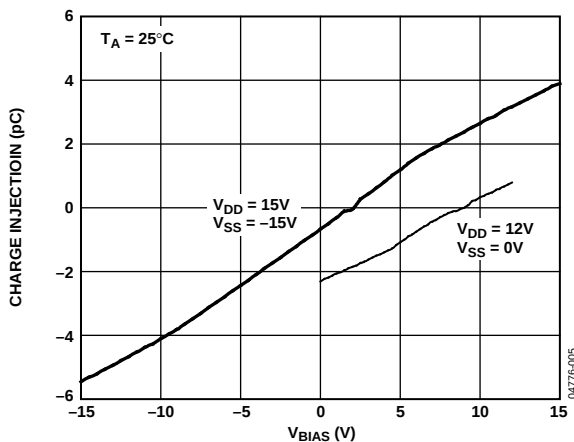


Figure 12. Charge Injection vs. Source Voltage

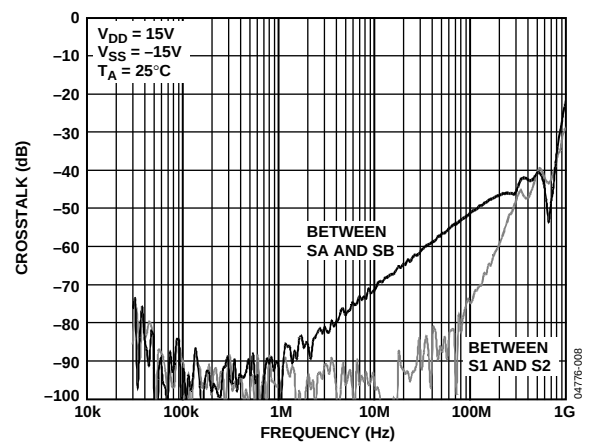


Figure 15. Crosstalk vs. Frequency

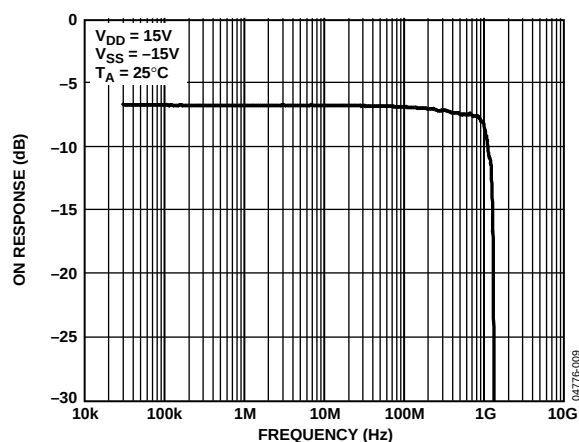


Figure 16. On Response vs. Frequency

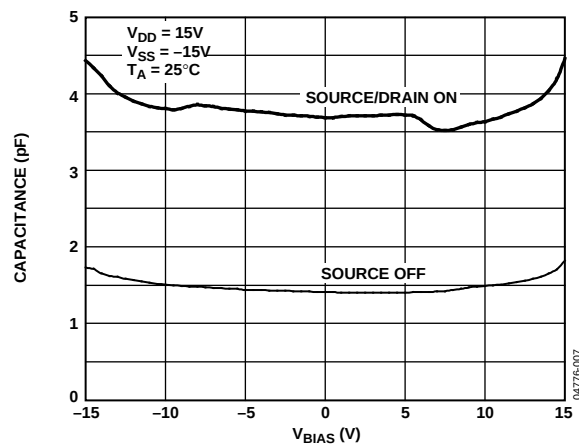


Figure 18. Capacitance vs. Source Voltage for Dual Supply

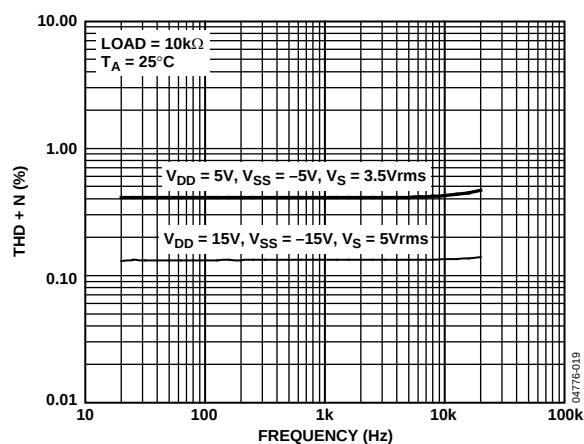


Figure 17. THD + N vs. Frequency

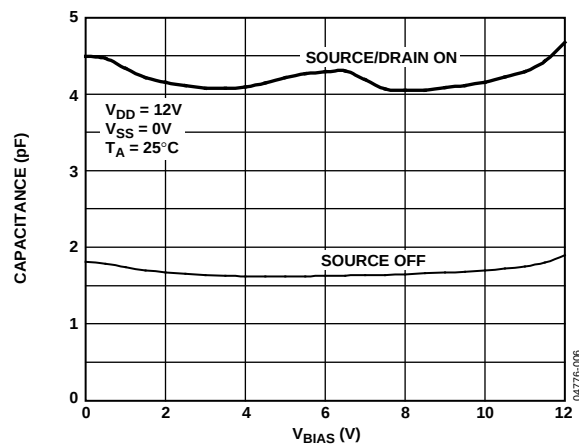


Figure 19. Capacitance vs. Source Voltage for Single Supply

## TEST CIRCUITS

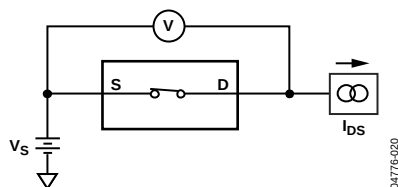


Figure 20. Test Circuit 1—On Resistance

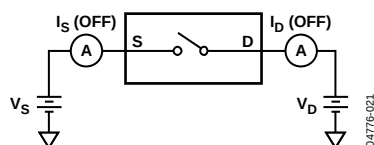


Figure 21. Test Circuit 2—Off Resistance

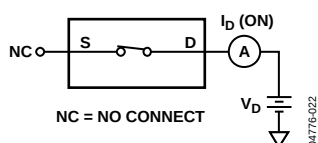


Figure 22. Test Circuit 3—On Leakage

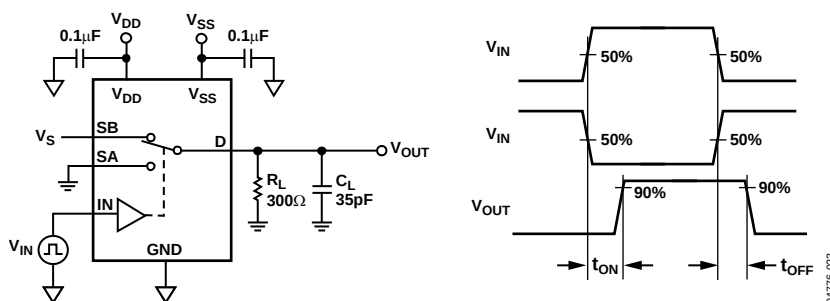


Figure 23. Test Circuit 4—Switching Times

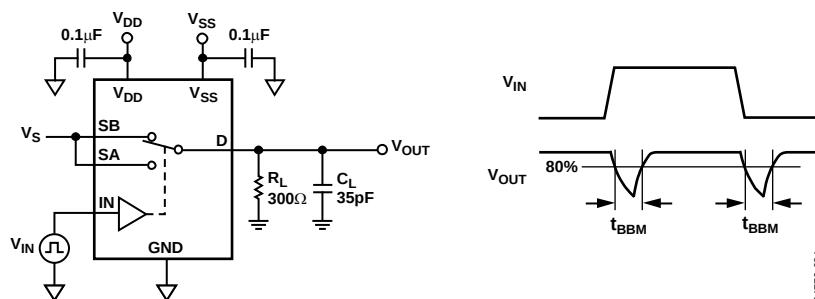


Figure 24. Test Circuit 5—Break-Before-Make Time Delay

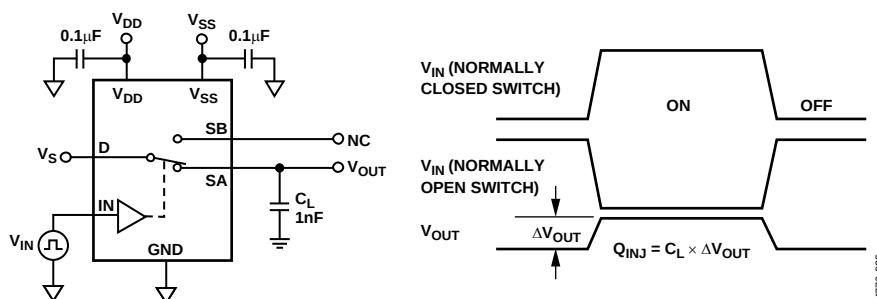


Figure 25. Test Circuit 6—Charge Injection

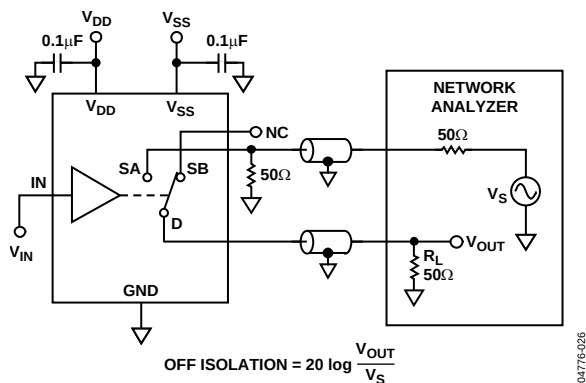


Figure 26. Test Circuit 7—Off Isolation

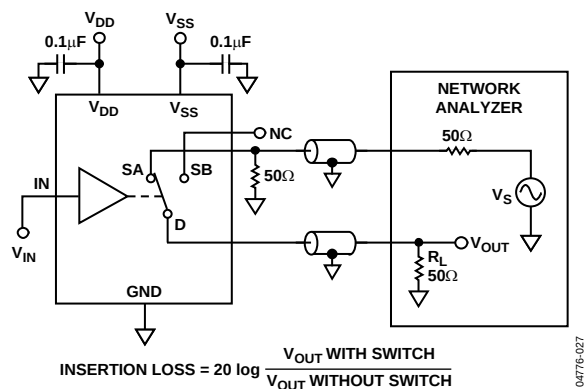


Figure 27. Test Circuit 8—Channel-to-Channel Crosstalk

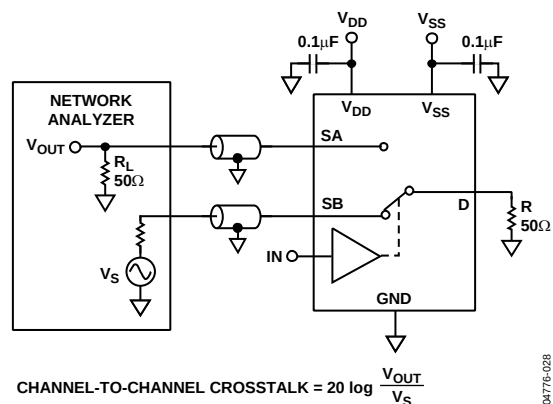


Figure 28. Test Circuit 9—Bandwidth

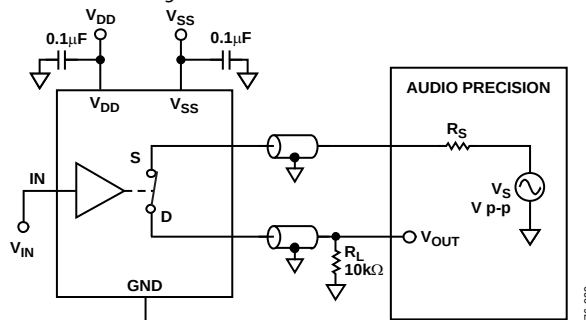


Figure 29. Test Circuit 10—THD + Noise

## OUTLINE DIMENSIONS

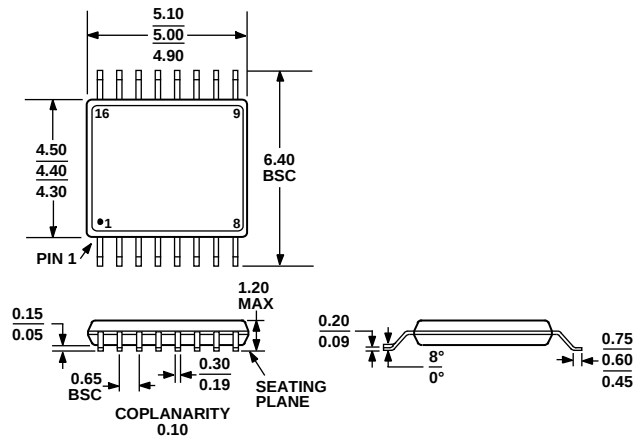


Figure 30. 16-Lead Thin Shrink Small Outline Package [TSSOP]  
(RU-16)

Dimensions shown in millimeters

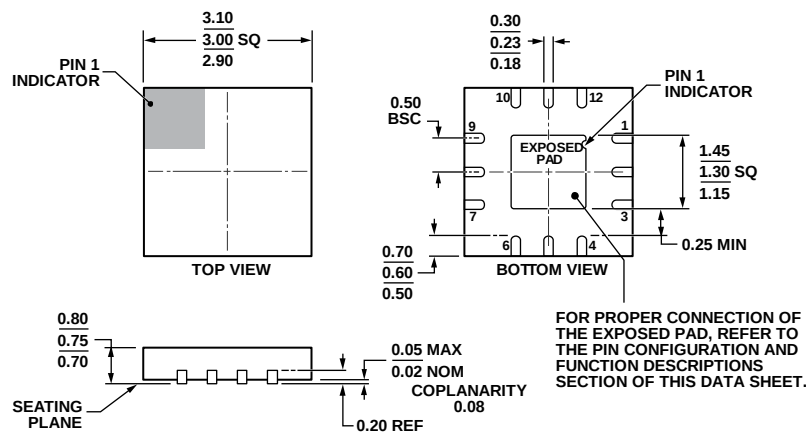


Figure 31. 12-Lead Lead Frame Chip Scale Package [LFCSP]  
3 mm × 3 mm Body and 0.75 mm Package Height  
(CP-12-4)

Dimensions shown in millimeters

## ORDERING GUIDE

| Model <sup>1</sup> | Temperature Range | Package Description                               | Package Option |
|--------------------|-------------------|---------------------------------------------------|----------------|
| ADG1236YRUZ        | −40°C to +125°C   | 16-Lead Thin Shrink Small Outline Package [TSSOP] | RU-16          |
| ADG1236YRUZ-REEL   | −40°C to +125°C   | 16-Lead Thin Shrink Small Outline Package [TSSOP] | RU-16          |
| ADG1236YRUZ-REEL7  | −40°C to +125°C   | 16-Lead Thin Shrink Small Outline Package [TSSOP] | RU-16          |
| ADG1236YCPZ-500RL7 | −40°C to +125°C   | 12-Lead Lead Frame Chip Scale Package [LFCSP]     | CP-12-4        |
| ADG1236YCPZ-REEL7  | −40°C to +125°C   | 12-Lead Lead Frame Chip Scale Package [LFCSP]     | CP-12-4        |

<sup>1</sup> Z = RoHS Compliant Part.

## NOTES

**NOTES**