

Ordering Information (Note 4)

Logic De 74 : Logic LVC : 1.65 to Logic 1G : One o	Prefix 5 5.5 V Family	<u>74 LVC1</u> Function 32 : 2-Input OR Gate	G 32 XXX -7 Package W5 : SOT25 SE : SOT353 Z : SOT553 FS3 : X2-DFN080 FW5 : X1-DFN10 FW4 : X2-DFN10 FX4 : X2- DFN14 FZ4 : X2- DFN14	08-4 10-6 (Type B) 10-6 09-6	Packing Tape & Reel
Part Number	Package Code	Package (Notes 5 & 6)	Package Size	7" Tape Quantity	e and Reel Part Number Suffix
74LVC1G32W5-7	W5	SOT25	3.0mm x 2.8mm x 1.2mm 0.95 mm lead pitch	3000/Tape & Reel	-7
74LVC1G32SE-7	SE	SOT353	2.0mm x 2.0mm x 1.1mm 0.65 mm lead pitch	3000/Tape & Reel	-7
74LVC1G32Z-7	Z	SOT553	1.6mm x 1.6 mm x 0.62mm 0.5 mm lead pitch	4000/Tape & Reel	-7
74LVC1G32FS3-7	FS3	X2-DFN0808-4	0.8mm x 0.8mm x 0.35mm 0.5 mm pad pitch (diamond)	5000/Tape & Reel	-7
74LVC1G32FW5-7	FW5	X1-DFN1010-6 (Type B)	1.0mm x 1.0mm x 0.5mm 0.35 mm pad pitch	5000/Tape & Reel	-7
74LVC1G32FW4-7	FW4	X2-DFN1010-6	1.0mm x 1.0mm x 0.4mm 0.35 mm pad pitch	5000/Tape & Reel	-7
74LVC1G32FX4-7	FX4	X2-DFN1409-6 (Chip scale alternative)	1.4mm x 0.9mm x 0.4mm 0.5 mm pad pitch	5000/Tape & Reel	-7
74LVC1G32FZ4-7	FZ4	X2-DFN1410-6	1.4mm x 1.0mm x 0.4mm 0.5 mm pad pitch	5000/Tape & Reel	-7

Notes:

For packaging details, go to our website at http://www.diodes.com/products/packages.html.
 Pad layout, as shown in Diodes Incorporated suggested pad layouts, can be found at http://www.diodes.com/package-outlines.html.
 The taping orientation is located on our website at https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf

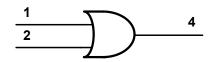
Pin Descriptions

Pin Name	Description	
A	Data Input	
В	Data Input	
GND	Ground	
Y	Data Output	
Vcc	Supply Voltage	
NC	No Connection	

Function Table

In	Output	
Α	В	Y
Н	Х	Н
Х	Н	Н
L	L	L

Logic Diagram



Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to 6.5	V
VI	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High Impedance or IOFF State	-0.5 to 6.5	V
Vo	Voltage Applied to Output in High or Low State.	-0.5 to V _{CC} +0.5	V
I _{IK}	Input Clamp Current V _I < 0	-50	mA
loκ	Output Clamp Current	-50	mA
Ιo	Continuous Output Current	±50	mA
I _{CC,} I _{GND}	Continuous Current Through V _{CC} or GND	±100	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Absolute Maximum Ratings (Notes 7 & 8) (@T_A = +25°C, unless otherwise specified.)

Notes: 7. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

8. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

Recommended Operating Conditions (Note 9) (@T_A = +25°C, unless otherwise specified.)

Symbol		Parameter	Min	Max	Unit	
N/		Operating	1.65	5.5	V	
Vcc	Operating Voltage	Data retention only	1.5	—	V	
		V _{CC} = 1.65V to 1.95V	0.65 x V _{CC}	—		
		V _{CC} = 2.3V to 2.7V	1.7	—	v	
VIH	High-Level Input Voltage	V _{CC} = 3V to 3.6V	2	—	v	
		V_{CC} = 4.5V to 5.5V	0.7 x V _{CC}	—		
		V _{CC} = 1.65V to 1.95V	_	0.35 x V _{CC}		
N/		V _{CC} = 2.3V to 2.7V	_	0.7	v	
VIL	Low-Level Input voltage	V _{CC} = 3V to 3.6V	_	0.8	v	
		V _{CC} = 4.5V to 5.5V	_	0.3 x V _{CC}		
VI	Input Voltage		0	5.5	V	
Vo	Output Voltage		0	Vcc	V	
		V _{CC} = 1.65V	_	-4		
		V _{CC} = 2.3V	_	-8		
Let i	High Lovel Output Current	V _{CC} = 2.7V	_	-12	mA	
lон	High-Level Output Current	V _{CC} = 3V	_	-16		
			—	-24		
		V _{CC} = 4.5V	—	-32		
		V _{CC} = 1.65V	—	4		
		$V_{CC} = 2.3V$	—	8		
I _{OL}	Low-Level Output Current	V _{CC} = 2.7V	—	12	mA	
IOL		V _{CC} = 3V	—	16		
		VCC - 3V	—	24]	
		$V_{CC} = 4.5V$	—	32		
		V_{CC} = 1.8V ± 0.15V, 2.5V ± 0.2V		20		
Δt/ΔV	Input transition Rise or Fall Rate	$V_{CC} = 3.3V \pm 0.3V$		10	ns/V	
		$V_{CC} = 5V \pm 0.5V$		5		
T _A	Operating Free-Air Temperature	—	-40	+125	°C	

Note: 9. Unused inputs should be held at V_{CC} or Ground.



Electrical Characteristics (All typical values are at V_{CC} = 3.3V, T_A = +25°C)

Symphol	Parameter	Test Conditions V _{CC}		-4	0°C to +85°	°C	-40°C to	+125°C	Unit
Symbol	Parameter	lest Conditions	Vcc	Min	Тур	Max	Min	Max	Unit
		I _{OH} = -100μA	1.65V to 5.5V	$V_{CC} - 0.1$	_	_	V _{CC} – 0.1	_	
		I _{OH} = -4mA	1.65V	1.2	_	_	0.95	_	
		I _{OH} = -8mA	2.3V	1.9	_	—	1.7	_	
Vон	High-Level Output Voltage	I _{OL} = -12mA	2.7V	2.2	_	—	1.9	_	V
	output voltage	I _{OH} = -16mA	3V	2.4	_	—	2.2	_	
		I _{OH} = -24mA	3V	2.3	_	_	2.0	_	
		I _{OH} = -32mA	4.5V	3.8	_	_	3.4	_	
		I _{OL} = 100μA	1.65V to 5.5V	—	_	0.1	—	0.1	
		I _{OL} = 4mA	1.65V	_	_	0.45	—	0.7	v
		I _{OL} = 8mA	2.3V	_	_	0.3	—	0.45	
V _{OL}	Low-Level Output Voltage	I _{OL} = 12mA	2.7V	_	_	0.4	_	0.6	
	output voltage	I _{OL} = 16mA	3V	_	_	0.4	—	0.6	
		I _{OL} = 24mA	3V	_	_	0.55	—	0.8	
		I _{OL} = 32mA	4.5V	_	_	0.55	—	.8	
lı	Input Current	V _I = 5.5 V or GND	0 to 5.5V	_	± 0.1	±5	—	± 100	μA
IOFF	Power Down Leakage Current	$V_{\rm I}$ or $V_{\rm O}$ = 5.5V	0V	_	_	±10	_	±200	μΑ
Icc	Supply Current	V ₁ = 5.5V or GND I _O =0	5.5V	_	0.1	10	_	200	μA
ΔI _{CC}	Additional Supply Current	One input at V_{CC} –0.6V Other inputs at V_{CC} or GND	3V to 5.5V	_	_	500	_	5000	μΑ
Ci	Input Capacitance	$V_i = V_{CC} - \text{ or } GND$	3.3V	_	5	_	—	_	pF

Package Characteristics (All typical values are at V_{CC} = 3.3V, T_A = +25°C)

Symbol	Parameter	Test Conditions	Vcc	Min	Тур.	Max	Unit
		SOT25		—	204	_	
		SOT353		_	371	_	
		SOT553		—	231	_	
0	Thermal Resistance	X2-DFN0808-4	(Note 10)		400	_	°CM/
θ _{JA}	Junction-to-Ambient	X1-DFN1010-6 (Type B)	(Note 10)		435	_	°C/W
		X2-DFN1010-6		_	445	_	
		X2-DFN1409-6		_	470	_	
		X2-DFN1410-6		_	460	_	
		SOT25		—	52	_	
		SOT353		—	143	_	
		SOT553		—	105	_	
0	Thermal Resistance	X2-DFN0808-4	(Note 10)	—	225	_	°C/W
θ _{JC}	Junction-to-Case	X1-DFN1010-6 (Type B)	(Note 10)	—	250	_	C/vv
		X2-DFN1010-6		—	250	_	
		X2-DFN1409-6		—	275	_	
		X2-DFN1410-6		—	265	_	

Note: 10. Test condition for each of the 8 package types: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.



Switching Characteristics

Parameter	From	То	Vcc	TA	= -40°C to +8	5°C	T _A = -40°C	to +125°C	Unit
Falameter	Input	Output	VCC	Min	Тур.	Max	Min	Max	Unit
			1.8V ± 0.15V	1.0	3.1	8.0	1.0	10.5	
			2.5V ± 0.2V	0.5	2.1	5.5	0.5	7.0	
t _{pd}	A or B	Y	2.7V	0.5	2.5	5.5	0.5	7.0	ns
			3.3V ± 0.3V	0.5	2.1	4.5	0.5	6.0	
			5.0V ± 0.5V	0.5	1.7	4.0	0.5	5.5	

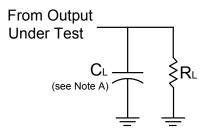
Figure 1 Typical Values at T_A = +25°C and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V.

Operating Characteristics

T _A = +25°C							
Parameter		Test Conditions	V _{CC} = 1.8V	V _{CC} = 2.5V	V _{CC} = 3.3V	$V_{CC} = 5V$	Unit
	Faranieter		Тур	Тур	Тур	Тур	Onit
C _{pd}	Power Dissipation Capacitance	f = 10MHz	20	20	21	22	pF



Parameter Measurement Information



V	In	puts	N	0	P
V _{cc}	VI	t _r /t _f	V _M	CL	RL
1.8V ± 0.15V	Vcc	≤2ns	V _{CC} /2	30pF	1kΩ
2.5V ± 0.2V	Vcc	≤2ns	V _{CC} /2	30pF	500Ω
2.7V	Vcc	≤2.5ns	1.5V	50pF	500Ω
3.3V ± 0.3V	3.0V	≤2.5ns	1.5V	50pF	500Ω
5.0V ± 0.5V	V _{CC}	≤2.5ns	V _{CC} /2	50pF	500Ω



Voltage Waveform Pulse Duration

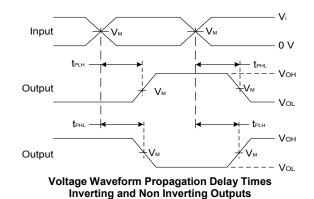


Figure 1 Load Circuit and Voltage Waveforms

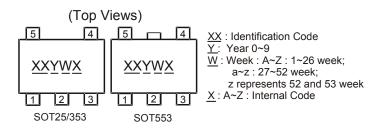
Notes:

- A. Includes test lead and test apparatus capacitance.
 B. All pulses are supplied at pulse repetition rate ≤ 10MHz.
 C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as $t_{\text{PD}}.$



Marking Information

(1) SOT25, SOT353 and SOT553



Part Number	Package	Identification Code
74LVC1G32W5-7	SOT25	UW
74LVC1G32SE-7	SOT353	UW
74LVC1G32Z-7	SOT553	UW

(2) DFN Packages

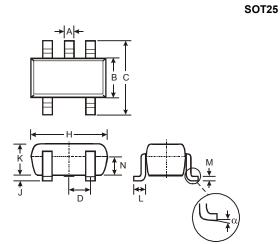


 $\begin{array}{l} \underline{XX}: \mbox{ Identification Code} \\ \underline{Y}: \mbox{ Year 0~9} \\ \underline{W}: \mbox{ Week : } A~Z: 1~26 week; \\ a~z: 27~52 week; \\ z \mbox{ represents 52 and 53 week} \\ \underline{X}: \mbox{ A~Z}: \mbox{ Internal Code} \end{array}$

Part Number	Package	Identification Code
74LVC1G32FS3-7	X2-DFN0808-4	WW
74LVC1G32FW5-7	X1-DFN1010-6 (Type B)	VP
74LVC1G32FW4-7	X2-DFN1010-6	UW
74LVC1G32FX4-7	X2-DFN1409-6	MJ
74LVC1G32FZ4-7	X2-DFN1410-6	UW



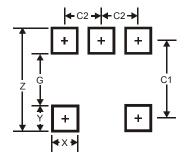
Please see http://www.diodes.com/package-outlines.html for the latest version



	SOT25			
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
С	2.70	3.00	2.80	
D	-	-	0.95	
н	2.90	3.10	3.00	
J	0.013	0.10	0.05	
κ	1.00	1.30	1.10	
L	0.35	0.55	0.40	
Μ	0.10	0.20	0.15	
Ν	0.70	0.80	0.75	
α	0°	8°	-	
All D	All Dimensions in mm			

Suggested Pad Layout

SOT25

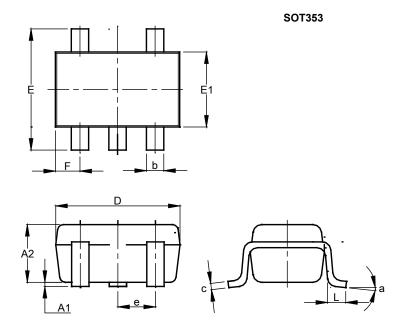


Dimensions	Value
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95

SOT25 Package Information Rev. 2017-04-11

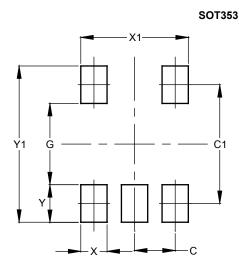


Please see http://www.diodes.com/package-outlines.html for the latest version



	SOT353				
Dim	Min	Max	Тур		
A1	0.00	0.10	0.05		
A2	0.90	1.00	0.95		
b	0.10	0.30	0.25		
С	0.10	0.22	0.11		
D	1.80	2.20	2.15		
Е	2.00	2.20	2.10		
E1	1.15	1.35	1.30		
е	C).650 B	SC		
F	0.40	0.45	0.425		
L	0.25	0.40	0.30		
а	0°	8°			
All	All Dimensions in mm				

Suggested Pad Layout

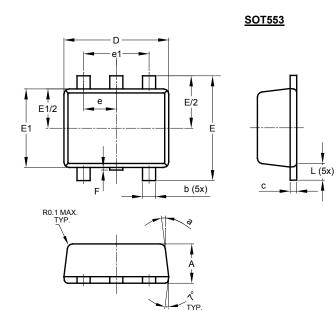


Dimensions	Value (in mm)
С	0.650
C1	1.900
G	1.300
Х	0.420
X1	1.720
Y	0.600
Y1	2.500

SOT353 Package Information Rev. 2018-01-16

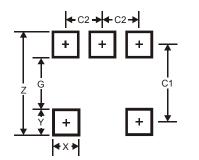


Please see http://www.diodes.com/package-outlines.html for the latest version



	SOT553			
Dim	Min	Max	Тур	
Α	0.55	0.62	0.60	
b	0.15	0.30	0.20	
С	0.10	0.18	0.15	
D	1.50	1.70	1.60	
E	1.55	1.70	1.60	
E1	1.10	1.25	1.20	
е	0.	50 BS(0	
e1	1.0	00 BS(0	
F	0.00	0.10		
L	0.10	0.30	0.20	
а	6°	8°	7°	
	All Dimensions in mm			

Suggested Pad Layout



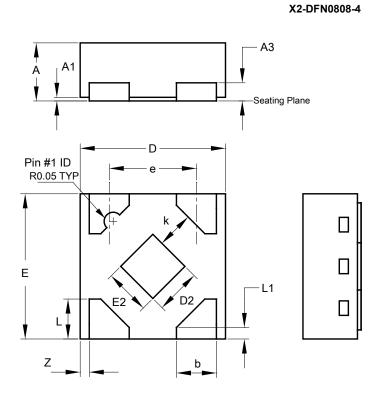
SOT553

Dimensions	Value
Z	2.2
G	1.2
Х	0.375
Y	0.5
C1	1.7
C2	0.5

SOT553 Package Information Rev. 2015-06-11



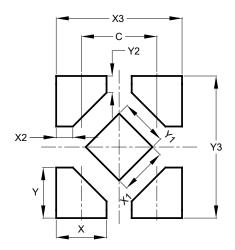
Please see http://www.diodes.com/package-outlines.html for the latest version.



	X2-DFN0808-4				
Dim	Min	Max	Тур		
Α	0.25	0.35	0.30		
A1	0	0.04	0.02		
A3	-	-	0.13		
b	0.17	0.27	0.22		
D	0.75	0.85	0.80		
D2	0.15	0.35	0.25		
Е	0.75	0.85	0.80		
E2	0.15	0.35	0.25		
е	-	-	0.48		
k	0.20	-	-		
L	0.17	0.27	0.22		
L1	0.02	0.12	0.07		
z	-	-	0.05		
A	All Dimensions in mm				

Suggested Pad Layout

X2-DFN0808-4

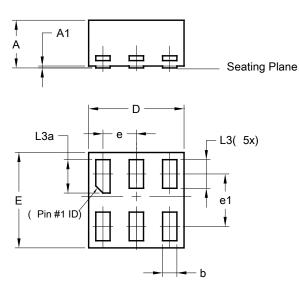


X2-DFN0808-4 Package Information Rev. 2015-06-05

Dimensions	Value
С	0.480
х	0.320
X1	0.300
X2	0.106
X3	0.800
Y	0.320
Y1	0.300
Y2	0.106
Y3	0.900



Please see http://www.diodes.com/package-outlines.html for the latest version.

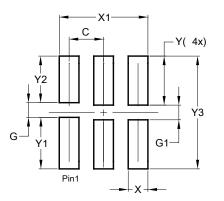


	X1-DFN1010-6 (Type B)			
Dim	Min	Max	Тур	
Α	-	0.50	0.39	
A1	1	0.04	-	
b	0.12	0.20	0.15	
D	0.95	1.050	1.00	
Е	0.95	1.050	1.00	
е		0.35 B	SC	
e1		0.55 B	SC	
L3	0.27	0.30	0.30	
L3a	0.32	0.40	0.35	
All	All Dimensions in mm			

Suggested Pad Layout

X1-DFN1010-6 (Type B)

X1-DFN1010-6 (Type B)



Dimensions	Value
Dimensions	(in mm)
С	0.350
G	0.150
G1	0.150
Х	0.200
X1	0.900
Y	0.500
Y1	0.525
Y2	0.475
Y3	1.150

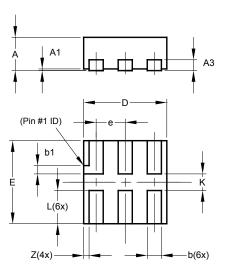
X1-DFN1010-6 (Type B) Package Information Rev. 2015-06-05



Please see http://www.diodes.com/package-outlines.html for the latest version.

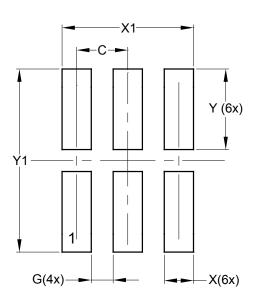
X2-DFN1010-6

X2-DFN1010-6



	X2-DFN1010-6			
Dim	Min	Max	Тур	
Α	_	0.40	0.39	
A1	0.00	0.05	0.02	
A3			0.13	
b	0.14	0.20	0.17	
b1	0.05	0.15	0.10	
D	0.95	1.05	1.00	
E	0.95	1.05	1.00	
е		_	0.35	
L	0.35	0.45	0.40	
K	0.15		-	
Z		_	0.065	
All	All Dimensions in mm			

Suggested Pad Layout



 Dimensions
 Value (in mm)

 C
 0.350

 G
 0.150

 X
 0.200

 X1
 0.900

 Y
 0.550

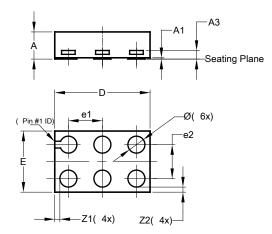
 Y1
 1.250

X2-DFN1010-6 Package Information Rev. 2018-07-17



Please see http://www.diodes.com/package-outlines.html for the latest version.

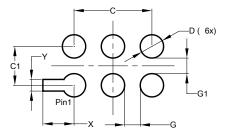
X2-DFN1409-6 CHIP SCALE ALTERNATIVE



X2-DFN1409-6			
Dim	Min	Max	Тур
Α	-	0.40	0.39
A1	0	0.05	0.02
A3	-	-	0.13
Ø	0.20	0.30	0.25
D	1.35	1.45	1.40
E	0.85	0.95	0.90
e1	-	-	0.50
e2	-	-	0.50
Z1	-	-	0.075
Z2	-	-	0.075
All Dimensions in mm			

Suggested Pad Layout

X2-DFN1409-6 CHIP SCALE ALTERNATIVE

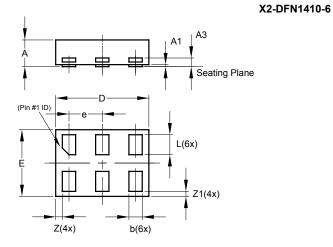


Dimensions	Value	
Dimensions	(in mm)	
С	1.000	
C1	0.500	
D	0.300	
G	0.200	
G1	0.200	
Х	0.400	
Y	0.150	

X2-DFN1409-6 Package Information Rev. 2018-07-17

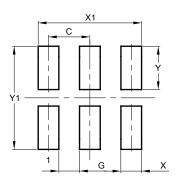


Please see http://www.diodes.com/package-outlines.html for the latest version.



X2-DFN1410-6				
Dim	Min	Max	Тур	
Α		0.40	0.39	
A1	0.00	0.05	0.02	
A3			0.13	
b	0.15	0.25	0.20	
D	1.35	1.45	1.40	
ш	0.95	1.05	1.00	
e			0.50	
L	0.25	0.35	0.30	
Z	_		0.10	
Z1	0.045	0.105	0.075	
All Dimensions in mm				

Suggested Pad Layout



X2-DFN1410-6	

Dimensions	Value (in mm)
C	0.500
G	0.250
Х	0.250
X1	1.250
Y	0.525
Y1	1.250

X2-DFN1410-6 Package Information Rev. 2015-06-08



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.

Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2020, Diodes Incorporated

www.diodes.com